

A Low-Power-Instruction-Queue Wakeup Mechanism

Authors: [Ramírez, Marco](#) / [Cristal, Adrián](#) / [Veidenbaum, Alexander](#) / [Villa, Luis](#) / [Valero, Mateo](#)

Publication: XIV Jornadas de Paralelismo

Place Published: Leganés, Spain

Pagination: 533?540

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 15 jul 2024 - 00:20): <https://www.bsc.es/ca/research-and-development/publications/low-power-instruction-queue-wakeup-mechanism>