

## [Kilo-instruction Processors](#)

**Authors:** [Cristal, Adrián](#) / [Ortega, Daniel](#) / [Llosa, Josep](#) / [Valero, Mateo](#)

**Publication:** 5th International Symposium on High Performance Computing (ISHPC-V)

**Place Published:** Tokio, Japan

**Pagination:** 10?25

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

---

**Source URL (retrieved on 20 des 2024 - 23:30):** <https://www.bsc.es/ca/research-and-development/publications/kilo-instruction-processors>