

Joint Circuit-System Design Space Exploration of Multiplier Unit Structure for Energy-Efficient Vector Processors

URL: <http://ieeexplore.ieee.org/xpl/articleDetails.jsp?reload=true&arnumber=7308672>

Authors: [Ratkovi?, Ivan](#) / [Palomar, Oscar](#) / [Stanic, Milan](#) / [Duric, Milovan](#) / [Peic, Djordje](#) / [Unsal, Osman](#) / [Cristal, Adrián](#) / [Valero, Mateo](#)

Publication: 2015 IEEE Computer Society Annual Symposium on VLSI

Place Published: Montpellier, France

Pagination: 19-26

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 2 abr 2025 - 11:39): <https://www.bsc.es/ca/research-and-development/publications/joint-circuit-system-design-space-exploration-multiplier-unit>