

INSTRUCTION WAKEUP MECHANISM: Power and Timing Evaluation

Authors: [Ramírez, Marco](#) / [Cristal, Adrián](#) / [Villa, Luis](#) / [Veidenbaum, Alexander](#) / [Valero, Mateo](#)

Publication: CIC,s Research and Computing Science

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 9 oct 2024 - 14:15): <https://www.bsc.es/ca/research-and-development/publications/instruction-wakeup-mechanism-power-and-timing-evaluation>