

Inici > Hybrid Transactional Memory to accelerate safe lock-based transactions

## Hybrid Transactional Memory to accelerate safe lock-based transactions

URL: http://capinfo.e.ac.upc.edu/PDFs/dir16/file003699.pdf

Authors: Vallejo, Enrique / Harris, Tim / Cristal, Adrián / Unsal, Osman / Valero, Mateo

**Publication:** 3rd ACM SIGPLAN Workshop on Transactional Computing (TRANSACT 2008)

Place Published: Salt Lake City, UT, United States

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

**Source URL** (**retrieved on** *10 febr 2025 - 08:20*): <a href="https://www.bsc.es/ca/research-and-development/publications/hybrid-transactional-memory-accelerate-safe-lock-based">https://www.bsc.es/ca/research-and-development/publications/hybrid-transactional-memory-accelerate-safe-lock-based</a>