

Inici > hdSMT: An Heterogeneity-Aware Simultaneous Multithreaded Architecture

hdSMT: An Heterogeneity-Aware Simultaneous Multithreaded Architecture

Authors: Acosta, Carmelo / Falcón, Ayose / Ramirez, Alex / Valero, Mateo

Publication: XVI Jornadas de Paralelismo

Pagination: 59-66

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (**retrieved on 2** *abr* **2025 - 11:36**): https://www.bsc.es/ca/research-and-development/publications/hdsmt-heterogeneity-aware-simultaneous-multithreaded