

## [Exploring Multiple Sleep Modes in On/Off based Energy Efficient HPC Networks](#)

**URL:**

<http://ieeexplore.ieee.org/xpl/login.jsp?tp=&arnumber=7357084&url=http%3A%2F%2Fieeexplore.ieee.org%2Fexpl>

**Authors:** [Saravanan, Karthikeyan](#) / [Carpenter, Paul](#) / [Ramirez, Alex](#)

**Research Lines:** [Microserver architectures and system software](#)

**Publication:** 33rd IEEE International Conference on Computer Design (ICCD)

**Pagination:** 54-61

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

---

**Source URL (retrieved on 27 set 2024 - 00:12):** <https://www.bsc.es/ca/research-and-development/publications/exploring-multiple-sleep-modes-onoff-based-energy-efficient>