

[Enabling a Reliable STT-MRAM Main Memory Simulation](#)

URL: <https://dl.acm.org/citation.cfm?id=3132402.3132416>

Authors: [Asifuzzaman, Kazi](#) / [Verdejo, Rommel](#) / [Radojkovic, Petar](#)

Research Lines: [Memory systems for HPC and AI](#)

Publication: Proceedings of the Third International Symposium on Memory Systems (MEMSYS)

Pagination: 283-292

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 10 Mar 2025 - 06:51): <https://www.bsc.es/ca/research-and-development/publications/enabling-reliable-stt-mram-main-memory-simulation>