

Inici > Core to Memory Interconnection Implications for Forthcoming On-Chip Multiprocessors

Core to Memory Interconnection Implications for Forthcoming On-Chip Multiprocessors

Authors: Acosta, Carmelo / Cazorla, Francisco / Ramirez, Alex / Valero, Mateo

Publication: 1st Workshop on Chip Multiprocessor Memory Systems and Interconnects (CMP-MSI 2007)

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 15 Mar 2025 - 09:03): <u>https://www.bsc.es/ca/research-and-</u>development/publications/core-memory-interconnection-implications-forthcoming-chip