

[Architectural Support for Fair Reader-Writer Locking](#)

Authors: [Vallejo, Enrique](#) / [Beivide, Ramón](#) / [Cristal, Adrián](#) / [Harris, Tim](#) / [Vallejo, Fernando](#) / [Unsal, Osman](#) / [Valero, Mateo](#)

Publication: International Symposium on Microarchitecture

Place Published: Atlanta, United States

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 5 febr 2025 - 05:12): <https://www.bsc.es/ca/research-and-development/publications/architectural-support-fair-reader-writer-locking>