

[Adaptive Runtime-Assisted Block Prefetching on Chip-Multiprocessors](#)

URL: <http://dx.doi.org/10.1007/s10766-016-0431-8>

Authors: [Garcia, Victor](#) / [Rico, Alejandro](#) / [Villavieja, Carlos](#) / [Carpenter, Paul](#) / [Navarro, Nacho](#) / [Ramirez, Alex](#)

Publication: International Journal of Parallel Programming

Pagination: 1?21

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 15 Mar 2025 - 10:00): <https://www.bsc.es/ca/research-and-development/publications/adaptive-runtime-assisted-block-prefetching-chip>