

<u>Inici</u> > Achieving High Memory Performance from Heterogeneous Architectures with the SARC Programming Model

## Achieving High Memory Performance from Heterogeneous Architectures with the SARC Programming Model

URL: http://portal.acm.org/citation.cfm?id=1621963

Authors: Ferrer, Roger / Beltran, Vicenç / González, Marc / Martorell, Xavier / Ayguade, Eduard

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on *13 des 2024 - 13:08*): <u>https://www.bsc.es/ca/research-and-</u> development/publications/achieving-high-memory-performance-heterogeneous-architectures