

MAPC. Memory Access Pattern based Controller

Authors: [Hussain, Tassadaq](#) / [Palomar, Oscar](#) / [Unsal, Osman](#) / [Cristal, Adrián](#) / [Aiguade, Eduard](#) / [Valero, Mateo](#)

Teams: [Computer Architecture For Parallel Paradigms](#) / [Resilience In Distributed Systems](#)

Publication: 24th International Conference on Field Programmable Logic and Applications (FPL), 2014

Place Published: Munich, Germany

Pagination: 1?4

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 25 nov 2024 - 00:53): <https://www.bsc.es/ca/node/40878>