

Inici > MAPC. Memory Access Pattern based Controller

MAPC. Memory Access Pattern based Controller

Authors: <u>Hussain, Tassadaq</u> / <u>Palomar, Oscar</u> / <u>Unsal, Osman</u> / <u>Cristal, Adrián</u> / <u>Ayguade, Eduard</u> / <u>Valero,</u> <u>Mateo</u>

Teams: Computer Architecture For Parallel Paradigms / Resilience In Distributed Systems

Publication: 24th International Conference on Field Programmable Logic and Applications (FPL), 2014

Place Published: Munich, Germany

Pagination: 1?4

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 10 febr 2025 - 08:30): https://www.bsc.es/ca/node/40878