

[Task Sampling: Computer Architecture Simulation in the Many-Core Era](#)

Authors: [Grass, Thomas](#) / [Rico, Alejandro](#) / [Casas, Marc](#) / [Moreto, Miquel](#) / [Ramirez, Alex](#)

Publication: Advanced Computer Architecture and Compilation for High-Performance and Embedded Systems

Place Published: Fiuggi, Italy

Pagination: 165?168

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 12 Mar 2025 - 06:20): <https://www.bsc.es/ca/node/40868>