

## [LiVe: Timely Error Detection in Light-Lockstep Safety Critical Systems](#)

**Authors:** [Hernández, Carles](#) / [Abella, Jaume](#)

**Teams:** [Computer Architecture - Operating Systems \(CAOS\)](#)

**Publication:** 51th Design Automation Conference (DAC) 2014

**Place Published:** San Francisco, United States

**Pagination:** 1?6

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

---

**Source URL (retrieved on 4 febr 2025 - 14:52):** <https://www.bsc.es/ca/node/40845>