

[FPGA-Based Prototype of the Task Superscalar Architecture](#)

URL: <http://hpc.ac.upc.edu/PDFs/dir21/file004313.pdf>

Authors: [Ahmadabadi, Fahimeh Yazdanpanah](#) / [Jiménez, Daniel](#) / [Álvarez, Carlos](#) / [Etsion, Yoav](#) / [Badia, Rosa](#)

Publication: WRC 2013 : 7th HiPEAC Workshop on Reconfigurable Computing

Place Published: Belin, Germany

Pagination: 1?10

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 13 des 2024 - 23:30): <https://www.bsc.es/ca/node/40625>