

## Circuit Design of a Novel Adaptable and Reliable L1 Data Cache

**Authors:** [Seyedi, Azam](#) / [Yalcin, Gulay](#) / [Unsal, Osman](#) / [Cristal, Adrián](#)

**Publication:** 23rd ACM international conference on Great lakes symposium on VLSI

**Place Published:** Paris, France

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

---

**Source URL (retrieved on 26 oct 2024 - 01:55):** <https://www.bsc.es/ca/node/40585>