

[Fault Tolerance for Multi-Threaded Applications by Leveraging Hardware Transactional Memory](#)

Authors: [Yalcin, Gulay](#) / [Unsal, Osman](#) / [Cristal, Adrián](#)

Publication: 10th conference on ACM Computing Frontiers

Place Published: Ischia, Italy

Pagination: 4?4

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

Source URL (retrieved on 22 nov 2024 - 07:01): <https://www.bsc.es/ca/node/40584>