

## [Techniques to Improve Performance in Requester-wins Hardware Transactional Memory](#)

**Authors:** [Armejach, Adrià](#) / [Gil, Rubén Titos](#) / [Negi, Anurag](#) / [Unsal, Osman](#) / [Cristal, Adrián](#)

**Publication:** ACM Transactions on Architecture and Code Optimization

**Volume / Number:** 10 / 42

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

---

**Source URL (retrieved on 5 febr 2025 - 04:53):** <https://www.bsc.es/ca/node/40564>