TOP HPC centres meet in Japan at JLESC

A group of BSC researchers will participate in the 6th JLESC Workshop, that will be held at RIKEN AICS, Kobe, Japan, from November 30th to December 2nd, 2016.

The workshop will gather leading researchers in high-performance computing from the JLESC partners Barcelona Supercomputing Center, INRIA, the University of Illinois, Argonne National Laboratory, Jülich Supercomputing Centre and RIKEN AICS to explore the most recent and critical issues in advancing the field of HPC from petascale to the extreme scale era.

The workshop featured sessions on these seven central topics:

- Applications and mini-apps
- Parallel Programming models and runtime
- Performance tools
- Resilience
- Big Data, I/O and in-situ visualization
- Numerical methods and algorithms
- Advanced architectures

A key objective of the workshop is to identify new research collaborations and establish a roadmap for their implementation.

See below the list of talks that will be given by BSC researchers:

Jesús Labarta	Opening talk: BSC Procurement
Ramon Nou	Project Talk: "Automatic I/O Scheduling algorithm selection for parallel le systems"
Name	**Talk**
Alberto Miranda Ramon Nou	Individual Talk: "NVRAM POSIX-like Filesystem with I/O hints support"
Antonio J. Peña	Project Talk: "Use of the Folding profiler to assist on data distribution for heterogeneous memory systems"
Carlos Alvarez	Individual Talk: "Performance of High level FPGA Accelerators"
Judit Giménez	BOS: "Interfacing Task-based Runtimes with Performance Tools"
Rosa M. Badia Ramon Nou	BOS: "Convergence of Cloud, BigData, and HPC"
Mariano Vázquez	Individual Talk:"Coupled multiphysics and parallel programming"
Leonardo Batista	Project Talk: "New Techniques to Design Silent Data Corruption Detectors"
Luc Jaulmes	Individual Talk: "Runtime driven online estimation of memory vulnerability"
Osman Unsal	Individual Talk: "Fault Tolerance and Approximate Computing"
Osman Unsal	Individual Talk: "Fault-tolerance for FPGAs"
Jesús Labarta	Project Talk: "Enhancing Asynchronous Parallelism in OmpSs with Argobots"
Leonardo Bautista	BOS: "Memory Errors at Extreme Scale"

List of talks by session with abstracts: 6th JLESC Workshop Guidebook (PDF 1,2mb)

About JLESC

Joint Laboratory for Extreme Scale Computing (JLESC) is an international, virtual organization whose goal is to enhance the ability of member organizations and investigators to make the bridge between Petascale and Extreme computing. The founding partners of the JLESC are INRIA and the University of Illinois (UIUC). Further members are Argonne National Laboratory (ANL), Barcelona Supercomputing Center (BSC), Julich Supercomputing Center (JSC) and RIKEN AICS.

JLESC involves computer scientists, engineers and scientists from other disciplines as well as from industry, to ensure that the research facilitated by the Laboratory addresses science and engineering's most critical needs and takes advantage of the continuing evolution of computing technologies.

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

---