BSC taking an active role at SC16

The SC16 will take place in Salt Lake City from 13 to 18 November 2016. Once more BSC will be actively present in the SC16 technical programme.

BSC experts will participate in the conference as follows:

Activity	Day	Room
HPGDMP'16: First International Workshop on High Performance Graph Data Management and Processing	13 November 2016, 9:00-12:00	251-C
Workshop on Latest Advances in Scalable Algorithms for Large-Scale Systems	13 November 2016, 17:30	250-E
Third SC Workshop on Best Practices for HPC Training	14 November 2016, 9:00-12:30	251-B
The Human Sniff: Application of NVIDIA IndeX Advanced Rendering Solution in HPC	15 November 2016, 11:00	Booth #2217 (NVIDIA)
Accelerating Precision Medicine with Intel® Xeon® and Intel® Xeon Phi™ Processors	15 November 2016, 13:15	Booth #1819 (Intel)
European Exascale Projects and Their International Collaboration Potential	16 November 2016, 10:30	155-A
The Mont-Blanc Prototype: An Alternative Approach for HPC Systems	16 November 2016, 11:30-12:00	355-D
MUSA: A Multi-Level Simulation Approach for Next-Generation HPC Machines	16 November 2016, 13:30-14:00	355-E

Activity	Day	Room
Unprotected Computing : A Large-Scale Study of DRAM Raw Error Rate on a Supercomputer	16 November 2016, 16:00-16:30	355-BC
Use Cases of Reconfigurable Computing Architectures for HPC	17 November 2016, 12:15pm	250-C
3rd International Workshop on Visual Performance Analytics – VPA 2016	18 November 2016, 8:30-12:00	355-E
Runtime Systems for Extreme Scale Programming Models and Architectures (RESPA)	18 November 2016, 8:30	155-A

- BSC's paper 'The Mont-Blanc Prototype: An Alternative Approach for HPC Systems' has been selected as a finalist in the best paper awards at the SC16 supercomputing conference. In addition to the Mont-Blanc paper, the SC16 Programme Committee has accepted two other papers submitted by BSC.

- Unprotected Computing: A Large-Scale Study of DRAM Raw Error Rate on a Supercomputer
- MUSA: A Multi-Level Simulation Infrastructure for Next-Generation HPC Machines

Read news Hat-trick of BSC papers accepted for SC16.

- In addition, two BSC projects have been nominated for the 2016 Annual HPC Readers' Choice Awards in two different categories:

Best Use of HPC Application in Life Sciences

- SMUFIN: The **SMUFIN (Somatic MUtations FINder)** method is capable of analysing the complete genome of a tumour and identifying its mutations within a few hours and without the need for a pre-alignment step.

Best Use of HPC Application in the Energy (formerly "Oil and Gas") Industry

- BSIT: **Barcelona Subsurface Imaging Tools** (BSIT) is an extensive set of HPC tools for subsurface imaging, including Modelling, Inversion and Migration of 3D datasets, and is built and ported onto many different HPC architectures, such as Nvidia GPUS, ARM, Intel Xeon and Xeon Phis (KNC & KNL).

Further information here.

- The HPC for Energy project will present a short video on the challenges that our society faces in energy production and consumption and how exascale supercomputing helps to address them. The first showing of the video will take place at the SC16 conference at the Barcelona BSC booth, 15 November 10:30 (local time). The video will be screened throughout the day on SC16 Exhibition.

Save the dates in your calendar!
As in previous editions, BSC will be participating in the SC16 exhibition. During the exhibition, you are welcome to visit our booth (#2511) and we will be happy to answer any questions you might have.