

## [442\\_24\\_CS\\_CAOS\\_RE2](#)

### Job Reference

442\_24\_CS\_CAOS\_RE2

### Position

Processor design engineer (RE2)

### Data de tancament

Dimecres, 31 Juliol, 2024

**Reference:** 442\_24\_CS\_CAOS\_RE2

**Job title:** Processor design engineer (RE2)

### About BSC

The Barcelona Supercomputing Center - Centro Nacional de Supercomputación (BSC-CNS) is the leading supercomputing center in Spain. It houses MareNostrum, one of the most powerful supercomputers in Europe, was a founding and hosting member of the former European HPC infrastructure PRACE (Partnership for Advanced Computing in Europe), and is now hosting entity for EuroHPC JU, the Joint Undertaking that leads large-scale investments and HPC provision in Europe. The mission of BSC is to research, develop and manage information technologies in order to facilitate scientific progress. BSC combines HPC service provision and R&D into both computer and computational science (life, earth and engineering sciences) under one roof, and currently has over 1000 staff from 60 countries.

Look at the BSC experience:

[BSC-CNS YouTube Channel](#)

[Let's stay connected with BSC Folks!](#)

We are particularly interested for this role in the strengths and lived experiences of women and underrepresented groups to help us avoid perpetuating biases and oversights in science and IT research. In instances of equal merit, the incorporation of the under-represented sex will be favoured.

### Context And Mission

The Computer Architecture and Operating System group at the Barcelona Supercomputing Center aims at carrying out research on critical embedded systems in charge of controlling fundamental parts of cars, airplanes and satellites. Our work is mainly done in the context of bilateral projects with several processor companies as well as several European-funded projects. For a complete list of publications of the group in the last years, please visit: [www.bsc.es/caos](http://www.bsc.es/caos)

The objective of this position is to work in the context of some projects enhancing processor designs prototyped in FPGAs as part of a young and dynamic team researching on hardware processor designs for domains with reliability requirements. The engineer is expected to have some experience (either in industry or academia) on hardware prototyping (VHDL, SystemVerilog or Verilog). Candidates with 0-5 years of experience as engineers are wanted.

## Key Duties

- Perform hardware prototyping in simulated environments and FPGA for processors with reliability requirements as part of a team
- Learn and participate in verification and validation activities of the developments performed
- Coordinate with a local team at BSC

## Requirements

- Education
  - Grade or Master degree in Electronics, Circuits, Systems, Computer Sciences, Telecommunications or Automation
- Essential Knowledge and Professional Experience
  - Practical experience designing digital hardware designs (VHDL, SystemVerilog or Verilog)
  - Familiarity with basics on digital processor or microcontroller designs, either as part of his/her studies or work experience
- Additional Knowledge and Professional Experience
  - Knowledge on processor microarchitecture (e.g. Gaisler, Arm, Infineon, Intel, or AMD processors among others) is welcome
  - Knowledge on electronics is welcome
  - Experience with FPGA use and debug tools is also welcome
- Competences
  - Problem-solving, pro-active, result-oriented work attitude
  - Good communication skills including a good command of the English language (written and spoken)

## Conditions

- The position will be located at BSC within the Computer Sciences Department
- We offer a full-time contract (37.5h/week), a good working environment, a highly stimulating environment with state-of-the-art infrastructure, flexible working hours, extensive training plan, restaurant tickets, private health insurance, support to the relocation procedures
- Duration: Open-ended contract due to technical and scientific activities linked to the project and budget duration
- Holidays: 23 paid vacation days plus 24th and 31st of December per our collective agreement
- Salary: we offer a competitive salary commensurate with the qualifications and experience of the candidate and according to the cost of living in Barcelona
- Starting date: August 2024

## **Applications procedure and process**

All applications must be made through BSC website and contain:

- A full CV in English including contact details
- A Cover Letter with a statement of interest in English, including two contacts for further references - Applications without this document will not be considered

In accordance with the OTM-R principles, a gender-balanced recruitment panel is formed for every vacancy at the beginning of the process. After reviewing the content of the applications, the panel will start the interviews, with at least one technical and one administrative interview. A profile questionnaire as well as a technical exercise may be required during the process.

The panel will make a final decision and all candidates who had contacts with them will receive a feedback with details on the acceptance or rejection of their profile.

At BSC we are seeking continuous improvement in our recruitment processes, for any suggestions or feedback/complaints about our Recruitment Processes, please contact recruitment [at] bsc [dot] es.

For more information follow [this link](#)

## **Deadline**

The vacancy will remain open until a suitable candidate has been hired. Applications will be regularly reviewed and potential candidates will be contacted.

## **OTM-R principles for selection processes**

BSC-CNS is committed to the principles of the Code of Conduct for the Recruitment of Researchers of the European Commission and the Open, Transparent and Merit-based Recruitment principles (OTM-R). This is applied for any potential candidate in all our processes, for example by creating gender-balanced recruitment panels and recognizing career breaks etc.

BSC-CNS is an equal opportunity employer committed to diversity and inclusion. We are pleased to consider all qualified applicants for employment without regard to race, color, religion, sex, sexual orientation, gender identity, national origin, age, disability or any other basis protected by applicable state or local law.

For more information follow [this link](#)

Barcelona Supercomputing Center - Centro Nacional de Supercomputación

---

**Source URL (retrieved on 22 jul 2024 - 13:22):** <https://www.bsc.es/ca/join-us/job-opportunities/44224cscaosre2>