CURRICULUM VITAE

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1. Summary

Professor Mateo Valero, born in 1952 (Alfamén, Zaragoza), obtained his Telecommunication Engineering Degree from the Technical University of Madrid (UPM) in 1974 and his Ph.D. in Telecommunications from the Technical University of Catalonia (UPC) in 1980. He has been teaching at UPC since 1974; since 1983 he has been a full professor at the Computer Architecture Department. He has also been a visiting professor at ENSIMAG, Grenoble (France) and at the University of California, Los Angeles (UCLA). He has been Chair of the Computer Architecture Department (1983-84; 1986-87; 1989-90 and 2001-2005) and the Dean of the Computer Engineering School (1984-85).

His research is in the area of computer architecture, with special emphasis on high performance computers: processor organization, memory hierarchy, systolic array processors, interconnection networks, numerical algorithms, compilers and performance evaluation.

Professor Valero has co-authored over 700 publications: over 500 in Conferences and the rest in Journals and book chapters. He was involved in the organization of more than 300 International Conferences as General Chair (11), including ICS95, ISCA98, ICS99 and PACT01, Steering Committee member (85), Program Chair (26) including ISCA06, Micro05, ICS07 and PACT04, Program Committee member (200), Invited Speaker (70), and Session Chair (61). He has given over 500 talks in conferences, universities and companies. He has been an associate editor of IEEE Transactions on Parallel and Distributed Systems, the IEEE Micro Journal and IEEE Computer Architecture Letters, the Journal of Parallel Programming Languages, as well as guest editing several special issues of the IEEE Transactions on Computers and Computer Magazine.

In the area of his research he has participated in 35 European ESPRIT, RACE and COMETT projects (for some of these projects, he was instrumental in obtaining the participation of Spanish industry), 20 Spanish CICYT projects (similar to NSF projects) and 13 projects with American companies as IBM, Intel, HP and Microsoft.

From 1990 to 1995 Professor Valero created and directed the European Center for Parallelism of Barcelona (CEPBA) performing basic and applied research in parallel computing. He was also the director of C4, the Catalan Center for Computation and Communications, during 1995-2000. Since October 2000 to 2004, he has been the director of CIRI, the CEPBA-IBM Research Institute, created to conduct research on parallel computers. Since May 2004, he has been the director of Barcelona Supercomputing Center, the Spanish national supercomputing centre.

From 1991 to 1993, he was the Spanish representative in the Rubbia Committee which defined the European Supercomputing Strategy. 2003 he was the Spanish Academy of Engineering representative in the EUROCASE (European Association of Academies) committee for the most prestigious awards in IT technologies in Europe. Professor Valero served on the research evaluation committee for engineering in Spain for 3 years and chaired it in 1998. For 3 years, he was a member of the committee for the Eckert-Mauchly

award (the most prestigious award in Computer Architecture) and chaired this committee in 1999. Since April 2002, he has been a Member of the "Ethics in Scientific and

Technological Research" consulting committee, and since February 2004, he has been a member of the "Science and Technology" consulting committee, both established by the Trust of the Spanish Foundation of Science and Technology. From September 2002 until May 2004, he was a member of the Advisory Board of the Intel Microprocessor Research Lab. From October 2005, he was member of the Telefonica Scientific Committee, from February 2006 spokesman of ST Microelectronics Advisory Board and from March 2006 member of the Nokia Advisory Board. He has been and is Board Advisor member for companies as ConSentry, Flowstorm and Xstream, dedicated to design processors for Internet. Since November 2008, he is the chairman for the Techical Advisory board of Telefónica and since December 2008 he is a member of the Advisory Board of the company Miraveo, oriented to the mobile market.

He was awarded the *Narcis Monturiol Award* presented by the Generalitat (Government) de Catalunya in April 1994; the *Salva i Campillo award* presented by the Catalan Telecommunication Engineers Association in January 1996; Spain´s most prestigious award in recognition of outstanding basic research, the *King Jaime I award* presented by the Generalitat Valenciana, in July, 1997; the Spanish National *Julio Rey Pastor award*, in recognition of research in Mathematics and/or IT technologies presented by the Spanish Ministry of Science and Technology in October, 2001; in 2006 he was awarded with the Spanish National Award "Leonardo Torres Quevedo" that recognizes the research in engineering; the First Award "*Distintion FibIT to recognize research*" presented by the Cercle FIBER, the Association of graduates students of the Computer Science Faculty (FIB) of Barcelona in May, 2002; the Award "*Distintion in recognition of research and the promotion of university research*" presented by the Generalitat de Catalonia (July, 2003); and the Lifetime Award "*Spanish Engineer in Telecommunications*" presented by the Spanish Association of Telecommunication´s Engineers (October, 2004). In 2005, he received the Lifetime Award "Merits to Research" presented by the INP, Instituto Politécnico Nacional de Méjico and the Lifetime Award ARITMEL to the "Spanish Engineer on Computer Science". In 2005 he also received the *Premio Cambresca*t for the best professional trajectory in the field on Information Technology and Communication, awarded by the Barcelona Chamber of Commerce, and was recognised as *Aragon Scientist of the Year* by the "Casas Regionales de Aragon". He has also received two collective awards: as CEPBA's Director, he received the 1994 Ciutat de Barcelona Technology Award, and as the Past Chair of the Computer Architecture Department, he received the "Fundación Universidad-Empresa" Award to the University Department with the best European research projects in 1992. In 2006 he was awarded by the Catalan Foundation for Research and Innovation as a researcher who have made a significant contribution to the development of science and technology in Catalonia. In 2007, he received the Eckert-Mauchly Award, the most important award in the world to recognize research in the field of computer architecture by the IEEE, Institute of Electrical and Electronics Engineers and the ACM, the Association for Computing Machinery, the special Award "Aragonés of the year 2007" to recognize research and he was elected knight of the "Royal Association of knights of the Monestir of Yuste". In 2008 he was member of "Hall of fame" at ICT European Program, and selected as one of the 25 most influent european researchers in Information Technologies. In 2009, he received the Harry Goode Award from the IEEE, recognizing his contribution to vector, out-of-order, multithread and VliW processor architectures. In 2013, he received the ACM Distinguished service Award, recognizing his leadership of initiatives in high-performance computing research and education. In 2017, he received the "Charles Babbage" Award by IEEE.

In December 1994, Professor Valero became a founding member of the Royal Spanish Academy of Engineering. In 2005 he was elected *Correspondant Academic* of the Spanish Royal Academy of Mathematics, Physics and Natural Sciences. In 2006, he was elected Academic of the Royal Academy of Science and Arts. In 2000 he became a Fellow of the

IEEE, the Institute of Electrical and Electronics Engineers. In 2002, he became an Intel Distinguished Research Fellow and a Fellow of the ACM, the Association for Computing Machinery. In 1998 he won a "Favourite Son" Award of his home town, Alfamén (Zaragoza) and in 2006, his native town of Alfamén named their Public College after him. In 2008 he was awarded the "Aragón Award" , the most important award given by the Government of Aragón, he was given the Platinum Medal on occasion of the 75th Anniversary of the Guarantee of Origin of "Cariñena wines". In 2008 he was also named Honorary Doctor by the University of Chalmers and by the University of Belgrade and in 2009, he was elected A member of the"Academia Europea", the "Academy of Europe" and he was named Honorary Doctor by the University of Las Palmas de Gran Canaria in Spain. In 2010, he was named Honorary Doctor by the University "Veracruzana", in México and in 2011, he was named Honorary Doctor by the University of Zaragoza. In 2012, he was elected Correspondant Academic of the Mexican Academy of Science. In 2013, he was named Honorary Doctor by the University Complutense de Madrid and "Distinguish Service Award" for ACM. In 2014 he was receive the Honor of COEINF by the College of Computer Engineering, Barcelona and the Honor Award of the ACET, Catalan Association of Telecommunications Engineers and COEINF, College of Computer Engineering. In 2015, he received Seymour Cray Award 2015, presented by the IEEE Computer Society "in recognition of seminal contributions to vector, out-of-order, multithreaded, and VLIW architectures". The Seymour Cray Computer Engineering award is one of the IEEE Computer Society's highest awards, and is presented in recognition of innovative contributions to high-performance computing systems that best exemplify the creative spirit demonstrated by Seymour Cray. He also received the awards: "Euro-Par Achivement Award" in the Euro-Par conference in Vienna, Austria, FEI Award, Innovative Research Category 2015. Open Universe award, Knowdle Consortium Group, open category research 2015, Madrid. He was named Honorary Doctor by the Universities of Cantabria and Granada. In 2016, he received the Gold Medal by AMETIC (Multi-sector Association of Electronics Companies), in recognition of scientific contribution, professional and human qualities. Barcelona. He was awarded the Creu de Sant Jordi. For his work in the field of computer architecture. It is the highest recognition granted by the Government of the Generalitat de Catalunya. In 2017 he was named Honorary doctor by CINVESTAV in México. He also receive the Charles Babbage Award given by IEEE, the highest global award in parallel computing area. He was recognized with an award for his scientific and technique career given by Universidad de Guadalajara and the mexican Supercomputing Congress, ISUM.In 2018, he was elected academic for the Gastronomy Academy in Murcia and Honorary Academic of the European Royal Academy of Doctors. He also received the Aguila Azteca Prize, the highest honour given by the Mexican government to a non mexican citizen and he was elected Correspondant academic by the Engineering Academy in Mexico. In 2020, he was elected Correspondant Academic of the "Real Academia de Medicina de Zaragoza". In 2021 he was awared for CPEIG as Honorary Collegiate and Elected Honorary Member of the "Real Academia de Medicina de Zaragoza".

Awards and Prizes

- *Elected Honarary Member of the "Real Academia de Medicina de Zaragoza", an institution founded in 1831 by King Fernando VII: honored by its important scientific activity. Zaragoza, December 21, 2021.*

- *CPEIG Honorary Collegiate Award of "Noitte Gallega" from Colexio Profesional de Enxeñaria en Informática de Galicia for its world leadership in the field of supercomputing. Santiago de Compostela, October 29, 2021.*

- *Fellow of the Asian-Pacific Artifical Intelligence Association (AAIA) from April 2021 "For his outstanding achievements in the area of supercomputing for artificial intelligence".*

- *Correspondant Academic of the Academia de Medicina de Zaragoza. From 18 December 2020*

- *HPCwire Readers' Choice Awards for RCA: Outstanding Leadership in HPC in November 2020 for "HPC pioneer since 1990 and the driving force behind the renaissance of European HPC independence".*

- *Anual award "AUTELSI 2020" given by the "Asociación Española de Usuarios de Telecomunicaciones y Sociedad de la Información (AUTELSI)" for the excellence job of BSC for being a refference in the tecnological area and for the support and agreement in the data tecnology, 26th march 2020*

- *National public-private R+D Award from the Government of Catalonia and the Catalan Foundation for Research and Innovation (FCRi) to the Repsol – BSC Research joint center. Barcelona, Dec.3th, 2019.*

- *Premio Cénits to the Excellent Research. Award given by the "Fundación COMPUTAEX" in their X Anniversart celebration. "Jornada 10 años de Supercomputación en Extremadura". Cáceres, Spain, March 22th, 2019.*

- *IEEE Life Fellow in recognition of the Mnay Years of Loyal Membership& Support of the Activities of IEEE, year 2018.*

- *"Cruz con distintivo blanco de la Guardia Civil". Barcelona, October 2018.*

- *Aguila Azteca. This is the highest prize given by Mexican government to a non mexican person. From April 2018.*

- *Correspondant Academic of the Academia de Ingeniería de México from April 2018*

- *Elected Honorary Academic of the Real Academia Europea de Doctores. From March 2018.*

- *Academic of the Academia de Gastronomía de Murcia. From February 2018.*

- *BSC award. MareNostrum 4, chosen as the most beautiful data centre in the world. The award, organised by @dcdnews, has been granted by popular vote. Decembre 2017.*

- *Named Honorary Doctor by Cinvestav in Mexico, September 2017.*

- *"Charles Babbage" Award from IEEE. "For contributions to parallel computation through brilliant technical work, mentoring PhD students, and building an incredibly productive European research environment.". April, 2017.*

- *Award for his outstanding trajectory in the scientific and technologic development for the Universidad de Guadalajara in Mexico and the National Committee of the*

International Congress [ISUM](), International Supercomputing Conference in México. Guadalajara, March, 2th, 2017.

- *Named Honorary Doctor by [Universidad de Granada](), May 2016.*

- *Honoured with [Creu de Sant Jordi](). For his work in the field of computer architecture. It is the highest recognition granted by the Government of the Generalitat de Catalunya. April, 2016*

- *Multi-sector Association of Electronics Companies ([AMETIC]()), Gold Medal in recognition of scientific contribution, professional and human qualities. Barcelona, February 2016.*

- *[Seymour Cray]() Award 2015, presented by the IEEE Computer Society "in recognition of seminal contributions to vector, out-of-order, multithreaded, and VLIW architectures". The Seymour Cray Computer Engineering award is one of the IEEE Computer Society's highest awards, and is presented in recognition of innovative contributions to high-performance computing systems that best exemplify the creative spirit demonstrated by Seymour Cray. SC15 Austin, Texas, November, 2015*

- *[FEI]() Award for Innovative Research Category 2015, Madrid, November 2015*

- *"Open Universe" Award presented by the [Knowdle Consortium Group]() (Open Research) Madrid, November 2015.*

- *[Euro-Par Achivement Award]() "For special contributions to Parallel Processing". Euro-Par Conference. Vienna, Austria, August 2015*

- *Named Honorary Doctor by the [Universidad de Cantabria](). March 2015*

- *Named Honorary Member of the Catalan College of Computer Engineering [COEINF](), Barcelona, May 2014.*

- *Award of Honour presented by [ACETT](), Catalan Association of Telecommunications Engineers and COEINF, the College of Engineering. Barcelona, 20 February 2014.*

- *Distinguish Service Award from [ACM](), the Association for Computing Machinery. San Francisco, June 2013 "in recognition of his leadership of initiatives in high-performance computing research and education, including the HiPEAC European Network of Excellence and the Barcelona Supercomputing Center".*

- *Named Honorary Doctor by **[UCM]()**, University Complutense of Madrid. May 9th, 2013*

- *Elected Correspondant Academic by the Academy of Science of Mexico. September 2012.*

- *First National Award from Generalitat to cooperation between research centres and companies. Awarded to BSC and IBM for their long and fruitful collaboration in research. Barcelona February 2012.*

- *[Severo Ochoa Award]() to BSC, the Barcelona Supercomputing Center. This Award was presented by the Spanish Minister of Science and Technology to recognizes BSC as one of the 8 best research centers in Spain. November 2011.*

- *Founding member of "[El Arbol de las Matemáticas]()". This is an initiative of the Spanish Royal Society of Mathematics and of the Universia to recognize latinoamaerica researchers on Mathematics and IT Technologies. Since November 2011. [http://www.arbolmat.com/]()*

- *Named Honorary Doctor by the **[Universidad de Zaragoza]()** in Spain. Zaragoza, 11th February 2011*

- *[Harry H. Goode]() Award, from IEEE, the Institute of Electrical and Electronics Engineers. Citation: "in recognition of Seminal contributions to vector, out-of-order, multithreaded and VLIW processor architectures".*

- *Named Honorary Doctor by the **la Universidad Veracruzana**, in México. Xalapa, 28th. May 2010.*

- *Named Honorary Doctor by the **Universidad de Las Palmas de Gran Canarias** in Spain, November 2010.*

- *Elected Academician of the European Academy, "Academia Europaea". June 2009*

- *Elected member of the ACM Europe Council. June 2009.*

- *"Hall of the Fame" member at the ICT European Program. Selected as one of the 25 most influential European researchers in IT, Information Technologies, during the period 1983-2008. Lyon, France, November 2008.*

- *Platinum Medal as Distinguished "Aragonés", on occasion of the celebration of 75th Anniversary of the Guarantee of Origin of "Cariñena" wines. Awarded by the Advisory Board in "Cariñena", September 2008.*

- *Named Honorary Doctor by the **Universidad de Belgrado**. Belgrade, September 2008. He has been the first expert in Computer Science named honorary Doctor by this University.*

- *Named Honorary Doctor by the **Universidad Tecnológica de Chalmers**. Gothenburg, Sweden, May 2008. He has been the first expert in Computer Science named Honorary Doctor by this University.*

- *"Award Aragón". Known as well as "San Jorge Award", is the most important award of the "Comunidad de Aragón". Awarded by the Government of Aragón. Zaragoza, 23rd April 2008*

- *Eckert-Mauchly Award in 2007, the most important award in the world to recognize research in the field of computer architecture by the IEEE, Institute of Electrical and Electronics Engineers and the ACM, the Association for Computing Machinery. Citation: "For extraordinary leadership in building a world class computer architecture research center, for seminal contributions in the areas of vector computing and multithreading, and for pioneering basic new approaches to instruction-level parallelism."*

- *Knight of the "Royal Association of knights of the Monestir of Yuste". Yuste, June 2007.*

- *Special Award "Aragonés of the year 2007" to recognize research. Zaragoza, May 2007. Aragon is a region in Spain where professor Valero was born.*

- *National Award "Leonardo Torres Quevedo" that recognize the engineering research in the 2006. This recognition is awarded for the Spanish Ministry of Science and Technology, and was presented by the king of Spain.*

- *Awarded by the Catalan Foundation for Research and Innovation as a researcher who have made a significant contribution to the development of science and technology in Catalonia. Barcelona, July 2006.*

- *Member of the Royal Academy of Science and Arts of Barcelona. Barcelona, February 2006.*

- *Correspondant Academic of the Spanish Royal Academy of Mathematics, Physics and Natural Sciences. Since December 2005.*

- *The Public College of his native town of Alfamén (Zaragoza) was named after him in October 2005.*

- *Aragon Scientist of the Year 2005. Awarded by the "Casas Regionales de Aragon."*

- *Lifetime recognition award for "Spanish Engineer in Computer Science". This Award was presented by the organizers of the first Spanish conference on Computer Science, Madrid, July 2005.*

- *Premio [Cambrescat](#) for the best professional trajectory in the field on Information Technology and Communication. Awarded by the Barcelona Chamber of Commerce, November 2005.*

- *Lifetime award in recognition of research (**[CIC](#)**). This award is presented by the Instituto Politécnico nacional de Méjico, Ciudad de Méjico, April 2005.*

- *Lifetime recognition award for "Spanish Engineer in Telecommunications". This Award is presented by the Spanish Association of Engineers in Telecommunications (**[COIT](#)**). Madrid, October, 2004.*

- *Award for "research and promoting research at the university". This award is presented by the Government of Catalunya. Barcelona, July 2003.*

- *[IBM Faculty Partnership Award](#) (FPA) for the research project "Power-PC VMX-Vector Architecture". May 2003.*

- *[Fellow del ACM](#) (Association for Computing Machinery), November 2002. Citation: "For contributions to the design of vector, superscalar, and VLIW architectures, and technical leadership".*

- *[Intel Distinguished Research Fellow](#) "in recognition of his extraordinary efforts to stablish a long term collaboration between the Universitat Politecnica de Catalunya and Intel Corporation leading to the formation of Intel Labs Barcelona", since September 2002.*

- *First Award "Distintion FibIT to recognize research" presented by the **[Cercle FIBER](#)**, the Association of graduates students of the Computer Science Faculty (FIB) of Barcelona. May, 2002*

- *"[Julio Rey Pastor](#)" Award, October, 2001. This is the first Spanish National Award to recognize research in Mathematics and/or IT Technologies. The Award is given by the Spanish Ministry of Science and Technology, and was presented by the King of Spain.*

- *[Fellow del IEEE](#) (The Institute of Electrical and Electronics Engineers), since January 2001. Citation: "For contributions to the design of vector architectures ans superscalar processors".*

- *"Favourite Son" of his birth town, Alfamén (Zaragoza), Spain, since August 1998.*

- *"[Rey Jaime I"](#) Award in Basic Research, November of 1997. Award is given by the "Generalitat de Valencia" and was presented by the Queen of Spain. This is one of the most prestigious awards in Spain. Several Nobel Prizes Laureates are members of the Jury.*

- *"[Salvà i Campillo](#)" Award to the best Telecommunication Engineer, January 1996. Award is presented by the Catalan Association of Telecommunication Engineers.*

- *Founding and youngest member of the [Spanish Academy of Engineering](#), since December 1994 until December 2008.*

- *"[Narcís Monturiol](#)" Award in Basic Research in Catalonia, April 1994. This award is presented by the Government of the "Generalitat de Catalunya".*

- *"[City of Barcelona](#)" Award, in Technology, February 1994. Received on behalf of the CEPBA (European Center for Parallel Computers in Barcelona).*

- *"Fundación Universidad-Empresa" Award to laureate the European University Department with the best Research and Development projects in the European ESPRIT framework. Received as a member of the Computer Architecture Department at UPC. Year 1993.*

- *"Mundo Electrónico" Award to the second best paper on research published during 1986.*

- *"Mundo Electrónico" Award for the best paper on research published in this Journal during 1983.*
- *"Hombre Informático Español". Award presented by the CHIP Magazine, 1983.*
- *"Olympiad in Mathematics" Award. Presented by the Spanish Royal Society in Mathematics, 1969.*

2. Other Honours

- *Member of the selection committee for the IEEE CS Seymour Cray Computer Engineering Award to be presented at SC'21 in November in St. Louis.*
- *Member of the evaluation panel for the scientific technical area of Scientific Computing at CIEMAT. May 2021*
- *Honorary member of the "Golden Anniversary Committee" at ICPP(International Conference on Parallel Processing), will be held in August 2021 in Chicago.*
- *Member of "Consell Acadèmic Assessor de l'Agenda 2030 de l'Ajuntament de Barcelona", 2020.*
- *Member of "Consell Empresarial/Científic de la Fundació BIT Habitat de l'Ajuntament de Barcelona", 2020.*
- *Member of"Consell Consultiu del Pacte Nacional per a la Societat del Coneixement", 20 setiembre 2019.*
- *Member of the Steering Committee for PACT 2020. Parallel Architectures and Compiler Technologies, Georgia Tech, 2019.*
- *Associate Editor of SN Computer Science from January 2019.*
- *Member of the Scientific League: "Men for Women in Science". L'oreal. November 2018.*
- *Honorary member of the "Comité Científico del Congreso GR22", "Conferencia Internacional sobre Relatividad Generay y Gravitación". Valencia, Spain, July 7-12th, 2019*
- *Member of the Committee for the IEEE Seymour Cray Award, presented by the IEEE, The Institute of Electrical and Electronics Engineers. Year 2018.*
- *Member of the Committee for the "Bob Rau" Award, presented by the IEEE, The Institute of Electrical and Electronics Engineers. Year 2018.*
- Member of the external Scientific Advisory Committee of the Universidad Complutense de Madrid. July 2018.
- Benefactor of the graduation of the 2018 promotion from Universidad San Jorge de Zaragoza. May , 2018
- *Club de Roma. Member of the spanish association. From December 2017*
- *Member of the Committee for the IEEE Sidney Fernbach Award, Year 2017*
- *IPDPS Recognition Award. IEEE International Conference on Parallel and Distributed Symposium. Orlando, Florida. June, 2017*
- *Top-10 finalist of the Global Energy Award. The honorees are awarded the Global Energy Prize at the International Economic Forum in St Petersburg. Year 2017.*
- *Foundational member of the IEEE Computer Society Special Technical Community (STC) on "Parallel Model & System: DataFlow and Beyond". Since January 2017.*

- *Organizing Committee Member of the XXVIII Parallel. Málaga, September 19-22, 2016.*

- *IEEE-Micro. Certificate of Appreciation. In grateful recognition of your contributions Editorial Board Member for IEEE Micro Journal. December 2016.*

- *Member of the Committee for the Wallace McDowell Award, presented by the IEEE, the Institute of Electrical and Electronics Engineers. Year 2016.*

- *Business Board Member of the UPC. From November 2016*

- *Member of the Steering Committee for SC, Supercomputing Conference, Utah, November 2016.*

- *Council Board Member of the AEI, Agencia Española de Investigación. From May 2016.*

- *Member of the Committee for the IEEE Seymour Cray Award, Year 2016.*

- *Member of the Committee for the IEEE Sidney Fernbach Award, Year 2016.*

- *Member of ToTA Award Committee (ACM Test of Time) Award Committee, recognizing papers from SC89 to SC06 that have profoundly influenced the discipline of High Performance Computing. 2016.*

- *Member of the ACM Europe Advisory Board. Since May 2015.*

- *Member of the Committee for the "Bob Rau" Award, presented by IEEE, the Institute of Electrical and Electronics Engineers. 2015.*

- *Member of the Committee for the "Harry Goode" Award, presented by IEEE, the Institute of Electrical and Electronics Engineers. 2015.*

- *Member of the Committee for the Wallace McDowell Award, presented by IEEE, the Institute of Electrical and Electronics Engineers. 2015.*

- *Editor with Ulises Cortés. Special Issue of "Computational Intelligence". Journal CyS ("Computación y Sistemas"). UNAM, Universidad Nacional Autónoma de México. Vol 18, no. 4, 2014.*

- *Member of the Committee for ToTA Award, the ACM Test of Time Award, recognizing papers from SC88 to SC05 that have profoundly influenced the discipline of High Performance Computing. 2015*

- *Member of the Committee for the IEEE Fellow Committee. 2015.*

- *Member of the Jury for the BBVA Foundation Frontiers of Knowledge Award on Information and Telecommunication technologies. Madrid, January 2015.*

- *Members of the Advisory Committee for the "50th Anniversary of the Telecommunications Engineering School Moncloa Campus, Madrid, March 2014.*

- *Member of the Jury for the BBVA Foundation Frontiers of Knowledge Award on Information and Telecommunication Technologies. Madrid, January 2015.*

- *Member of the Steering Committee for the Workshop on Big Data in Biomedicine: Challenges and Opportunities. Organized by B-Debate, International Center for Scientific Debate. Barcelona, November 11-12th, 2014*

- *Member of the Advisory Committee for the 50th. Anniverssary of the School of Telecomunication Engineers of Madrid. October 2014.*

- *Member of the Advisory Board for the Journal SUPERFRI: International Journal on Supercomputing Frontiers and Innovations. Since, May 2014*

- *Member of the Committee for the IEEE Fellow Committee. Year 2014.*

- *Member of the Committee for the "Harry Goode" Award, presented by the IEEE, The Institute of Electrical and Electronics Engineers. Year 2014*

- *Member of the Committee for the [Wallace McDowell Award](), presented by the IEEE, the Institute of Electrical and Electronics Engineers. Year 2014*

- *Member of the Committee for the [George Michael Fellowship Award](), presented by the IEEE, The Institute of Electrical and Electronics Engineers. Year 2014*

- *Member at large for the Steering Committee of the [IEEE-ACM PACT Conference](). International Conference on Parallel Architecures on Compiler Techniques. Since January 2014.*

- *Honorary Chair. [HPCS-2014](). The International Conference on High Performance Computing and Simulation. Bologna, Italy, July 21-25, 2014.*

- *Member of the Jury for the [IEEE-TCSC Award](), The Institute of Electrical and Electronics Engineers Technical Committee on Scalable Computing. Award for Excellence in Scalable Computing. Year 2014*

- *Selected as one of the most important 25th Spanish Scientist of 2013. Newspaper El Mundo, January 2014*

- *Member of the Jury for the [BBVA Foundation Frontiers of Knowledge Award]() on Information and Telecommunication technologies. Madrid, January 2014.*

- *Member of the "[UPC]()´s Industrial Advisory Board", since June 2013.*

- *Miembro del "Consell de Col-laboradors" de [ICREA](). Barcelona, Abril, 2013.*

- *Member of the Committee for selecting the best paper "Test and Time" Award, for the IEEE-ACM Supercomputing Conference. Year 2013.*

- *Selected as one of the most important 25th Spanish Scientist of 2012. Newspaper El Mundo, January 2013*

- *Vice-president and member of the Committee of Associated Research Centers to [UPC](), representing Barcelona Supercomputing Center, since January 2013*

- *Member of the Jury for the [BBVA Foundation Frontiers of Knowledge Award]() on Information and Telecommunication technologies. Madrid, January 2013.*

- *Member of the Committee for the evaluation of projects for the [US-Israel Binational Science Foundation](). February 2013.*

- *Memer of the Advisory Board of "Consejo Asesor de las Tecnologías de la Información y la Comunicación" . Generalitat de Cataluña. Year 2012.*

- *Member of the Scientific Advisory Board of the Barcelona Knowledge Campus, representing UPC, Technical University od Catalonia, since September 2012.*

- *Member of the Committee for the "[Bob Rau" Award](), presented by the IEEE, the Institute of Electrical and Electronics Engineers. November, 2012*

- *Member of the Committee for the "[Harry Goode" Award](), presented by the IEEE, The Institute of Electrical and Electronics Engineers, November 2012*

- *Member of the Committee for the [Wallace McDowell Award](), presented by the IEEE, the Institute of Electrical and Electronics Engineers, November 2012*

- *Member of the Editorial Board for [LITES](), the Leibniz Transactions on Embedded Systema, since July 2012.*

- *Member of the [IEEE TCPP Award Committee](). Year 2012*

- *Member of the Board "[UPC Alumni]()". UPC, Technical University of Catalonia. Since February 2012.*

- *Member of the Academy´s Board of Trustees of the [EIA](), European Alliance for Innovation Academy. Since 2012.*

- *Member of the Advisory Board for the EU project SCC-Computing: strategic collaboration with China on super-computing.*

- *Member of the Evaluation Committee [Wallenberg Academy Fellows](#) 2012, presented by Chalmers University.*

- *Associate editor of the Journal IEEE Transactions on Computers, since October 2011.*

- *Member of the IEEE Computer Society 2012 Fellows Evaluation Committee*

- *Member of the Committee for the ["Bob Rau" Award](#), presented by the IEEE, the Institute of Electrical and Electronics Engineers*

- *Member of the Committee for the ["Harry Goode" Award](#), presented by the IEEE, The Institute of Electrical and Electronics Engineers*

- *Member of the Committee for the [Wallace McDowell Award](#), presented by the IEEE, the Institute of Electrical and Electronics Engineers*

- *Selected as one of the most important 25th Spanish Scientist of 2009. Newspaper El Mundo, January 2011*

- *Member of the Advisory Board for ISC 2010, the International Conference on Supercomputing. Hamburg, Germany, May 30, June 3, 2010.*

- *Co-editor for the special issue of the IEEE Micro Journal: "Multicore and Europe". September- Ocober 2010.*

- *Godfather of the Graduation Ceremony for the students of [the School of engineers in Terrassa](#). July 15th, 2010.*

- *Chairman of the Committee to evaluate the projects on "Applications for Exaflops Supercomputers", funded by the countries members of the G8 organization.*

- *Selected as one of the most important 25th Spanish Scientist of 2009. Newspaper El Mundo, January 2010.*

- *Member of the Advisory Board of theResearch Center on Information and Communication technologies. University ofsantiago de Compostela, Spain. Since November 2009.*

- *Member of the Editorial Board of the [UPM Press](#), (Technical University of Madrid), a Digital editorial. UPM, Madrid, since November 2009.*

- *Member of the Advisory Board of the CBBC, the Colombian National Center for Bioinformatics and Computacional Biology. June 2009.*

- *Member of the Scientific Advisory Board of the City of Barcelona. April, 2009.*

- *Member of the Executive Committee for the International Exascale Software Project ([IESP](#)), worldwide initiative oriented to the programming of the future Exaflops supercomputers. Since April 2009.*

- *Chair of Gordon Bell Prize Committee that will award [Gordon Bell Prize](#) in 2009. This prize recognizes outstanding achievement in high performance computing (supercomputing) in several areas emphasizing innovation in applying HPC to applications in science. Portland, November 2009.*

- *Selected as one of the most important 25th Spanish Scientist of 2008. Newspaper El Mundo, January 2009.*

- *Member of the Advisory Board of the company [Miraveo](#), that creates infrastructure and applications for Spontaneous Area Networks through cell phones. Since December 2008.*

- *Chair of [Telefónica](#) Scientific Advisory Board. Since November 2008.*

- *Member of Advisory Board of the Spanish National Research Council, [CSIC](#). Nominated by MEC, Education and Science Ministry. Since January 2008.*

- *Member of the Jury that awarded the Prize of "Proyección Investigadora de la UPM", Technical University of Madrid. Madrid, December 2008.*

- *Member of Editor Board of the Magazine Scientific Programming. Special number of "Scientific Computing" on "High Performance Computing on Cell B.E. Processors". December 2008.*

- *Member of the Advisory Board of the Technical Telecommunication Engineering School of Barcelona. Since January 2008.*

- *Member of Advisory Board of ISC (International Symposium on Supercomputing), since 2007.*

- *Distinguish Engineer in Residence. Invitation to visit the Cockrell School of Engineering at the University of Texas at Austin as the 2008 Distinguish Engineer in Residence. Austin, February 2008.*

- *Member of the Steering Committee and Adviser for CIDC, "Centre Internacional per al Debat Cientific", created by the Catalan Government. Since January de 2008.*

- *Coordinator for the Workshop on "High-Performance Computing Systems", organized by the European Union. Brussels, December, 17th, 2007*

- *ISTAG member. ISTAG is an advisory board to the European Commission in the field of Information and Communication Technology. In addition, member of the working Group on Intenational Cooperation, InCo. Since October 2007.*

- *Distinguished Lecturer for DSP (Distinguish Speakers program) del ACM, "Association for Computer Machinery". Since September 2007.*

- *Member of the Jury of the Gordon Bell Awards, to recognize the efficiency of parallel applications executed in Supercomputers. Reno, November, 2007.*

- *Coordinator for the Workshop on "High Performance Computers" organized by the Spanish Royal Academy of Engineering. Madrid, October 23th, 2007.*

- *Main Speaker for the "Fiestas del Pilar" (The Mayor Festibity in Aragon) in Barcelona, organized by the Aragones Center in Barcelona, October 7th, 2007.*

- *Colaborator of IACAT, Institute for Advanced Computing Applications and Technologies of the University of Illinois at Urbana Champaign, UIUC, since May 2007.*

- *Member of the Committee in charge of selecting the editor in chief of the Journal CAL, Computer Architecture Letters of the IEEE, Institute of Electrical and Electronics Engineers, May 2007.*

- *Member of the Technical Advisory Board of CIAT, International Center for Research in High Technology, of the ITLA, the Institute of Technology Las Américas. Republican Of Dominica, April, 2007.*

- *Member of the Advisory Board on "HPC, High Performance Computing Simulation Challengue", EDF, Electricité de France, March, 2007.*

- *Member od the Jury for the Awards of the RAI, Royal Spanish Academy of Engineering, February 2007.*

- *Member of the "Consell Promotor de l'Any de la Ciència 2007", from June 2006. Barcelona Council.*

- *Evaluator for the IEEE Micro Journal special issue on Hotchips on Computer Architecture. December 2006*

- *Member of the Jury of the Seymour Cray Award, Reno, November 2007.*

- *Associated Editor for the Journal IJHPS, "International Journal of High Performance Systems Architecture" for InderScience Publisher. From June 2006.*

- *Member of Technical Advisory Board in Nokia related to Computer Architecture, April 2006.*

- *First research group that obtains a contract with Microsoft to investigate in Computers Architecture. The project is called: "Many-core Architectures" and the objective is the design for future chips. February 2006.*

- *Jury member for the National Award on Computer Science, 2006*

- *STMicroelecrónica Technology Council member related to Computer Architecture, 2006*

- *Member of the Jury of the Seymour Cray Award, Tampa, November 2006.*

- *General Vice-chair. IEEE International Conference on Cluster Computing. Barcelona September, 25-28, 2006.*

- *Member of the Steering Committee of the HiPEAC Conference, High Performance Embedded Applications and Architectures. Since December 2005*

- *Joint Editor, with Tom Conte, Nacho Navarro, Wen-mei W. Hwu and Theo Ungerer of HiPEAC Conference "High Performance Embedded Architecture and Compilers". Lecture Notes on Computer Science, no. 3793. Barcelona, Spain, November 2005. ISBN 3-540-30317-0*

- *Member of the Jury for the Spanish National Award "Julio Rey Pastor" for research in Mathematics and Information Technology and Communications. This prize was awarded by the Ministry of Science and Technology, Madrid in October, 2005.*

- *Evaluator for the IEEE Micro Journal special issue on Top-Picks on Computer Architecture. November 2005*

- *Member of the Advisory Technical Committee of the ConSentry company, dedicated to the design of network processors. Since July 2005*

- *Member of the Technical Advisory Board of Telefónica, since July 2005.*

- *Member of the Editorial Board of the IEEE Micro Journal, since July 2005.*

- *Distinguish Professor of CIC, Research Center on Computers. IPN, Polithecnic Institute of Mexico, since April 2005.*

- *Associate Editor for the "Journal of High-Performance Embedded Architectures and Compilation Methods". Since May 2005.*

- *Member of the Jury for "L2-Cache Championship". April 2005.*

- *Member of the Steering Committee for ACACES, the Summer School of HiPEAC. Láquila, Italy, July 2005.*

- *Gold Medal of the Computer Science Faculty of Barcelona, for 25 years of teaching. Barcelona, April 2005.*

- *Member of the Advisory Board of the Foundation "Zaragoza, city of knowledge" created to potentiate the Expo-2008. Since April 2005.*

- *Member of the Committee "Science and Technology" oriented to promote the collaboration between basic research and industry and promoted by COSCE (Association of the spanish Scientific Societies). Since January 2005.*

- *Member of the Editorial Board of the Journal "Research on Computer Science", the latinoamerican Journal on Computation. Since January 2005.*

- *Member of the technical Board of Directors of Telefónica I+D. Since July 2005.*

- *Coordinator of HiPEAC (High Peformance Embedded Architectures and Compilers). HiPEAC is a European Network of Excellence on research. All the European research groups with research and interest in these topics collaborate in the network. From September 2004 to September 2008.*

- *Evaluator for the IEEE Micro Journal special issue on Top-Picks on Computer Architecture. August 2004.*

- *Director of the Summer School "An Open Window to the future of the Computer Architecture". Organized by the University of "Castilla La Mancha". Albacete, July 7-9 2004.*

- *Invited speaker for the Students Chapter of the ACM in Mexico. Inaugural lecture: "Kilo-instruction Processors". Ciudad Madero, Méjico. May 2004.*

- *Member of the Jury of the "Championship Branch Predictor Competition". November 2004*

- *Founding Member of the Consulting Committee "Championship Branch Predictor Competition". This Championship is sponsored by Intel-MRL and by the IEEE Techincall Committee on Microarchitecture. Since July 2004.*

- *Member of the Advisory Board of the U.S. Company Tidal Networks, dedicated to the design of special purpose hardware for Internet security, since 2003.*

- *Director of the Barcelona Supercomputing Center, since May 2004. BSC is the National Center of Supercomputingnter. He promoted the creation of this center for several years.*

- *Promoter of the ACA action. The "Advanced Computer Architecture" action was created as part of the FET (Future Emerging Technology) Program. FET is part of the IST program of the European Union.*

- *Coordinator of the Spanish committee on E-science. This committeee was created by the Spanish Foundation of Science and Technology. Since March 2004.*

- *Member of the "Science and Engineering" consulting committee. Presented by the Trust of the Spanish Foundation of Science and Technology. Since February 2004.*

- *Moderator of the Panel "New Computing Architectures: Advanced Compilers Technologies and Processor Architectures". European FET (Future Emerging Technologies) Program. Brussels, Oct. 13th. 2003.*

- *Founding member of the Editorial Board for the Journal "Revista latina del IEEE". This Journal was founded by Region 9 of the IEEE (The Institute of Electrical and Electronics Engineering). Since November 2003.*

- *Member of the Committee of the Spanish National Award "Julio Rey Pastor" to recognize research in Mathematics and/or IT Technologies. The Award is given by the Spanish Ministry of Science and Technology. Madrid, September 2003.*

- *Founding member of the Editorial Board for the Journal IJCSE (International Journal of Computational Science and Engineering) InderScience Publishers, since June 2003.*

- *Member of the NSF (National Science Foundation) Panel "On the Future of Computer Architecture Research: Wise Views and Fresh Perspectives". San Diego, June 2003.*

- *Member of the Editorial Board of the Journal IJHPCN, International Journal on High Performance Computing and Networking, since April 2003.*

- *President of the Jury to evaluate the best Thesis in Computer Science. Technical University of Catalonia. April, 2003.*

- *Member of the committee on "Large Supercomputer Facilities", on behalf of the spanish ministery of Science and Tecnology. This committee reports to ESFRI (European Strategy Forum for Researcher Infraestructure. Year 2003.*

- *Member of the Advisory Board of the Intel-MRL, Microprocessor Research Laboratories, from September 2002 until May 2004.*

- *Member of the "Ethics in Scientific and Technologic Research" consulting committee. Presented by the Trust of the Spanish Foundation of Science and Technology. Since April 2002.*

- *Member of the Advisory Board of the EPFL (Ecole Polytechnique Fédéralé de Lausanne) School of Computer and Communication Sciences. Year 2002.*

- *Member of the Senate of UPC (Technical University of Catalonia). The Senate, was created in January 2002, and is composed by professor who received recognitions to their research activities. Foundational Member.*

- *Member of the Advisory Board of the XXV Anniversary of the Computer Science Faculty of Barcelona, 2002*

- *Member of the Editorial Board of the Journal "Computación y Sistemas", the Latinoamerican Journal on Computation. ISSN 1405-5546. Since January 2002.*

- *Member of the Editorial Board of the IEEE Journal Computer Architecture Letters since Oct. 2001.*

- *Member of the Advisory Technical Committee of the TecnoCampus Iniciative of the city of Mataró (Barcelona). Since November 2001 to December 2004.*

- *Member of the Advisory Board of the IEEE Computer Society Technical Committee on Computer Architecture (TCCA), since September 2001.*

- *Member of the Advisory Technical Committee of the Flowstorm Company, dedicated to the design of network processors. Since 2001 to 2003.*

- *Joint Editor, with Lionel M. Ni of the Proceedings of the 2001 International Conference on Parallel Processing. Valencia 3-7 of September, 2001. IEEE Computer Society Press. ISBN 0-7695-1257-7.*

- *Iberdrola award for "Visiting Professors" for years 2001, 2002 and 2003.*

- *Joint Editor, with S. Vajapeyam of the IEEE Computer Magazine special issue on "Early 21st Century Processors". April 2001, pp.47-82.*

- *Member of the Jury to evaluate the research activities of the LRI, Laboratoire de Recherche en Informatique. Université Paris-Sud. Paris, April, 2001.*

- *Member of the Spanish advisory board for "Large Scale Scientific Facilities", years 2001-2004.*

- *Joint Editor, with V. Prasanna and S. Vajapeyam of the "International Conference on High-Performance Computing, HiPC-7". Lectures Notes on Computer Science number 1970. Bangalore, Dec. 2000. ISBN 3-540-41429-0.*

- *Member of the Editorial Board of the Journal "Concurrency: Practice and Experience". John Wiley and Sons. Since December 2000.*

- *Joint Editor, with K. Joe, M. Kitsuregawa and H. Tanaka of the "Third International Symposium on High Performance Computing, ISHPC 2000". Lectures Notes on Computer Science number 1940. Tokyo, October 2000. ISBN 3-540-41128-3.*

- *Director of CIRI (CEPBA-IBM Research Institute), devoted to research on parallel computers. Since October 2000 to April 2004.*

- *Evaluator for European Projects on the topic "Future Emerging Technologies". Brussels, May 2000.*

- *Senior member of IEEE (The Institute of Electrical and Electronics Engineers), since February 2000.*

- *Gold Medal of the School of Telecommunication Engineerr of Barcelona, for 25 years of teaching. Barcelona, October 1999.*

- *ACM (Association for Computer Machinery) representative to IFIP (International Federation of Information Processing) TC10: Technical Committee on Computer System Technology. Years 1999-2001.*

- *Four (six years each) possitive evaluations of the research activity corresponding to the period 1975-1998.*

- *UPC professor representative in the UPC Board of Trustees, from 1999 to 2001.*

- *Spanish Academy of Engineering representative in the Jury of the "European Information Technology Prizes". These are the best European awards for IT Technologies Projects. 1997 (Brussels),1998 (Vienna), 1999 (Helsinky), 2000 (Nice), 2001 (Dusseldorf), and 2002 (Copenhaguen).*

- *Member of the "Comisión de Investigcación del Alto Consejo Ejecutivo" to the Government of the "Generalitat de Valencia". Since 2000. This Council advises the Government of Valencia on research initiatives.*

- *ACM (Association for Computer Machinery) representative to the Eckert-Mauchly Award Committee. This IEEE (Institute of Electrical and Electronics Engineers) and ACM (Association for Computer Machinery) award is the most prestigious award in the field of Computer Architecture. 1998, 1999 (chair), and 2000.*

- *Member of the Advisory Technical Committee of the Xstream Company, dedicated to the design of simultaneous multithreaded processors designed for network computing. From 1999 to 2001.*

- *Member of the Jury for the "Salva i Campillo Awards" in 1999. This Jury decides the ACET (Catalan Association of Telecommunication Engineers) award for Telecommunication Engineering.*

- *Joint Editor, with V. Milutinovic, of the IEEE (Institute of Electrical and Electronics Engineers) Transactions on Computers special issue on "Cache Memories and Related Problems", February 1999.*

- *Member of the Board of the Spanish Academy of Engineering, from 1999 to 2003.*

- *Member of the Council of UPC (Technical University of Catalunya) from 1983 to 1986 and from 1996 to 2001.*

- *Member of the Working Group "Information and Telecommunication Technologies". OCYT (Spanish Office for Science and Technology), 1999. This group wrote the Information and Telecommunication Technologies part of the Spanish "Plan Nacional de Investigación" for the years 2000-2003.*

- *Librarian of the Spanish Academy of Engineering, since 1999.*

- *Member of the Executive Committee of the IEEE (The Institute of Electrical and Electronics Engineers) CSIDC (Computer Society International Design Competition), from 1998 to 2001.*

- *Member of the Executive Board of the CIMNE (Center for Numerical Methods in the Engineering) in Barcelona, from 1996 to 2000.*

- *Member of the Editorial Board of Scientific Programming. Editor John Wiley and Sons. Since 1992.*

- *Member of the Jury for the "City of Barcelona" award in Technology. Years 1996 and 1998.*

- *Member of the Technical Committee on "Innovation and Knowledge", Barcelona City Hall, since 1998.*

- *Member of the "Alto Consejo Ejecutivo" to the Government of the "Generalitat de Valencia" since 1998. This Council advises the Government of Valencia on research initiatives.*

- *Associate Editor to the IEEE (The Institute of Electrical and Electronics Engineers) Transactions on Parallel and Distributed System. 1995 to 1999.*

- *Member of the Spanish "Research Evaluation Committee" for Engineering. 1995, 1996 and 1997 (chair). This committee evaluates the research activities on engineering of all the spanish professors on engineering.*

- *Associate Editor. Computer Science Books. Editor Chapman and Hall. 1995, 1996, 1997 and 1998.*

- *President of the Spanish committee responsible for the evaluation of the Computer Science Faculty of the University Jaume I of Castellón. September, 1996.*

- *Director of the C4 (Catalan Center for Computation and Communication) during 1995-2000. This Center was created by the Government of the "Generalitat de Catalunya" to coordinate the activities of CESCA (Supercomputing Center of Catalonia) and CEPBA (European Center for Parallel Computers in Barcelona).*

- *Promoter and first director of CEPBA (European Center for Parallel Computers in Barcelona). CEPBA belongs to UPC (Universitat Politècnica de Catalunya) and was created in 1991 by the Ministry of Education of Spain and by the catalan CIRIT (Comissió Interministerial de Recerca e Innovació Tecnològica) to promote Basic Research and Tecnology Transfer to industries on Parallel Computing. Director from 1991 to 1995.*

- *Associate Editor of the Parallel Processing Letters Journal. World Scientific since 1985.*

- *Consultor to the Catalan Government on topics related to Supercomputers, 1994.*

- *Spanish Ministry of Education representative to the European Committee concerning Supercomputing, chaired by the Nobel Prize Laureate Carlo Rubbia, from 1991 to 1993.*

- *Joint Editor, with E. Oñate, M. Jane, J-LL. Larriba and B. Súarez for the Proceedings of the PACTA-92 Conference: "Parallel Computing and Transputers Applications: Parts I and II". IOS Press, Amsterdam. ISBN: 90-5199-096-0 . October 1992.*

- *Joint Editor, with S. Y. Kung, T. Lang and J. Fortes, for the Proceedings of the Conference "Application Specific Array Processors". IEEE Computer Society Press. September 1991. ISBN 0-8186-9237-5.*

- *Evaluator of IT European ESPRIT projects. Brussels, 1986.*

- *Vice-President of the IEEE (The Institute of Electrical and Electronics Engineers) Computer Society Spanish Chapter from 1986 to 1990.*

- *Visiting Professor at UCLA (University of California at Los Angeles). July and August 1989, and April and May of 1986.*

- *Dean of the Computer Engineering School at UPC (Universitat Politècnica de Catalunya) in 1984 and 1985.*

- *Director of the Computer Architecture Department at UPC (Universitat Politecnica de Catalonia). 1983 -1984, 1986-1987, 1989-1990 and since 2001.*

- *Visiting Professor at ENSIMAG, Grenoble (France). September to December of 1977 and October to December of 1978.*

- *Member of the Editorial Board of the Spanish Journal "Mundo Electrónico" from 1982 to 1989.*

- *Scholarship holder from the Spanish Ministry of Education. Ph.D student from 1975 to 1978.*

- *Scholarship holder from the Bank "Caja de Ahorros de Zaragoza, Aragón y Rioja" to carry out studies at IBM in 1973 and 1974.*

- *Scholarship holder from the "Spanish Royal Academy of Mathematics" to study Mathematics. Finalist in the "Olympiad in Mathematics". 1969-1970.*

3. Conferences Organization

3.1 General Chair

- *RISC V*
- *NCP Japan*
- *General Chair. ACM Europe Conference. Barcelona, September 7-8, 2017.*
- *Honorary Chair, IEEE/ACM CCGrid Conference, International Symposium on Cluster, Cloud and Grid Computing. Madrid, Spain, from 14th to 17th of May 14-17th, 2017.*
- *Co-general Chair del Workshop RoMoL, Riding on the Moore`s Law. Barcelona, March, 17-18, 2016.*
- *Co-general Chair of the Workshop Mateo 2012: Multicore Architectures and Their Effective Operation. Barcelona, June, 28-29, 2012.*
- *Co-general Chair dWorkshop BMW, Barcelona Multicore Workshop, from Microsoft and BSC (Barcelona Supercomputing Center). Barcelona, October 2010 and November 2011.*
- *PACT-2001. IEEE-ACM "Parallel Architectures and Compilation Techniques". Barcelona, Sept. 8-12th 2001.*
- *Summer School on Computer Architecture. University of Castilla La Mancha, Albacete, July 2004.*
- *ICS-13. IEEE-ACM "Int. Conference on Supercomputing". Rhodes, June 1999 (General Co-Chair).*
- *ISCA-25. IEEE-ACM "International Symposium on Computer Architecture". Barcelona, June 1998.*
- *ICS-9. IEEE-ACM "International Conference on Supercomputing". Barcelona, July 1995.*
- *Spanish Workshop on Parallel Computing. Barcelona, July 1995.*
- *EUROMICRO. Barcelona, September 1993.*
- *PACTA-92 "Parallel Computers and Transputer Applications". Barcelona, September 1992.*
- *EWPC´92 "European Workshop on Parallel Computing" Barcelona, March 1992.*
- *ASAP´91. IEEE "Algorithm Specific Array Processors". Barcelona, September 1991.*
- *Spanish Workshop on Parallel Computing. Sitges (Barcelona), October 1990.*

3.2 Program Chair / Vice-chair

- *HPCC. IEEE International Conference on High Performance and Communications. Co-program Chair. Dalian, China, September 25-29, 2008*
- *ICS-2007. Co-program Chair. IEEE-ACM International Conference on Supercomputing. Seattle, June 2007.*
- *ISCA-2006. IEEE-ACM International Symposium on Computer Architecture. Program Chair. Boston, May 2006.*

- *General Vice-chair. IEEE International Conference on Cluster Computing. Barcelona September, 25-28, 2006.*

- *International Vice-chair. IEEE INFOCOM. The IEEE Conference on Computer Communications. Barcelona, April 23-27, 2006.*

- *Micro-2005. The 38th. IEEE/ACM International Symposium on Microarchitecture. Program Chair. Barcelona, 12-16 November 2005.*

- *HiPEAC´05. International Conference on High Performance Embedded Architectures and Compilers. Program Chiar. Barcelona, November 17 to November 18, 2005.*

- *CF-2005. Program Chair. ACM Conference on Computing Frontiers. Ischia, Italy, April 2005.*

- *PACT-2004. Program Chair. IEEE International Conference on Parallel Architectures and Compilation Techniques. Antibes, France. October 11-15, 2004.*

- *Institutional Chair. HPCA-10. IEEE, International Symposium on High Performance Computer Architecture. Madrid, February 14-18, 2003.*

- *Plenary Chair. DSD-03 "Euromicro Workshop on Digital System Design". Nevada, Las Vegas, Sept 2003.*

- *Awards Co-Chair. ICPP-2002. "International Conference on Parallel Processing". Vancouver, Canada. August 18-21, 2002.*

- *ISHPC. "International Symposium on High Performance Computers". Computer Architecture Vice-Program Chair. Nara, Japan. May, 15-17th, 2002.*

- *ICPP. "International Conference on Parallel Processing". Program Chair. Valencia, Spain. Sept. 2001.*

- *EuroPar. Program Vice-chair for the Workshop "Parallel and Distributed Embedded Systems". Manchester, Sept. 2001*

- *HiPC-2000. International Conference for High-Performance Computing. Bangalore (India), Dec. 2000. Co-chair for the special invited session on "Future Processors".*

- *EuroPar. Program Vice-chair for the Workshop "Parallel Computer Architecture". Munich, Sept. 2000.*

- *ICPP. "International Conference on Parallel Processing". Computer Architecture Program Vice-chair. Minneapolis, USA, August 2000.*

- *HiPC. "International Conference On High Performance Computing". Program Chair. Bangalore, 17-20th Dec. 2000.*

- *PACT. IEEE – ACM "Parallel Architectures and Compilation Techniques". European Publicity Chair. Philadelphia, USA, October 2000.*

- *ISHPC. "International Symposium on High Performance Computers". Program Chair. Tokyo, Japan, October 2000*

- *HiPC. "Int. Conference On High Performance Computing", Program Vice-chair. Calcuta, Dec. 1999.*

- *EuroPar. Program Chair for the Instruction Level Parallelism Track. Toulouse, Sept. 1999*

- *EuroPar. Program Chair for the Parallel Computer Architecture Track. Southampton, September 1998*

- *EuroPar. Program Chair for the Automatic Parallelization and High Performance Computers Track. Lyon, 1996.*

- *PDP. "Euromicro Workshop on Parallel and Distributed Processing". Program Chair. Sanremo, Italy, January 1994.*

3.3 Steering Committee

- *ISCA. IEEE-ACM "International Conference on Computer Architecture". Durante los años 1999, 2001, 2002, 2004, 2005, 2007, 2008 y 2010.*

- *MICRO, IEEE/ACM International Symposium on Microarchitecture. Since September 2005.*

- *PACT. IEEE-ACM-IFIP "Parallel Architectures and Compilation Techniques". Since 2001 to 2007*

- *ICS. IEEE-ACM "International Conference on Supercomputing". Since 1995.*

- *HPCS, 2013, International Conference on High Performance Computing and Simulation. Helsinki, Finland, July 1-5, 2013.*

- *ISCA. IEEE-ACM "International Conference on Computer Architecture". 1999, 2001, 2002, 2004, 2005, 2007, 2008 and 2010.*

- *MICRO, IEEE/ACM International Symposium on Microarchitecture. Since September 2005*

- *PACT. IEEE-ACM-IFIP "Parallel Architectures and Compilation Techniques". Since 2001 to 2007.*

- *ICS. IEEE-ACM "International Conference on Supercomputing". Since 1995*

- *Summer School on "Advanced Computer Architecture and Compilation for Embedded Systems. HiPEAC: European Network of Excellence. Láquila, Italy, July 24 to July 30, 2005.*

- *CF. ACM conference on Computing Frontiers. Year 2005 and 2006.*

- *HiPEAC Conference, since Noviembre de 2005.*

- *ACSAC. Asia-Pacific Computer Architecture Conference. Since 2004.*

- *EuroPar. 1995, and since 1999 till now.*

- *HPCS. International Conference on High Performance and Simulation. Istanbul, Turkey, 2011*

- *ParCo. Parallel Computing. Málaga, Spain, 2005.*

- *Workshop on Software Tools for Manycore Systems, to be held in conjuction with ISCA 2008, Beijin, China, June 2008.*

- *PARLE. "Parallel Architectures and Languages Europe". 1992 and 1993*

- *Spanish Workshop on Parallel Computers, since 1990.*

3.4 Program Committee Member

- *ISCA. IEEE-ACM "International Conference on Computer Architecture" for 1995 (Santa Margarita, Italy), 1996 (Philadelphia), 1997 (Denver), 1999 (Atlanta), 2001 (Göteborg, Sweden), 2002 (Anchorage, Alaska), 2004 (Munchen, Germany), 2005 (Wisconsin, USA), 2008 (Beijing, China), 2010 (Saint-Malo, France) and 2011, San José, USA..*

- *MICRO. IEEE-ACM "International Symposium on Microarchitecture". 1999 (Haifa, Israel), 2000 (Monterey, USA), 2001 (Austin, TX) and 2006 (Orlando, Florida, USA).*

- *HPCA. IEEE "International Symposium on High Performance Computer Architecture". 1998 (Las Vegas, Nevada), 1999 (Orlando, Florida), 2001 (Monterrey, Mexico), 2002 (Boston, USA), 2003 (Anaheim, California), 2005 (San Francisco, California), 2006 (Austin, Texas), 2008 (Raleig, North Carolina, USA) and 2010 (Bangalore, India).*

- *Evaluator for the special issues on Top-Picks for the IEEE Micro Journal, years 2004 and 2005.*

- *ICS. IEEE-ACM "International Conference on Supercomputing". 1993 (Tokyo, Japan), 1994 (Manchester, England), 1998 (Melbourne, Australia), 2000 (Santa Fe, New Mexico), 2002 (New York, USA), 2005 (Boston, USA) and 2009 (New York, USA).*

- *PACT. IEEE-ACM "Parallel Architectures and Compilation Techniques". 1997 (San Francisco, USA), 1998 (Paris, France), 1999 (Newport Beach, California), 2002 (Charlottesville, Virginia, USA), 2003 (New Orleans, USA) and 2007 (Bucharest, Romania).*

- *ISC, International Supercomputing Conference. Dresden, Germany, June 2005*

- *Hot Chips 16. IEEE Symposium on High Performance Chips. Stanford, August 2004*

- *CF. ACM Computing Frontiers. Ischia, Italy, April 2006 and May 2007.*

- *ISPASS. IEEE, International Symposium on Peformance Analysis of Systems and Software. Austin, Texas, 2005.*

- *IEEE Cluster Computing. Year 2001, Newport Beach, October 2001.*

- *ICPP. "International Conference on Parallel Processing". 1997 (Chicago, USA), 1999 (Aizu, Japan) and 2002 (Vancouver, Canada).*

- *SC. ACM "Supercomputing Conference", 1998 (Orlando, Florida), 2000 (Dallas, Texas) and 2002 (Baltimore, MD).*

- *PaCT-2005. Parallel Computing Technologies. Krasnoyarsk, Russia, Sept. 5-9, 2005 and Pereslav- Zalesky, Russia, Sep. 3-7, 2007 and Novosibirsk, Rusia, August 31 to September 4, 2009 and Sptember 2011, (Kazan, Russia).*

- *MTAAP'07. Workshop on Multithreaded Architectures and Applications. Held in conjunction with the IEEE IPDPS-2007. International Parallel and Distributed Processing Symposium. Long Beach, California, March 26-30, 2007 and Miami, Florida, April 14-18, 2008. , Rome, Italy, May 29-31th 2009, Atlanta, USA, April 23th, 2010 and May 20, 2011, Anchorage, USA*

- *ISCC: IEEE International Symposium on Computers and Communications. Cartagena, Spain, June 2005.*

- *CCGrid´2001. "IEEE International Symposium on Cluster Computing and the Grid". Brisbane, Australia, May 2001.*

- *EuroPar. 1995 (Stockholm, Sweden) , 1999 (Toulouse, France), 2000 (Munich, Germany) and 2001 (Manchester, England).*

- *PARLE. "Conference on Parallel Architectures and Languages Europe". 1989 (Eindhoven, The Netherlands), and 1991 (Eindhoven, The Netherlands).*

- *ASAP. IEEE "International Conference on Application Specific Array Processors". 1990 (Princeton, USA), 1992 (San Francisco, USA), 1994 , 1995 (Strasburg, France), 1996 (Chicago, USA), 1997 (Zurich, Switzerland), 2000 (Boston, USA), 2002 (San Jose, USA) and 2003 (Netherlands).*

- *IPPS/SPDP. 1997 (Geneve, Switzerland), 1998 (Orlando, Florida), and 2000 (Cancun, Mexico).*

- *IPDPS. IEEE-ACM International Parallel and Distributed Processing Symposium. 2002 (Fort Lauderdale, USA), 2003 (Nice, France), 2005 (Denver, Colorado) and 2006 (Rhodes).*

- *CASES. International Conference on Computers, Architectures and Synthesis for Embedded Systems. 2001 (Atlanta, Georgia).*

- *SPAA: ACM Symposium on Parallel Algorithms and Architectures. 2001 (Rhodes, Greece).*

- *ACSAC. Asia-Pacific Computer Architecture Conference, Seoul, Korea, 2007.*

- *HiPC. "International Conference on High Performance Computing". 1997 (Bangalore, India), 1998 (Madras, India), 2001 (Hyderabad, India) and 2002 (Bangalore, India).*

- *IEEE "International Conference on Systolic Arrays", 1989 (Killarney, Ireland).*

- *MEDEA. Workshop on "Memory Access Decoupled Architectures and related work". 2001 (Barcelona, Spain), 2002 (Charlottesville, Virginia, USA), 2003 (New Orleans, USA), 2004 (Antibes, France), 2007 (Bucharest, Romania) and 2009, Raleig, North Carolina, USA.*

- *NPC. IFIP International Conference on Network and Parallel Computing. Tokyo, Japan, October 2006.*

- *Workshop on Network Processors, as a part of HPCA: IEEE "International Symposium on High Performance Computer Architecture". 2002 (Anaheim, USA) and 2004 (Madrid, Spain).*

- *MTPP. Conference on Methods and Tools of Parallel Programming of Multicomputers. Pereslav- Zalesky, Russia, Sep. 2-3, 2007.*

- *WASP. Workshop on Application Specific Processors, held in conjunction with IEEE MICRO 2003, San Diego, December 2003.*

- *PDCS. International Conference on Parallel and Distributed Computing Systems. Louisville, Kentucky, USA. September 2002.*

- *VECPAR. "International Meeting on Vector and Parallel Processing Systems and Applications". 1993 (Porto, Portugal), 1996 (Porto, Portugal), 1998 (Porto, Portugal), 2000 (Porto, Portugal), 2002 (Porto, Portugal), 2004 (Valencia, Spain) and 2006 (Rio de Janeiro, Brasil).*

- *CONPAR. "Joint Conference on Vector and Parallel Processing", 1992 (Lyon, France).*

- *ISHPC. "International Symposium on High Performance Computing", 1999 (Kyoto, Japan), 2000 (Tokyo, Japan), 2002 (Nara, Japan) and 2003 (Tokyo, Japan).*

- *Euromicro. 1991 (Viena, Austria), 1995 (Como, Italy), 1996 (Prague, Checoslovaquia) and 2001 (Las Palmas de Gran Canaria).*

- *SBAC-PAD. International Symposium on Computer Architecture and High Performance Computing. 2002 (Vitoria, Brasil), 2003 (Sao Paulo, Brasil), 2004 (Foz Do Iguazu, Brasil), 2005 (Rio de Janeiro, Brasil) and 2007 (Gramado, Rio Grande Do Sul, Brasil), 2008 (Campo Grande, Brasil) and 2010 (Petrópolis, Brasil).*

- *"International Conference on Massively Parallel Computing Systems", 1994 (Ischia, Italy).*

- *MTEAC. Workshop on "Multithreaded Execution, Architecture and Compilation", 2000 (Toulouse, France) and 2002 (Istanbul, Turkey).*

- *IWACT. International Workshop on Advanced Compiler Technology for High Performance and Embedded Processors. Bucharest, Romania, July 18-20, 2001.*

- *International Workshop "Engineering the Grid". SUN, Seconda Università di Napoli. San Leucio, May 2003.*

- *ICPP-HPSECA-03. Workshop on High Performance Scientific and Engineering Computing with Applications. Taiwan, Oct. 6-9, 2003.*

- PDP. *"Euromicro Workshop on Parallel and Distributed Processing". 1994 (San Remo, Italy), 1996 (Braga, Portugal), 1997 (London, England), 1998 (Madrid, Spain), 1999 (Milan, Italy), 2000 (Rhodes, Greece), 2001 (Mantova, Italy), 2002 (Las Palmas de Gran Canaria, Spain), 2003 (Genoa, Italy), 2004 (A Coruna, Spain) and 2005 (Lugano, Switzeland).*

- *Embedded Software Forum at DATE (Design, Automation and Test in Europe). Munich, Germany. March 4-6th. 2003.*

- *PARCO. "Parallel Computer Conference". 1997 (Bonn, Germany), 1999 (Delft, Netherlands), 2001 (Naples, Italy) and 2003 (Dresden, Germany).*

- *DSD: "Euromicro Workshop on Digital System Design". 1998 (Wasteras, Sweden), 1999 (Milan, Italy), 2000 (Maastrich, Netherland), 2004 (Rennes, France)), 2006 (Catvat, Croatia) and 2009 (Patras, Greece).*

- *"Cray-SGI MPP Workshop". Bologna, Italy, 1999.*

- *SympA. "Symposium en Architectures Novelles de Machines". 1999 (Rennes, France), and 2000 (Desanson, France).*

- *IWIA: "International Workshop on Innovative Architecture for Future Generation High-Performance Processors and Systems". Maui, (Hawai, 1998), Big Island (Hawaii, 2002) and Maui (Hawaii, 2004).*

- *International Workshop on Practical Systems and Environments for Parallel and Distributed Computing. In collaboration with PDDTA (Parallel and Distributed Processing Technical Applications). Las Vegas, Nevada. June 25-28, 2001.*

- *IRREGULAR. "Parallel Algorithms for Irregular Structured Problems", 1995 (Lyon, France)*

- *CPC: Workshop on Compilers for Parallel Computations, 1995 (Malaga, Spain).*

- *"Fifth International Conference on Parallel and Distributed Computing and Systems", 1992 (Pittsburg, USA).*

- *PACTA. "Parallel Computers and Transputer Applications", 1992 (Barcelona, Spain).*

- *EWPC "European Workshop on Parallel Computing", 1992 (Sitges, Barcelona).*

- *EDMCC. "Second European Distributed Memory Computing Conference", 1991 (Munich, Germany).*

- *"European Workshop on Hypercube and Distributed Computers", 1989 (Rennes, France).*

- *"European PVM-MPI Users Group Meeting". 1995 (Lyon, France), 1996 (Munich, Germany), 1997 (Krakow, Polland).*

- *CICINDI. "3th. International Conference on Control, Virtual Instrumentation and Digital Systems" México DF. August 27-31th, 2001, October 2004 and November 2007.*

- *SISOFT. Simposio Internacional de Sistemas de Información e Ingenieria del Software en la Sociedad del Conocimineto. Cartagena de Idias, Colombia, 223-25 de Agosto de 2006.*

- *CIC "Congreso Internacional de Supercomputación". 2002 (México DF) and 2005 (Mexico, DF).*

- *"International Conference of the Chilean Computer Science Society", 1995 (Arica, Chile).*

- *HICSS. "Hawaii International Conference on System Sciences". 1994 and 1998.*

- *ICPI. Intelligent Computing in the Petroleum Industry. Acapulco, Mexico. August 9, 2003.*

- *Because European Workshop: "Benchmark of Concurrent Architectures for their use in Scientific Engineering", 1992 (Sophia Antipolis, France).*

- *CISIC. "I Congreso Internacional de la Sociedad de la Información". Las Palmas de Gran Canaria. 27 de Febrero a 1 de Marzo de 2002.*

- *SEID "Simposio Español de Informática Distribuida", 1999 (Santiago de Compostela, Spain), 2000 (Santiago de Compostela, Spain).*

- *SIT. International Symposium on I+T. 2001 (Santiago de Compostela, Spain. July, 18-20) and 2002 (Sevilla, Spain, Septiembre del 2002).*

- *Congreso "Informática y Automática" de la AEIA "Asociación Española de Informática y Automática", 1995 (Las Palmas de Gran Canaria, Spain).*

- *Primeras Jornadas Técnicas sobre Simulación y sus Aplicaciones, 1993 (Vigo, Spain).*

- *Congreso "Informática y Automática" de la AEIA "Asociación Española de Informática y Automática", 1995 (Madrid, Spain).*

- *Simposio Internacional del Conocimiento y su Ingeniería. Congreso de la AEPIA, 1985 (Madrid, Spain).*

- *Member of the Advisory Board for the "Universidad de Verano Campus TI". Valencia, Spain, July 2002 and July 2003.*

3.5 Session Chair.

- *Panel session: "Future of the HPC". ACM, ICS, International Conference on Supercomputing. Beijing, June 15th. 2018.*

- *Chairman de la sesión: "De los Datos a la Información". XVII Jornadas de la Fundación Circulo de Tecnologías para la Defensa y la Seguridad. Museo Casa de la Moneda. Madrid, October 25th, 2016.*

- *Chairman of the panel session: "Why Connexion to Industry is Critical for Research in Scientific Computing". JLEC Workshop. Joint INRIA-ANL-UIUC-BSC Workshop. Chicago, Nov. 24-26, 2014.*

- *Chairman of the Inaugural Session of the PRACE days 14. Barcelona, May 21th, 2014.*

- *Panel Session. Joint meeting Brasil and EU collaboration on HPC. Porto de Galinha, Brasil. October 25, 2013.*

- *Panel session. EU-Russia HPC Workshop. Sant Peterburg, Russia. September 30th, 2013.*

- *Panel session "Discussion for collaboration between EU and China on Supercomputing". First Europe- China HPC Conference. Sofia, Bulgaria, April, 9th, 2013.*

- *Chairman of the Inaugural Session of the EESI Conference. The Europen Exascale Software Initiative. Barcelona, October 10th-11th, 2011*

- *Chairman of the Ceremony presenting the Gordon Bell Awards. Chairman of the Committee. ACM, SC09, Supercomputing Conference. Portland, USA, November 19th, 2009*

- *Session where the papers finalist to the Gordon Bell Award were presented. ACM, SC09, Supercomputing Conference. Portland, USA, November 18th, 2009.*

- *Working day RAI Coordinator, Real Academia de la Ingeniería de España, about "High Performance Computing". Madrid, October 23rd 2007.*

- *Panel session "Architecture for Manycore". The Manycore Computing Workshop, organized by Microsoft. Seattle, June 20-21, 2007.*

- *Keynote Talk at ICS 2007. "Harnessing Massive Paralellism in the era of Paralellism for the Masses". Craig Stunkel. ICS, IEEE-ACM International Conference on Supercomputers. Seattle June16-20, 2007.*

- *Keynote talk at ISCA33. "Computer Architecture Research and Future Microprocessors: Where do we go from here? Yale Patt University of Texas at Austin. The 33th IEEE/ACM International Conference on Computer Architecture. Program Chiar, Boston 17-21 June 2006.*

- *Talk coordinator on TIC Communication and Technologies organized by the Royal Academy of Engineering of Spain, Madrid 25th January 2006.*

- *Keynote talk at Micro38. "The Future Evolution of High-Performance Microprocessors" by Norm Jouppi, HP Labs. The 38th. IEEE/ACM International Symposium on Microarchitecture. Program Chair. Barcelona, 12-16 November 2005.*

- *Session on "e-Science at CERN". Jornadas españolas de e-Ciencia. Santiago de Compostela, Spain. July 9, 2005*

- *Session on "e-Science Programme in the UK". Jornadas españolas de e-Ciencia. Santiago de Compostela, Spain. July 9, 2005*

- *Session on "European Infrastructures for E-Science". Jornadas españolas de e-Ciencia. Santiago de Compostela, Spain. July 9, 2005.*

- *"Virtual Machines: Supporting Changing Technology and New Applications". Keynote lecture at CF05 given by Professor Jim Smith. Ischia, Italy. May 4th., 2005*

- *Moderator of the Workshopn on "E-Science in Spain", organized by FECyT, the spanish Foundation for Science and Technology. Madrid, October 8th, 2004.*

- *"Breaking Down the Memory Wall for Scalable Microprocessors Platforms". Keynote lecture at PACT gyven by Professor Wen-mei Hwu. Antibes, France, October 1st 2004.*

- *"Microarchitectural Concepts". ISCA. IEEE-ACM "International Conference on Computer Architecture". Munich, June 2004.*

- *Special session on "Memory Wall". CF-2004, ACM Conference on Computing Frontiers, Ischia, Italy. April 14th-16th. 2004.*

- *Session on "Network Processors and Interfaces". WASP: Workshop on Application Specific Processors". Held in conjunction with Micro-35. Istanbul, Turkey, November, 2002.*

- *Session on "Vector Processors". ISCA-2002, International Symposium on Computer Architecture, Anchorage, Alaska, May, 25, 2002.*

- *Session on "ILP Issues". WMPI-2002. Annual Workshop on Memory Performance Issues. ISCA-2002, International Symposium on Computer Architecture, Anchorage, Alaska, May, 25, 2002.*

- *Session on "The net and digital libraries". Universidad Internacional Menéndez y Pelayo. Seminar about HAL9000. Valencia, October 18, 2001.*

- *Invited Talk at PACT-2001. Joel Emer, "EV8: The Post Ultimate Alpha" Barcelona Sept. 12th, 2001.*

- *"Best Papers Session". International Conference on Parallel Processing, Valencia, Spain. Sept. 3-7, 2001.*

- *"Multithreading and Speculation". Session 1. ISCA-28. IEEE and ACM, "International Conference on Computer Architecture". Goteborg, Sweden. June 28-July 4th. 2001.*

- *"MP Memory Hierarchies". Workshop on Memory Performance Issues. Goteborg, Sweden. June 30-July 1, 2001.*

- *"Memory Models". Session 1. ICS-15. IEEE-ACM "International Conference on Supercomputing". Sorrento, Italy, June 18-21th. 2001.*

- *"Code Generation Techniques". Session 4. IEEE-ACM HPCA-7. International Conference on High Performance Computers. Monterrey, Mexico, 20-22th. January 2001.*

- *"Invited Talks at the ISHPC Conference". International Symposium on High Performance Computing. Tokyo, October 18, 2000.*

- *"Tutorial on OpenMP". International Symp. on High Performance Computing. Tokyo, Oct. 19, 2000.*

- *"The Future of Simultaneous Multithreaded Architectures". Panel Chair. Workshop on Multithreaded Execution, Architecture and Compilation. HPCA-6. IEEE-ACM. International Conference on High Performance Computer Architecture. Toulouse, France, 10-12th January 2000.*

- *"Prediction I". HPCA-6. IEEE-ACM. International Conference on High Performance Computer Architecture. Toulouse, France, 10-12th January 2000.*

- *"Low Power Enhancements". Micro-32. IEEE-ACM "International Symposium on Microarchitecture". Haifa, Israel. 16-19th November 1999*

- *"Architecture-Driven Compilation". PACT99. IEEE. "Parallel Architectures and Compiler Technology". Newport Beach, California, 15th October 1999.*

- *"Instruction-Level Parallelism: Branch Prediction and Predication". EuroPar99. Toulouse, 1-3th September 1999.*

- *"Instruction Level Distributed Processing". Invited Lecture. ICS-13. IEEE-ACM "International Conference on Supercomputing". Rhodes, 22th June, 1999.*

- *"How Conferences about High Performance Computers should be organized?". Moderator of the Panel Session. ISHPC "International Symposium on High Performance Computing". Nara, Japón, May 28th 1999.*

- *"Performance Enhancements". HPCA-5. IEEE "International Symposium on High Performance Computer Architectures". Orlando, Florida. 9-13th January 1999.*

- *"Current projects and the future of HPCN in the FP5". ARCADE meeting (European Supercomputers Centres Meeting). Paris, Francia 20th November 1998.*

- *"Branch and Value Prediction". PACT´98. IEEE-ACM "Parallel Architectures and Compilation Techniques". Paris, 13-15th October 1998*

- *"Architecture". ICS-98. IEEE-ACM "International Conference on Supercomputing". Melbourne, 17th July 1998.*

- *Chairman Opening Session. ISCA-98. IEEE-ACM "International Symposium on Computer Architecture". Barcelona, June 1998.*

- *"Interconnection Networks". 6th Euromicro Workshop on Parallel and Distributing Processing. Madrid, January 21-23th 1998.*

- *"ILP Optimization and Code Scheduling". PACT'97. IEEE-ACM "Parallel Architectures and Compilation Techniques". San Francisco, 10-14th November 1997*

- *"Parallel Architectures". ICS-11 IEEE-ACM "International Conference on Supercomputing". Vienna, July 1997*

- *"Architecture". IPPS-11. IEEE-ACM "International Parallel Processing Symposium ", Ginebra, 1-5 April, 1997.*

- *"Computer Architecture II". XV International Conference of the Chilean Computer Science Society. Arica (Chile), 1-3 November 1995.*
- *ICS-9. IEEE-ACM "International Conference on Supercomputing". Chairman, Opening Session. Barcelona, July 1995.*
- *"High Performance Computing Applications". High Performance Computing: Current Trends and Applications. Barcelona, November 1994.*
- *"Cache Coherence I" ICS-8. IEEE-ACM "International Conference on Supercomputing". Manchester, England. July 11-15th 1994.*
- *"Evaluation of Parallelization Techniques". Keynote Talk. Euromicro Workshop on Parallel and Distributed Processing. Málaga, 26-28 January 1994.*
- *"Euromicro 93". Chairman Opening Session. Barcelona, 7 September 1993.*
- *"Parallel Architectures". High Performance Computing Forum. European Commett Initiative. Patras, 29-30th May 1993.*
- *"Architecture I". Parallel Architectures and Languages Europe (PARLE-92). París, June 1992.*
- *"Supercomputers: Current Status". Panel session. Workshop on Supercomputers. Centro Informático Científico de Andalucía. Sevilla, 3-5 December 1991.*
- *"Supercomputers Centres in Spain". Spanish society on Numerical Methods. Workshop on Supercomputers and Networking. Barcelona, 21 November 1991.*
- *"Linear Algebra in Systolic Arrays". Minisimposium. "Second SIAM Conference on Linear Algebra". San Francisco, November 3-5th 1990.*
- *"Linear Algebra in Multiprocessor Systems". Minisimposium "Second SIAM Conference on Linear Algebra". San Francisco, November 3-5th 1990.*
- *"VLSI implementation". V European Signal Processing Conference. Barcelona, Sept. 1990.*
- *"Partial Differential Equations". 1st. European Workshop on Hypercube and Distributed Computers. Rennes, October 1989.*
- *"Systolic Algorithms". PARLE "Parallel Architectures and Languages Europe". Eindhoven, June 1989.*
- *"Software Issues". International Conference on Systolic Array Processors. Killarney, Ireland, June 1989.*
- *"Multiprocessor Systems". International Symposium on Mini and Microcomputers, París, June 1982.*
- *"Distributed Systems". SEIR-2. Santiago de Compostela. September 1982.*
- *"Computer Architecture and Operating Systems". XII Spanish Meeting on Operational Research. Jaca, September 1980.*

4. Projects

4.1 CICYT (Spanish National Science Foundation)

- *"High Performance Computing VI: Architectures, Compilers, Operating Systems, Tools and Algorithms". Proyecto de la CICYT. Responsable. TIN2015-65316-P. Años 2016-2019.*
- *"High Performance Computing VI: Architectures, Compilers, Operating Systems, Tools and Algorithms". Proyecto Consolider de la CICYT. Responsable. TIN2012-34557. Años 2012-2015.*

- *"High Performance Computing V: Architectures, Compilers, Operating Systems, Tools and Algorithms". Proyecto Consolider de la CICYT. Responsable. TIN2007-60625. Años 2007-2011.*
- *"Supercomputación y eCiencia". Proyecto Consolider del Programa Ingenio-2010. Responsable. CSD2007-00050, años 2007-2011.*
- *"High Performance Computing IV: Architectures, Compilers, Operating Systems, Tools and Algorithms". Responsable. TIN2007-60625. Años 2007-2011.*
- *"High Performance Computing IV: Architectures, Compilers, Operating Systems, Tools and Algorithms". Responsible. TIN2004-07739-C02-01. Years 2004-2007*
- *Coordination of the network on Computer Architecture and Supercomputers. TIC2002-10332-E. Years 2003-2004.*
- *"High Performance Computing III: Architectures, Compilers, Operating Systems, Tools and Algorithms". Responsible, TIC2001-0995. Years 2002 to 2004.*
- *"High Performance Computing: Architectures, Compilers, Operating Systems and Compilers". Responsible. TIC98-0511. Years 1998 to 2001.*
- *"High Performance Computing". Responsible. TIC95-0429. Years 1995-1998.*
- *"Support to access CEPBA supercomputing facilities". Co-responsible. Years 1996 - 2003 (7 grants).*
- *"Architectures and Compilers for Supercomputers". Responsible. TIC880-92. Years 1992-1994.*
- *"Parallelism Exploitation in High Speed Architectures". Researcher. Years 1989-1992.*
- *"Design of High Speed and Low Cost Parallel Architectures". Responsible. PA85-0314. Years 1986-1989.*
- *"Architectures for Digital Signal Processing". Responsible. PA2906-83. Years 1984-1985.*
- *"Design and Evaluation of Interconnection Networks for Multiprocessors". Responsible. PA1739-82. Years 1982-1983.*

4.2 CIRIT (Catalan National Science Foundation)

- *Responsible for the "Qualified Research Team" on High Performance Computers. Years 1993 to 2004 (5 grants) and Years 2009-2013.*
- *Support to the CEPBA activities. Co-responsible. Years 1995 to 2003 (9 grants).*

4.3 European Projects

- *RoMoL, Riding on Moore's Law. EU-FP7 "Ideas" European Research Council. ERC-321253. Years 2013-2018.*
- *HiPEAC-3. Network of Excellence on High Performance Embedded Architectures and Compilers. EU-FP7. ICT- 287759. Years 2011-2014.*
- *RISC, Red Iberoamericana de Supercomputación. EU-FP7. ICT-288883. Coordinator. Years: 2011-2014.*
- *HiPEAC-2. Network of Excellence on High Performance Embedded Architectures and Compilers. IST217068. Years 2007-2011.*
- *SARC: "A Performance System for Scalable Computing". IST project. Co-responsible, 2005-2008.*
- *DEISA. Distributed Europen Infrastructures for Supercomputer Applications. IST project. Co-responsible, 2005-2008.*

- *HiPEAC. Network of Excellence on High Performance Embedded Architectures and Compilers. IST- 004408. Years 2004-2008.*
- *Access to CESCA and CEPBA largE Scale facilitieS-2. Contract number: HPRI-2001-CT-0011. Años 2002 a 2004. Access to CESCA and CEPBA largE Scale facilitieS-2. Contract number: HPRI-2001-CT-0011. Años 2002 a 2004*
- *ACCESS: Access to the Large Scale Facility on Supercomputers CESCA-CEPBA. Responsible. Years 2001 to 2003.*
- *CIRI. Colaboración entre IBM y CEPBA. Responsable. Años 2000 a 2004.*
- *MHAOTEU. Co-responsible. ESPRIT Long-Term Research project. Years 1998-2001.*
- *CEPBA Technology Transfer Node. Co-responsible Years 1997-2000.*
- *NANOS. Co-responsible. ESPRIT Long-Term Research Project. 1996-1999.*
- *PROMENWIR. Co-responsible. ESPRIT IT Project Years 1996-1998.*
- *PARALIN Co-responsible. European collaboration with Latin-america. Years 1996-1997.*
- *PARANDES. Co-responsible. European collaboration with Latin-america. Years 1996-1997.*
- *CORDIAL.Co-responsible. European collaboration with Latin-america under the Alfa Program. Years 1996-1997.*
- *Training and Mobility of Researchers. Years 1995 to 2000.*
- *PECOS. Human Capital and Mobility Program for European East Countries. Years 1994-1995.*
- *PACOS: Parallel Computing Initiative for Spain. Co-responsible. European Parallel Computing Initiative. Years 1994-1996.*
- *PCI-II. Parallel Computing Initiative (PCI) for Spain (PACOS) and for Italy (CAPRI). ESPRIT 9602. Co-responsible. March 1996 to October 1998*
- *Human Capital and Mobility Program. Years 1993-1996.*
- *APPARC. "Performance-Critical Applications of Parallel Architectures". Responsible. ESPRIT Long-Term Research Project. Years 1992-1995.*
- *IDENTIFY. "Interactive Design in Fluids using a Network of Transputers". IT European ESPRIT Project. Co-responsible. Years 1992-1994.*
- *SHIPS. "Supercomputer Highly Parallel system". IT European ESPRIT Project. Co-responsible. Years 1992-1994.*
- *SEPIA "Software for Multiprocessor Systems with Distributed Memory". European Basic Research Project. Responsible. Years 1989-1990.*
- *GENESIS. European Supercomputer. Responsible. IT European ESPRIT Project. Years 1990-1992.*
- *Supernode II. IT European ESPRIT Project. Co-responsible. Years 1989-1991.*
- *Technology for ATD. European IT RACE Project. Co-responsible. Years 1988-1991.*
- *"Systolic Array Processors Oriented to Matrix Operations". Responsible. Contract with Telefonica. Year 1986.*
- *"Software for Digital Signal Processing". European Space Agency. Co-responsible. Year 1986.*

4.4 USA Projects

- *"Many-core Architectures". Microsoft. Responsible. Years 2006-2007.*

- "Operating Systems for Simultaneous Multithreading processors". Intel Corporation, Year 2005
- "Heterogeneity-Aware Multithreading Architectures". Intel Corporation. Year 2005
- "Register File Organization for Future Superscalar Processors". Intel Corporation. Responsible. Year 2004.
- "Power-PC VMX-Vector Architecture". IBM Corporation. Year 2003.
- "High performance Fetch for SMT Processors". Intel Corporation. Co-responsible. Yeas 2003 and 2004..
- "Advanced Optimizations Enabled by the Stream Predictor". Intel Corporation. Co-responsible. Years 2003 and 2004.
- "Fast-Packets Buffers". IBM, Haifa Grant. Co-responsible, 2002-2003
- IPF Grant. Intel-HP Itanium Processor Family University Grant Program. Co-responsible, 2001-2002.
- "Research on VLIW compilers and architectures". Hewlett Packard. Co-responsible. Year 2002.
- "Memory Architecture for Multithreaded Processors". Intel Corporation. Co-responsible. Years 2001 and 2002.
- "Speculative Vector Processors". Intel Corporation. Co-responsible. Years 2001, 2002 and 2003.
- "Semantic Gap". Intel Corporation. Co-responsible. Years 2000, 2001 and 2002.
- "Smart Registers". Intel Corporation. Co-responsible. Years 2000, 2001 and 2002.
- "UPC-USA Universities Collaboration". Fulbright. Years 1999-2001.
- "Data Distribution Tool". Convex Corporation. Co-responsible. Years 1992-1995.

5. Publications: Full List

5.1 Conferences

- *Master conference "La Supercomputación Inteligencia Artificial para el Desarrollo" and "Presentación de Índices Globales" from Portulans Instute, organized by the Chamber of Deputies from Comisión de Ciencia, Tecnología e Innovación. Theme: "2021, Año de la Independencia y la Grandeza de México". Ciudad de México, December 8th, 2021.*

- *Round table Panel: "Innovation as an instrument to transform the world" STS forum – Latin America and the Caribbean High-Level Conference "Science and Technology Diplomacy". Mexico – Japan, Ciudad de México, December 8th, 2021.*

- *Conference Cycle: "Desafios del s. XXI" organized by El Mundo and Actualidad Económica with the collaboration of "La Caixa" Foundation. Talk: "Supercomputación y retos del futuro". Madrid, December 2nd, 2021.*

- *SEDIA press conference on "Lenguaje natural". Round table with the Director of the National Library, Secretary of State for Digitalization and Artificial Intelligence and project technicians. Madrid, November 11, 2021.*

- *IGISC 2021 Round table "¿Por qué es importante la Supercomputación? Una apuesta por la sostenibilidad: IA ciudades inteligentes". Ciudad de México, November 3th, 2021.*

- *CITIUS Conference "Revolution in High Performance Computing to accelerate scientific progress". Santiago de Compostela, October 29, 2021.*

- *"PrioRAT: Criticality-Driven Prioritization Inside the On-Chip Memory Hierarchy", 27th International European Conference on Parallel and Distributed Computing (Euro-Par), to be published at the end of 2021. Vladimir Dimić, Miquel Moretó, Marc Casas and Mateo Valero.*

- *"VIA: A Smart Scratchpad for Vector Units With Application to Sparse Matrix Computations presented in the 27th IEEE International Symposium on High-Performance Computer Architecture , HPCA2021, Seoul 2021 Feb-Mar 2021. J.Pavon, I. Vargas, A. Barredo, J. Marimon, M. Moreto, F. Moll, O. Unsal, M. Valero, A. Cristal.*

- *"Runtime-Guided ECC Protection Using Online Estimation of Memory Vulnerability"(pap393s4),to The International Conference for High Performance Computing, Networking, Storage, and Analysis (SC20- Supercomputing Conference 2020).*

- *"An Academic RISC-V Silicon Implementation Based on Open-Source Components"35th Conference on Design of Circuits and Integrated Systems (DCIS), Segovia (Spain), November 18 - 20 2020 J. Abella, G. Cabo, F J. Cazorla, A. Cristal, M. Doblas, R. Figueras, A. Gonzalez, C. Hernandez, C.Hernandez, L. Kosmidis, V. Kostalabros, R. Langarita, N. Leyva, G. Lopez-Paradis, J. Marimon, R. Martinez, J. Mendoza, F. Moll, M. Moreto, J. Pavon, C. Ramirez, M. A. Ramirez, C. Rojas, A. Ruiz, A. Rubio, N. Sonmez, V. Soria, L.Teres, O. Unsal, M. Valero, I. Vargas, L.Villa*

- *Improving accuracy and speeding up Document Image Classification through parallel systems. Paper in ICCS 2020. J.Ferrando, JL. Domínguez, J.Torres, R. García, D. García, D. Garrido, J. Cortada and M. Valero*

- *"Improving Predication Efficiency through Compaction/Restoration of SIMD Instructions", in the 26th IEEE International Symposium on High-Performance*

Computer Architecture (HPCA), 2020. A.Barredo, JM. Cebrian, M. Moreto, M. Casas, and M. Valero

- *RICH: Implementing Reductions in the Cache Hierarchy, ICS 2020. V. Dimić, M.Moretó, M. Casas, J. Ciesko and M.*

- POSTER: An Optimized Predication Execution for SIMD Extensions. <u>PACT 2019</u>: 479-480. *23-26 Sept. 2019*. A. Barredo, JM. Cebrian, M. Moretó, M. Casas, M. Valero

- *A Vulnerability Factor for ECC-protected Memory. <u>IOLTS 2019</u>: 176-181. 1-3 July 2019 L. Jaulmes, M. Moretó, M. Valero, M. Casas*

- *Optimizing Computation-Communication Overlap in Asynchronous Task-Based Programs. ACM, International Conference on Supercomputing (ICS), Phoenix, USA, June 2019. E. Castillo, N. Jain, M. Casas, M. Moreto, M. Schulz, R. Beivide, M. Valero, A. Bhatele.*

- *Power Efficient Job Scheduling by Predicting the Impact of Processor Manufacturing Variability. ACM, International Conference on Supercomputing (ICS), Phoenix, USA, June 2019. D. Chasapis, M. Casas, M. Moretó, M. Schultz, B. Rountree, M. Valero*

- *Memory Vulnerability: A Case for Delaying Error Reporting. 12th International Workshop on Programmability and Architectures for Heterogeneous Multicores (MultiProg), 2019. MP-19. L. Jaulmes, M. Moreto, M. Casas and M. Valero*

- *Optimizing computation-communication overlap in asynchronous task-based programs: poster. PPoPP 2019, pp. 415-416, Washington DC, USA. [PPoPP'19] E. Castillo, N. Jain, M. Casas, M. Moreto, M. Schulz, R. Beivide, M. Valero, A. Bhatele.*

- *Runtime-Assisted Cache Coherence Deactivation in Task Parallel Programs. International Conference for High Performance Computing, Networking, Storage and Analysis (SC), Dallas, USA, Nov 2018. P. Caheny, Ll. Alvarez, M. Valero, M. Moretó, and M. Casas.*

- *Stencil Codes on a Vector Length Agnostic Architecture. International Conference on Parallel Architectures and Compilation Techniques (PACT), Limassol, Cyprus, Nov 2018. A. Armejach, H. Caminal, J. M. Cebrian, R. Gonzalez-Arbequilla, C. Adeniyi-Jones, M. Valero, M. Casas, and M. Moretó.*

- *Reducing Data Movement on Large Shared Memory Systems by Exploiting Computation Dependencies. To be presented in ICS'18 Conference. June 2018. I. Sánchez, M. Casas, M. Moreto and M. Valero.*

- *Runtime-Guided Management of Stacked DRAM Memories in Task Parallel Programs. To be presented in ICS'18 Conference. June 2018. L. Alvarez, M. Casas, M. Moreto and M. Valero.*

- *Graph partitioning applied to DAG scheduling to reduce NUMA effects. Presented in the 23rd ACM SIGPLAN Symposium on Principles and Practice of Parallel Programming. PPoPP'18. February 2018.*

- *"Architectural Support for Task Dependence Management with Flexible Software Scheduling". Presented in HPCA February 2018 in Vienna. E. Castillo, L. Alvarez, M. Moreto, M. Casas, R. Beivide, E. J.L Bosque and M. Valero.*

- *A machine learning approach for perfomance prediction and heterogeneous CPU scheduling. Presented in the International Symposium on Computer Architecture and High Perfomance Computing (SBAC-PAD), October 2017. D. Nemirovsky, T. Arkose, N. Markovic, O. Unsal, A. Cristal and M. Valero.*

- *"A Deep learning mappers (DLM) for Heterogenous Scheduling". Presented at Latin Amercia High Perfomance Computing Conference (CARLA), september 2017, to be*

published in Cluster Computing: Special Issue Carla 2017. D. Nemirovsky, T. Arkose, N. Markovic, O. Unsal, A. Cristal and M. Valero.

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5.3 Books

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6. Invited Talks: Full List

- *Panel Discussion and talk "Conferencias formación de ingenieros (Real Academia de Ingeniería y Academia de Ingeniería de México) El mundo del futuro y la ingeniería. Invited talk : "La Supercomputación, Herramienta Esencial para el Avance de la Ingeniería". Madrid-México, 27th September 2021*

- *Keynote talk at Ciclo de conferencias del CNB "Revolution in High Performance Computing to accelerate scientific progress" . Barcelona, 17th September 2021*

- *Keynote talk at ACM Summer School "Revolution in High Performance Computing to accelerate scientific progress" . Barcelona, 3rd September 2021*

- *Plenary talk at 35º Encuentro de la Economía Digital y las Telecomunicaciones "Revolution in High Performance Computing to accelerate scientific progress" . Santander, 3rd September 2021*

- *Keynote talk HPC Summer School Colombia21 "The Future is wide open". 12th July 2021*

- *Panel Discussion: The Future of High-Performance Computing (HPC). EU-ASEAN High-Performance Computing Virtual School 2021. 09th July 2021.*

- *Plenary talk in the 47th EPS plasma conference: "Revolution in High Performance Computing to accelerate scientific progress". 23rd June 2021.*

- *Invited talk in "la Conferencia Anual de la Real Academia de Ciencias Exactas, Físicas y Naturales". "BSC: Tecnologías de la información y las comunicaciones uadas en la lucha contra la Covid-19". 2nd June 2021.*

- *Roundtable about "Executive Education & Business Challenges Forum- Barcelona as a digital HUB."- UIC Barcelona 05th May 2021.*

- *Invited talk "Barcelona Supercomputing Center" en Encontro 94, de la Asociación de Empresarios Gallegos de Cataluña. 26th April 2021*

- *Roundtable about "Barcelona Innovation Coast- un espai d'impuls i coordinació de la innovació al servei de la ciutat". 15th April 2021*

- *Roundtable about "Retos de una Sociedad Digital" in Grupo Balear del Capítulo Español del Club Roma. 24th March 2021.*

- *Invited talk in Oryzon Genomics "Barcelona Supercomputing Center". 16th March 2021*

- *Talk about "El cuidado de la intimidad en el universo big data" Fundació "la Caixa" in the Palau Macaya, 11th March 2021.*

- *Cycle of webinars Mexico-Cataluña on science and technology: "La Supercomputación al servicio de la investigación y la educación". 25 February 2021*

- *Roundtable with Salvador Illa (Presidente PSC Catalunya), la ministra Reyes Maroto (ministra de Industria, Comercio y Turismo) y Matias Carnero (sindicalista de Seat) at the PSC headquarters in Barcelona. 3rd February 2021.*

- *Participation in the webinar de "Los retos de la ciencia en España: de la vacuna al chip" organized by the Real Academia Europea de Doctores. 26th January 2021.*

- *Invited talk "Barcelona Supercomputing Center, una herramienta para la ciencia" in "IV Encuentro Jovenes Investigadores en el Extranjero", 23rd December 2020.*

- *Keynote "The Future HPC Will be Open" in the HPCC-SmartCity-DSS-DependSys-2020, December 14th 2020.*

- *Comparecencia Comisión Economía del CES (Consejo Económico Social) "The Barcelona Supercomputinc Center" el 30 de noviembre de 2020.*

- *Participation in an unprecedented project in the history of the Center for Genomic Regulation (CRG), together with professionals from different sectors to discuss the CRG's current research topics and possible future directions. 20th October 2020*

- *Presentation at the project meeting Next Generation en el Grupo de Microelectrónica: "The Future HPC will be open". 27th October 2020*

- *Round table in "53a Junta Directiva de CINDA (Centro Universitario de Dessarolo) en el diálogo "Creatividad y tecnología para hacer frente a la pandemia"". October 15th 2020*

- *Opening talk of the "curso académico 2020-2021 de la UIC (Universitat Internacional de Catalunya): "La supercomputación y la inteligencia artificial como herramientas fundamentales para la ciencia y la ingeniería". October 8th 2020*

- *Invited talk in CICOS (Congreso Internacional de Cómputo en Optimización y Software): "Desarrollo del BSC en los últimos 6 años y el Diseño de las Arquitecturas Computacionales en el futuro". October 7th 2020*

- *Round table in BNEW (Barcelona New Economy Week)en BINDUSTRYNeural Networks, Artificial Intelligence and Robotic en el en Edificio Movistar Centre. October 7th 2020*

- *Invited talk in Russian Supercomputing Days Congress "BSC: The future HPC will be open". 22nd September 2020.*

- *Invited Talk Mateo en Workshop on HPC Collaboration between Europe and Latin America. #CARLA2020. 11st September 2020.*

- *Participación en la mesa redonda de la RedClara para el Panel La Inteligencia Artificial en el soporte de la universidad inteligente.01 de septiembre 2020*

- *Participación en el Seminario de Tecnologías digitales en la gestión de la COVID-19 organizado por la UIMP(Universidad Internacional Menendez Pelayo): "BSC contra la COVID-19". 31 de agosto 2020.*

- *Participación en el Curso de Verano del CESEDEN (Centro Superior de Estudios de la Defensa Nacioanl) sobre "Tecnologías relacionadas con la COVID-19: dependencia y capacidades estratégicas españolas". 7 de julio 2020*

- *Panel discussion about "Digital futures: Will AI and data transform post-crisis healthcare?" organized by Science business. June 30th, 2020.*

- *Participación en la Jornada de Redes de Conocimiento para la nueva empresa organizada por la Fundación Internacional ORP. 11 de Junio de 2020.*

- *BSC: Supercomputación al servicio de la investigación y de la educación. Invited lecture. Congreso "Universidad 2020". La Habana, Cuba. Feb.13th., 2020.*

- *RISC-V and open hardware: opportunity and challenge for the EU. Invited lecture. Meeting de la red española sobre RISC V. Universidad Autónoma de Barcelona. Escuela de Ingenieria. Barcelona, 6 de Febrero de 2020.*

- *Roadmap for HPC in Europe. Invited presentation. European Comission. Brussels, January 22th, 2020.*

- *"BSC: Supercomputación generadora de riqueza". Invited lecture. Opening of a new supercomputer at the "National Laboratory for High Perfromance Computing". Chile. Santiago de Chile, January 15th, 2020.*

- *"BSC: Supercomputación al servicio de la investigación y de la educación". Invited lecture. Congreso "Universidad 2020". La Habana, Cuba. Feb.13th., 2020.*

- *RISC-V and open hardware: opportunity and challenge for the EU. Invited lecture. Meeting de la red española sobre RISC V. Universidad Autónoma de Barcelona. Escuela de Ingenieria. Barcelona, 6 de Febrero de 2020.*

- *Roadmap for HPC in Europe. Invited presentation. European Comission. Brussels, January 22th, 2020.*

- *"BSC: Supercomputación generadora de riqueza". Invited lecture. Opening of a new supercomputer at the "National Laboratory for High Perfromance Computing". Chile. Santiago de Chile, January 15th, 2020.*

- *BSC: Past, Present and Future. Invited lecture. "Ministerio de Ciencia, Tecnología, Conocimiento e Innovación". Chile. Consejo Nacional de Innovación para el Desarrollo (CNID). Santiago de Chile, January 15th, 2020.*

- *BSC: Past, Present and Future. Invited lecture. "Ministerio de Ciencia, Tecnología, Conocimiento e Innovación". Chile. Santiago de Chile, January 15th, 2020.*

- *Supercomputación e Inteligencia Artificial. Invited lecture. Congreso Futuro 2000. Chile. Santiago de Chile, January 14th, 2020.*

- *The Future will be open!!!!. Invited lecture. RIAG, the EuroHPC Research &Innovation Advisory Group. Brussels, Dec.12th, 2019.*

- *La Supercomputación: Un Instrumento para la Ciencia. Sesión de Clausura de la Real Academia de Medicina de Zaragoza. Invited lecture. Zaragoza, Dec, 5th. 2019.*

- *La Supercomputación: Motor para la Ciencia y la Ingeniería. Keynote lecture. XXVII Congreso COPIMERA, Conferencia Panaamericana de Ingeniería Mecánica, Eléctrica, Industrial y Ramas Afines. Cancun, México. Nov, 22th, 2019.*

- *BSC and the Future European Processor. Invited Talk. RES, Red Española de Supercomputación. Zaragoza, September 18th, 2019.*

- *BSC: Past, Present and Future. Keynore lecture. FPL, The International Conference on Field-Programmable Logic and Applications. Barcelona, Spain, Sept 9th. 2019*

- *BSC and the European Processor. Invited lectute. AMETIC. Summer School. Universidad Internacional Menéndez Pelayo. Santander, España, Sept. 2th, 2019.*

- *From Classical to Runtime Aware Architectures and Beyond. Academic Investiture Speech. Academia de Ingeniería de México. México DF, June 14th. 2019.*

- *"La Supercomputación y sus aportaciones a Ciencia e Ingeniera". <<Sesión: Aprendizaje de Máquinas, Inteligencia Artificial y Supercomputación>> .UNAM. México DF, June 13th, 2019.*

- *BSC at a Glance. Invited lecture. University of Zagreb. FER, Faculty of Electrical Engineering and Computing. Zagreb, Croacia, June, 3th, 2019.*

- *From Classical to Runtime Aware Architectures and Beyond. Invited lecture. University of Zagreb. FER, Faculty of Electrical Engineering and Computing. Zagreb, Croacia, June, 3th, 2019.*

- *Barcelona Supercomputing Center: past, present and future. Invited Lecture. Facultad de Naútica de la Universidad Politécnica de Barcelona. Celebración del 250 aniversario. Barcelona, May 29th., 2019.*

- *La Supercomputación: Motor para la Ciencia y la Ingeniería. Invited lecture. Universitat Ramon Llull. Entrega de los premios de investigación a estudiantes de bachillerato. Barcelona, May22th, 2019.*

- *Pilots and system-level co-design: The BSC vision. Invited talk. EuroHPC Summit Week. Poznan, Polland. May 15th. 2019.*

- *BSC: Presente, pasado y futuro. Invited lecture. Tertulia Sopars de Girona. Barcelona, May 8th 2019.*

- *BSC proposals for the use of HPC in the Mediterranean countries. Invited presentation. Digital5med Conference. European Comission. Brussels, April 8th, 2019.*

- *BSC Roadmap for HPC in Europe. Invited lecture. ISS, Industry Strategy Symposium. Organized by SEMI. Milano, italy, April 2th, 2019.*

- *BSC Roadmap for HPC in Europe. Keynote Talk. ISUM-2019. X International Supercomputing Conference in Mexico. Monterrey, México. March, 28th, 2019.*

- *Somos Investigadoras. Invited Talk. ISUM-2019. X International Supercomputing Conference in Mexico. Monterrey, México. March, 27th, 2019*

- *BSC y lo que la supercomputación puede hacer por nostros. Invited Talk. X Aniversario de CénitS-COMPUTAEX. Cácerés, Sapin, March 22th, 2019.*

- *La Supercomputación: Motor para la Ciencia y la Ingeniería. Invited Lecture. ATENEO de la EINA, sesión especial 3300. Zaragoza, March, 20th, 2019.*

- *BSC: Presente, pasado y futuro. Invited Talk. Programa de desarrollo de talento hacia el 2020. Fujitsu. Madrid, March, 2019.*

- *Mobiles: on the road to Supercmputers. Invited talk. GoingDigital Community. MWCongress. Barcelona, Feb. 28th, 2019.*

- *HPC Perspectives and Challengues for Europe. Invited Talk. CIC, Centro de Investigación en Computación del IPN, Instituto Politécnico Nacional. Ciudad de México, February 6th, 2019.*

- *Barcelona Supercomputing Center: past, present and future. Invited Lecture. DIPC INternatinall Colloquia. Donostia International Physics Center. Donostia, January 11th, 2019.*

- *HPC Perspectives and Challengues for Europe. Keynote Talk. ICT 2018: HPC & the Future of Computing. ICT 2018: Imagine Digital-Connect Europe. Vienna, Austria, Dec.4-6th, 2018.*

- *Presentación de Francisco González, Presidente del BBVA en el Premio "Los Leones." A la mejor gestión empresarial. Este premio es concedido por el Periódico "El Español". Madrid, October 25th, 2018.*

- *BSC European Exascale Plans. Invited Talk. Workshop: A technology roadmap for European Exascale Systems. European Commission. Brussels, Oct. 24th, 2018.*

- *BSC at a Glance. Invited lecture. Jornadas sobre: "Soluciones Inteligentes para Sociedades Seguras". Centro de GeoInteligencia. Ciudad de México, October 17th. 2018*

- *Financiación de la Investigación. Keynote talk. Jornadas sobre: "Soluciones Inteligentes para Sociedades Seguras". Centro de GeoInteligencia. Ciudad de México, October 17th. 2018*

- *Mi vida Investigadora. Invited Talk. Universidad de Guadalajara. Inauguración del Centro de Supercmputación. Guadalajara, México. October 16th. 2018*

- *Barcelona Supercomputing Center. Keynote Lecture. 8esJornades TIC Salut i Social. Tecnología i Humanisme: Teràpies digitals. Vic, Barcelona, October 27th, 2018,*

- *From Classical to Runtime Aware Architectures and Beyond. Keynote lecture. Jornadas de Paralelismo de SARTECO, Sociedad de Arquitectura y Tecnología de Computadores. Teruel, September 13th, 2018.*

- *Barcelona Supercomputing Center: past, present and future. Keynote lecture. ECMOR XVI (European Conference on the Mathematics and Oil Recovery, organized by EAGE, European Association of Geoscientifics & Engineers. Barcelona, September 3th, 2018.*

- *BSC at a Glance. Invited talk. PUMPS+AI) Summer School, Programming and Tuning Massively Parallel Systems + Artificial Intelligence. Barcelona, Spain, July 16th, 2018.*

- *From Classical to Runtime Aware Architectures and Beyond. Keynote lecture. ECRTS, Euromicro Conference on Real Time Systems. Barcelona, Spain, July 6th, 2018.*

- *The Supercomputing: Engine for the Science and the Engineering. Invited Lecture. Seminary about "Economic Power and Scientific Power". UIMP, Universidad Internacional Menéndez Pelayo. Santander, June 2018.*

- *From Classical to Runtime Aware Architectures and Beyond. Keynote lecture. ACM, ICS, International Conference on Supercomputing. Beijing, China. June 14th. 2018.*

- *BSC at a Glance. Invited talk. Universidad de Murcia. May, 4th. 2018.*

- *From the Neurons and Transistors to the electric brians. Invited Talk. Cerebro: Creatividad. Organized by Instituto de España. Madrid, 30th May 2018.*

- *Runtime Aware Architectures Keynote Talk. ACM SIGSIM Conference on Principles of Advanced Discrete Simulation (PADS). Rome, Italy, May 25th. 2018.*

- *From Classical to Runtime Aware Architectures and Beyond Invited Talk. PER-18. Perspectives on the Future of Computing. University of Chalmers. Goteborg, May,23th. 2018.*

- *AI and the new workplaces. Invited lecture. Seminary "Com seran la Societat i L'economia del Segle XXI". Escola Europea D´Humanitats. Barcelona May16th, 2018.*

- *European Processor Initiative and RISC-V. Keynote lecture. RISC-V Workshop. Barcelona, May 9th, 2018.*

- *Perspective on achieving Exascale Computing: A perspective from Europe. Invited lecture. JLESC meeting. Barcelona April 17-20th 2018*

- *The Supercomputing: Engine for the Science and the Engineering. Invited Lecture. Von Misses Foundation. Restaurante 7 Puertas. Barcelona, April 26th, 2018.*

- *The Supercomputing: Engine for the Science and the Engineering. Invited Lecture.Foro Ibercaja. Ibercaja Obra Social. Zaragoza, March, 21th. 2018.*

- *Barcelona Supercomputing Center. Charla efectuada para profesores y alumnos de la Universidad Católica de Avila. Barcelona, 12th March 2018.*

- *BSC at a Glance. Keynote lecture. ISUM, International Supercomputing Conference in Mexico. Mérida, México. March 8th, 2018.*

- *EPI: European Processor Initiative. Invited talk CIC, Centro de Informática y Computación, IPN, Instituto Politécnico Nacional. México DF. March 6th, 2018.*

- *The Supercomputing: Engine for the Science and the Engineering. Invited Lecture. Universidad Autónoma de Occidente. Culiacán, Sinaloa, México. March 5th, 2018.*

- *Mare Nostrum 4. Charla efectuada en la FIB para la Fundación de Estudios Médicos de Molina de Segura y Alumnos de IES de Murcia. Barcelona, 20th February 2018.*

- *¿What can we expect about the Artificial Intelligencel? Given talk in Palau Macaya. 01 Febuary, 2018.*

- *The Supercomputing: Engine for the Science and the Engineering. Given talk in Universidad Católica de Ávila. 25 January 2018.*

- *From Classical to Runtime Aware Architectures Invited Talk. V Jornada de Bioinformàtica i Genòmica. Hospital Universitari Vall D´Hebron. Barcelona, Dec. 20th, 2017.*

- *The Supercomputing: Engine for the Science and the Engineering. Invited talk. Fundación de Estudios Médicos, FEM. Teatro Villa de Marina. Molina de Segura, Murcia, Dec.5th, 2017.*

- *The Supercomputing: Engine for the Science and the Engineering. Invited talk. Fundación de Estudios Médicos, FEM. Universidad Politécnica de Cartagena. Escuela de Ingenieros de Telecomunicación. Cartagena, Dec. 5th, 2017.*

- *From Classical to Runtime Aware Architectures Invited Talk. 40th Aniversario de la Facultat de Informática de Barcelona. Barcelona Nov. 29th, 2017.*

- *Supercomputers: An Instrument for Science, technology and the progress of Society. Academia Europeae. Barcelona Disputatio. Institut de Estudis Catalans. Barcelona, November 24th, 2017. Invited lecture*

- *Quien no computa no compite Conferencia Invitada Organizó: Foro de Foros. Colegio de Economistas de Cataluña. Barcelona, November 6th., 2017*

- *The Supercomputing: Engine for the Science and the Engineering. Universitat de Girona. Casa de Cultura de la Diputació de Girona. Girona, October, 26th, 2017.*

- *Science and Culture at the territory. Panel Session. Interaction Conference. Science and Culture. Restoring the Connection. Barcelona, October 20th, 2017.*

- *Barcelona Supercomputing Center. Invited Lecture. University of Shiraz. Tehran, Iran. October 7th, 2017.*

- *Barcelona Supercomputing Center. Invited Lecture. UAT, Amirkabir University of Tehran. Tehran, Iran. October 6th, 2017.*

- *From Classical to Runtime Aware Architectures. Keynote Talk. SMC-IT2017.International Conference on Space Mission Challenges for Information Technologies. Alcalá de Henares, Madrid, Spain. September, 27-29, 2017.*

- *From Classical to Runtime Aware Architectures. Keynote Talk. CARLA Latin American Conference on High Performance Computing. Buenos Aires, Argentina. September 20-22, 2017.*

- *EPI: European Processor Initiative. Invited Talk. Jornada sobre el futuro centro de supercomputación en energía CENSCE. Organizado por CONACYT y SENER. México D.F. September 12th, 2017.*

- *From Classical to Runtime Aware Architectures. Invited Talk. CINVESTAV. Honorary Doctor degree. México D.F. September 11th, 2017.*

- *Panel Session: "Towards Exascale Computing". ACM Europe Conference. Barcelona September 6-7, 2017.*

- *From Classical to Runtime Aware Architectures Invited Talk Stamatis Symposium: A Celebration of the Life of Stamatis Vassiliadis. Samos, Greece, July 19th., 2017.*

- *Barcelona Supercomputing Center, Wealth generator Invited talk in the Science and Technology Room. Universidad de Alicante. June, 15th, 2017.*

- *From Classical to Runtime Aware Architectures keynote talk IEEE IPDPS, 31st IEEE International Parallel and Distributed Processing Symposium. Orlando, Florida, USA. May 29- June 2, 2017*

- *From Classical to Runtime Aware Architectures Invited talk CIC, Centro de Informática y Computación, IPN, Instituto Politécnico Nacional. México DF. May 29th, 2017.*

- *From Classical to Runtime Aware Architectures keynote talk ACM Computer Frontiers Conference. Siena, Italy, May15-18, 2017.*

- *BSC at a Glance Invited talk 4th BSC Severo Ochoa Doctoral Symposium. Barcelona, 2-4 May, 2017.*

- *From Classical to Runtime Aware Architectures Invited talk. Red SYEC Congress. Postgraduate conferences of the UCM, Universidad Complutense de Madrid. Madrid, April 25th, 2017.*

- *Runtime Aware Architectures Invited talk. "Treball de Torn". RACAB, Real Academía de Ciencias y Artes de Barcelona. Barcelona, April, 29th, 2017.*

- *BSC at a Glance Conferencia Invitada. Universidad de Guanajuato. Guanajuato, México, March 1th, 2017*

- *BSC at a Glance Keynote talk. ISUM-2017 International Supercomputing Conference in Mexico. Guadalajara, México, February 28-March 3, 2017*

- *BSC at a Glance Invited talk, CCS (Center for Computational Simulation) and BSC. Universidad Politécnica de Madrid. Madrid, 21 February 2017*

- *Barcelona Supercomputing Center, Wealth generator, Invited Talk. Financial Club Génova. Madrid, February 2017*

- *The investigation, wealth generator invited talk. The investiture act of the new Doctors from the ETSI of the Madrid's telecommunication. Universida Politécnica de Madrid. Madrid, January, 2017*

- *Building European Low-Power Microprocessor Technologies Keynote European Commission. Brusselas, January 18th, 2016*

- *Does it have limits the Supercomputation? Barcelona Supercomputing Center, an Engine for the Science and the Engineering.Invited talk in the Cycle:Science to be discovered. Obra Social de Ibercaja. Zaragoza, Dec, 1th, 2016.*

- *Transhumanism and Immortality: ¿Dream or Nightmare? Round table. Organized for "Obra Social de la Fundación La Caixa" and the office of "Club de Roma" in Barcelona. Barcelona, Nov. 28th, 2016*

- *BSC: Supercomputing at the service of the Engineering. Invited Talk. 50th Anniversary of the Industrial Engineering School and the 25th Anniversary of the Telecommunication Engineering School. Sevilla, 10 November 2016*

- *The Engineering and its education: ¿a convolution?Round table. University Carlos III. Celebration of the 25th Anniversary of the Technical Superior College. Madrid, 8th November 2016*

- *The IBM-BSC Deep Learning Center Invited Talk OpenPOWER Summit Europe. Barcelona October 27th, 2016*

- *BSC: Engine for the Science and the Engineering invited talk. Opening conference of the new school year 2016-2017 of Andorra's University. Sant Julià de Lòria, Andorra. October, 13th, 2016.*

- *BSC: Supercomputing for the Development of a Nation, invited talk. Forum: "Educación Superior y Conocimiento: Avances Globales y Locales". BUAP: Benemérita Universidad Autónoma de Puebla. Puebla, México, October 3th, 2016*

- *BSC: Engine for the Science and the Engineering, invited talk. InKietos Association. Barcelona, September 21, 2016*

- *¿Is it possible to build the Airbus of the Supercomputing in Europe? Keynote Lecture. Telecommunication meeting and Digital economy, year 30. University Menéndez y Pelayo courses. Santander, 5-8 September 2016.*

- *Runtime Aware Architectures Keynote Lecture. ICETE. 13th International Joint Conference on e-Business and Telecommunications. Lisbon, Portugal, July 27th, 2016*

- *BSC: Engine for the Science and the Engineering, Invited talk. Club de Roma. Fundació La Caixa. Barcelona, 11 July 2016*

- *BSC: Science and Engineering Motor Invited Talk. Universitat Rovira i Virgili.Tarragona. Celebration of the XXV Anniversary of the Computing Faculty. Tarragona, July, 1th, 2016*

- *Runtime Aware Architectures Keynote Lecture. VECPAR. International Meeting on High Performance Computing for Computational Science. Porto, Portugal, June 28th, 2016*

- *BSC at a Glance Invited talk. IFT: Instituto de Física Teórica. Severo Ochoa Center. Universidad Autónoma de Madrid. Campus de Cantoblanco. Madrid, 7 June 2016*

- *BSC-CNS Achievements and Challengues Reunión de Centros Severo Ochoa with the countries from the North Pacific. Lima. April, 26, 2016*

- *"Cool Techniques for Hot Chips". Keynote. Cool Chips Conference. Yokohama, Japan. April, 22th, 2016.*

- *"BCS at a Glance". Invited talk. UAEM, Autonomous Mexico State University. Toluca, Mexico, April, 13th, 2016.*

- *"Cool Techniques for Hot Chips". Keynote. ISUM 2016. International Supercomputing Conference in Mexico. Puebla, Mexico. April, 12th, 2016.*

- *"BSC at a Glance". Invited talk: "Jueves de la Ciencia". UNED. National University of Distance Education. Guadalajara, March 31th, 2016.*

- *"RoMoL 2016 Workshop". Welcome talk. Barcelona March 17-18, 2016.*

- *"The European Exascale Opportunity" Guest Presentation, ETP4HPC meeting. Barcelona, March, 15th. 2016.*

- *"Runtime Aware Architectures". Keynote Lecture. GPGPU Workshop. Collocated with HPCA. Barcelona, March, 12th, 2016.*

- *"Runtime Aware Architectures". Invited lecture. National Congress of Engineering Computer System. Bucaramanga- Santander. Colombia, February 29 to March 3, 2016.*

- *"BSC at a Glance". Presentation to Lenovo. Barcelona, February 23th, 2016*

- *"BSC at a Glance". Invited talk. First SyeC (Supercomputing and eScience) network workshop. Barcelona, January 26, 2016*

- *"BSC at a Glance". Invited talk. CFIS, Interdisciplinary Training Superior Centre Graduation Ceremony(Seventh Group) CosmoCaixa Auditorium. December 18, 2015.*

- *"Seymour Cray Award", Supercomputing 2015, Austin, Texas, November 18, 2015.*

- *"The Transformative Impact of Excellent Science: The case of High Performance Computing", ICT 2015, Lisbon, Portugal, October 22, 2015.*

- *"BSC-CNS Achievements and Challenges". Meeting of Severo Ochoa Centres. 100*CIENCIA, Communicating Frontier Science. La Palma, Islas Canarias, October 7-9, 2015.*

- *Roundtable: "Science and solutions to the problems and challenges of the XXI Century". Meeting of Severo Ochoa Centres 100*SCIENCE, Communicating Frontier Science. La Palma, Islas Canarias, October 7-9, 2015.*

- *"Why Exascale Will Not Appear Without Runtime Aware Architectures". Invited lecture. Russian Supercomputing Days. Moscow, Russia, September 2015*

- *"Research on Computer Architecture at BSC: RoMoL and Montblanc". Invited Talk. Superior Polytechnic Center. University of Zaragoza. June 1, 2015.*

- *"Science and Industry: Partners for Innovation". Panel session. PRACE Days 15. Dublin, Ireland. May 28, 2015*

- *"Runtime Aware Architectures". Invited Lecture. PRACE Days 15. Dublin, Ireland. May 27, 2015*

- *"Research on Computer Architecture at BSC: RoMoL and Montblanc". Invited Talk. IHPCF, The second International High Performance Computing Forum. Tianjin, China. May, 20-22, 2015.*

- *"HPC and Big Data Convergence". Panel Session. The second International High Performance Computing Forum. Tianjin, China. May, 20-22, 2015.*

- *"Supercomputing, Big Data and cognitive computing". Invited conference. Department of Computer Architecture and Technology. University of Granada. Granada, April 17, 2015.*

- *"Big Data: Economics and social challenges". Invited conference. Organized by ASCENTIC, Cántabra Association of New Information and Communications Technologies. Sodercan Assembly Hall. Santander, Cantabria. March 20, 2015.*

- *"BSC: Research, Technology Transfer and education". Invited conference. Organized by ASCENTIC, Cantabrian Association of the New Information and Communications Technologies. Sodercan Assembly Hall. Santander, Cantabria. March 20, 2015.*

- *"My research life". Roundtable for the doctoral students at the University of Cantabria. Santander, Cantabria. March 20, 2015.*

- *"The state of Big Data Art & Data Science. The revolution data". Invited Talk. School of Computer Engineering. Veracruz University. Veracruz, March 13, 2015.*

- *BSC: Research, Technology Transfer and Education". Invited Talk. School of Computer Engineering. Veracruz University. Veracruz, March 13, 2015.*

- *"Runtime Aware Architectures". Keynote Lecture. ISUM-2015, International Supercomputer in Mexico. Mexico City, Mexico. March 11, 2015.*

- *Chairman and panel member: "Future Exascale Architectures". ISUM-2015. International Supercomputer in Mexico. México City, Mexico. March 10, 2015*

- *"Technology Transfer at BSC". Conference on knowledge transfer in the Severo Ochoa Centre of Excellence. Congreso Transfiere. Malaga, February 11-12, 2015.*

- *"BSC: Research, Technology Transfer and education". Invited Talk. Faculty of Informatics of the University of Malaga. February 11, 2015.*

- *"RoMoL: Riding on Moore´s Law". Invited talk. Faculty of Informatics at the University of Malaga. February 11, 2015.*

- *"BSC: Research, Technology Transfer and Education". Intern conference, Fulbright of the Caixa. Barcelona, February 9, 2015*

- *"RoMoL: Riding on Moore´s Law". Invited talk. BDEC, Big Data and Extreme Computing, Workshop. Barcelona, January 29-30, 2015.*

- *"Social and Economic Challenges of Open Data and Big Data". Invited Lecture. VII Jornadas de la Red de Bibliotecas. Biblioteca Cervantes. Madrid, December 11, 2014.*

- *"BSC: Research, Technology Transfer and Education". Invited conference. Medical Research Foundation Molina de Segura. Faculty of Medicine, Campus de Espinardo. Molina de Segura, Murcia, Spain. December 5, 2014.*

- *"The national center of computing in our lives". Invited conference. Medical Research Foundation Molina de Segura. Colegio Infantil Paseo Rosales. Molina de Segura, Murcia, España. December 4, 2014.*

- *"Barcelona Supercomputer Center and its Impact on the Economy". Invited lecture. Cámara de Comercio de Barcelona. December 2, 2014*

- *"Runtime Aware Architectures". Invited Lecture. JLEC Workshop. Joint INRIA-ANL-UIUC-BSC Workshop. Chicago, November 24-26, 2014.*

- *"Runtime Aware Architectures". Invited Lecture. SC, ACM Supercomputer Conference. New Orleans, USA, November 20, 2014.*

- *"Barcelona Supercomputer Center and its Impact on the Economy".Keynote Talk. CIIP: Iberoamerican Congress on Engineering Projects. Loja, Ecuador. Nov, 13-14, 2014.*

- *Panel member: "Societal Aspects of Big Data (Ethical Issues, Citizen Participation, Educational aspects, Business Opportunities and Public Policies). Workshop on Big Data in Biomedicine: Challenges and Opportunities. Organized by B-Debate, International Center for Scientific Debate. Barcelona, November 11-12, 2014.*

- *"The state of the art in Big Data & Data Science. The data revolution." Keynote talk. CICOS, International congress on computer and software optimization. Cuernavaca, México. October 21, 2014*

- *"Research at BSC." Keynote talk. CICOS, International congress on computer and software optimization. Cuernavaca, México. October 21, 2014*

- *"Runtime Aware Architectures". International seminar on Advances in Research in the Area of Computer Science. Invited Lecture. CIC, Centro de Investigación en Comunicaciones. IPN, Instituto Politécnico Nacional. México DF. October 17, 2014.*

- *"BSC: Research, Technology Transfer and education". Invited Talk. CIDETEC, Centro de Innovación y Desarrollo Tecnológico en Cómputo. 10th International Congress Technological Trends in Computing. México DF, October 13-17, 2014.*

- *"Barcelona Supercomputer Center and its Impact on the Economy". Invited Lecture. Forum España Innova. Desayunos del hotel Ritz. Madrid, October 7, 2014.*

- *"A Case for HPC in Horizon2020". Invited Lecture. Presentation to the HPC European Committee". Brussels, October 2, 2014.*

- *"Barcelona Supercomputing Center at a Glance". Invited Lecture. NCSA, National Center for Supercomputing Applications. Urbana Champaign. September 22, 2014.*

- *"Runtime Aware Architectures". Invited Lecture. PCI, Parallel Computer Institute, Distinguished Lecture. University of Urbana Champaign, September 22, 2014.*

- *"Runtime Aware Architectures". Invited Lecture. A Computer Architecture Workshop: Visions for the Future. Celebrating Yale@75. University of Texas in Austin. September 19, 2014.*

- *"Social and Economic Challenges of Open data and Big Data". Invited lecture. UIS, Industrial University of Santander. Bucaramanga, Colombia, September 12, 2014.*

- *"Barcelona Supercomputer Center and its Impact on the Economy".Keynote Talk. Forum HPC/IT for the Industrial Competitiveness, organized by UIS, Universidad*

Industrial de Santander. Technology Park Guatigará. Bucaramanga, Colombia, September 11, 2014.

- *"Social and Economic Challenges of Open data and Big Data". Invited lecture. Summer School on New Technologies, Public Policy & Creative innovation. Institut Barcelona d'Estudis Internationals, IBEI. Barcelona, July 23, 2014*

- *"Research at BSC". Invited lecture. Riken. ACCC, Advanced Center for Computing and Communication. Tokyo, July 15, 2014.*

- *"Research at BSC". Invited lecture. University of Tokyo. Research Center for Advanced Science and Technology. Campus of Komaba. July 17, 2014.*

- *"Research at BSC". Invited lecture. University of Tokyo. Campus of Kashiwa. July 15, 2014.*

- *"BSC at a Glance". Invited talk. Fujitsu. Tokyo, Japan, July 15, 2014.*

- *"The State of the Big Data Art & Data Science. The Revolution Data". Invited lecture. Conference about "Big Data: of scientific research to business managment". Ramón Areces Foundation. Madrid, July 3, 2014.*

- *"Runtime Aware Architectures". Invited Lecture. Workshop on Extreme Scale Scientific Computing". Moscow State University. Moscow, Russia, June 30, 2014*

- *"BSC at a Glance". Invited Lecture. University of the Balearic Islands. Majorca, May 30, 2014*

- *"BSC at a Glance". Talk to the EUSJA (European Union of Science Journalist Association). Barcelona Supercomputing Center. Barcelona, May 21, 2014.*

- *"Runtime Aware Architectures". Keynote Talk. HiPEAC, Computing Systems Week. HiPEAC 10th Anniversary. Barcelona, May 14, 2014.*

- *"Supercomputers of the Future". Invited lecture. Seminar on "Supercomputers, a Tool for Engineering". 20 Anniversary of the Spanish Royal Academy of Engineering. Madrid, April 29, 2014.*

- *"BSC at a Glance". Invited Lecture. IBM Watson Research Center. New York, USA, March 31, 2014.*

- *"Supercomputing, a Tool for Sciences and Engineering". Invited Conference. National College of Mexico. March 12, 2014.*

- *"BSC: Research, Technology Transfer and Education". Invited Talk. Conference on "Innovation and Entrepreneurship in Spain and Europe, Prospects for mobilization". Academy of Engineering of Spain. Madrid, February 14, 2014.*

- *"HPC and Education: Working Examples, BSC". RISC (Red Iberoamericana de Supercomputación) Workshop. IPN, National Polytechnical Institute. Mexico City, November 25-26, 2013.*

- *"H2020: Challenges and Opportunities for HPC". Keynote talk. RISC (Red Iberoamericana de Supercomputación) Workshop. IPN, National Polytechnic Institute. Mexico City, November 25-26, 2013.*

- *"Supercomputing, a Tool for Science". Invited Talk. Patronat D´Estudis Osonencs. Temple Romá. Vic. November 16, 2013.*

- *"Supercomputing, a Tool for Science". Inaugural lecture. "Science Week" Catalonia. Catalan Parliament. Barcelona, November 11, 2013.*

- *"BSC at a Glance". Invited Talk. Master de Bioinformática. Universitat Autonoma de Barcelona. Bellaterra, November 6, 2013.*

- *"Research, wealth creator". Invited talk, Celebration of 25 Anniversary of the Red Iris. Ministry of Industry. Madrid, October 29, 2013.*

- *"Killer mobiles: the way towards energy efficient High Performance Computers?" Keynote lecture. SBAC-PAD. Porto de Galinha, Brasil. October 23, 2013.*

- *"BSC at a Glance". Invited Talk. University of Andes. Bogotá, Colombia. October 22, 2013.*

- *"BSC at a Glance". Invited speaker. Experiences and Perspectives in the Spanish-German Cooperation in I+D+i. Cervantes Institute. Berlin, October 18, 2013.*

- *"Killer mobiles: the way towards energy efficient High Performance Computers?" Keynote lecture. PaCT-2013, Parallel Computing Technologies. Saint Petersburg, Russia, September 30, 2013.*

- *"The Future of HPC in Europe". Invited Talk. EU-Russia Workshop on High Performance Computers. Saint Petersburg, Russia, September 30, 2013.*

- *"Training+Research=Excellence". Invited Talk. VII Ibero-American Forum of Higher Education, Sciences and Technology. Panama City, Panama, September 10-11, 2013.*

- *"Supercomputers: Past, Present and Future". Invited Talk. Summer course on trends in computer engineering. University of Granada. Almuñecar, Granada, July 22-25, 2013.*

- *"Killer mobiles: the way towards energy efficient High Performance Computers?" Keynote lecture. ACSD. IEEE Conference on Application of Concurrency to System Design. Barcelona, Spain, July 8, 2013.*

- *"Killer mobiles: the way towards energy efficient High Performance Computers?" Keynote lecture. HPCS, the IEEE-ACM International Confrence on High Performance Computing&Simulation. Helsinki, Finland, July 1-5th, 2013*

- *"BSC: Severo Ochoa Research Programme". Evaluation Committee. Madrid. June 30, 2013.*

- *"BSC at a Glance". Invited lecture. ICCS, International Conference on Computational Science. Barcelona, June 5, 2013.*

- *"Killer mobiles: the way towards energy efficient High Performance Computers?" Keynote lecture. RISC Workshop. University of Manizales, Colombia. May 16-17, 2013.*

- *"BSC at a Glance". Invited lecture. RISC Workshop. University of Manizales, Colombia. May 16-17, 2013.*

- *"RISC: Latin Amercian Supercomputing Network". Invited lecture. RISC Workshop. University of Manizales, Colombia. May 16-17, 2013.*

- *"Killer-mobiles: the way towards energy efficient High Performance Computers?". Invited lecture. First Europe-China HPC (High Performance Computing) Conference. Sofia, Bulgaria, April 8-10, 2013*

- *"BSC: Past, Present and Future". Invited Talk. Microsoft, Madrid, March 19, 2013.*

- *"BSC: Center of Excellence Severo Ochoa". Invited Talk. Innovation Forum Spanish-German. Madrid, 20th, 2013.*

- *"Killer-mobiles: the way towards energy efficient High Performance Computers". Keynote Talk. ISUM-2013. International Supercomputing Conference in Mexico. Manzanillo, Colima, México. February 6, 2013*

- *"RISC: Red Iberoamericana de Supercomputación". Invited Talk. Workshop organized by SCALAC, Advanced Computing Services for Latin America and Caribbean. Bucaramanga, Colombia, February 1st, 2013.*

- *"Killer-mobiles: the way towards energy efficient High Performance Computers?".
  Keynote Talk. PECCS, International Conference on Pervasive and Embedded
  Computing and Communication Systems. Barcelona, Spain, February 21th, 2013.*

- *"Supercomputers to create wealth". Invited Lecture. V Coloquio IUMA-Matemáticas.
  Zaragoza, Spain. January 11, 2013.*

- *"Supercomputers to create wealth". Keynote Lecture. International Workshop EU-
  China on Scientific Computing. Zaragoza, Spain. November 26-28, 2012.*

- *"Sin I+D+i no hay futuro". Invited Talk. CSIC, Madrid, Nov. 6th, 2012.*

- *"Conference I+D+i Programme Horizon 2020". Invited Talk. Group S&D PSOE
  European Parlament, Madrid, Nov 24th, 2012.*

- *"Supercomputers Generators Wealth". Speech input as Academic of the Mexico
  Academy of Sciences, ACM. Mexico DF, Auditorium "José Adem", Cinvestav, IPN,
  October 23th, 2012*

- *"HPC Centers of Excellence in the EU". Invited Presentation. Round Table. EU,
  Brussels, October 18th, 2012*

- *"Killer-mobiles: the way towards energy efficient High Performance Computers".
  Keynote Talk. HiPEAC Computing System Week. Ghent, Belgium, October 17th,
  2012.*

- *"Supercomputers to create wealth". Conference Invitation. MIC Summit 2012,
  Microsoft. Palma de Mallorca, September 21 2012.*

- *"RISC: Red Iberoamericana de Supercomputacíon". Invited Talk. CLCAR,
  Conferencia Latinoamericana de Alto Rendimiento. Universidad Tecnológica de
  Panamá. Ciudad de Panamá, August 29, 2012.*

- *"Killer-mobiles: the way towards energy efficient High Performance Computers".
  Keynote Talk. CLCAR, Conferencia Latinoamericana de Alto Rendimiento.
  Universidad Tecnológica de Panamá. Ciudad de Panamá, 29, Agosto 2012.*

- *"Research at BSC: Barcelona Supercomputing Center". Keynote Lecture. ECAR
  2012. High Performance Computing School. Buenos Aires, Argentina, July 25 to
  August 3, 2012.*

- *"Killer-mobiles: the way towards energy efficient High Performance Computers".
  Keynote Talk. V HPC LatAm 2012 International Symposium. Buenos Aires,
  Argentina, July 23-24, 2012.*

- *"The Mont-Blanc Approach Towards Exascale". Keynote Talk. ISPA 2012, the IEEE
  International Symposium on Parallel and Distributed Processinf with Applications.
  Leganés, Madrid, July 10-13, 2012.*

- *"The Mont-Blanc Approach Towards Exascale". Invited Talk. ISC, International
  Supercomputing Conference. Hamburg, June 20th, June, 2012.*

- *"Mont-blanc: Enfoque Europeo hacia un HPC energéticamente eficiente".
  Conferencia Invitada. Entrega de Diplomas de los Estudios Oficiales de Master de
  la Escuela de Ingenieria y de Arquitectura de la Universidad de Zaragoza.
  Zaragoza, 14 de Junio de 2012.*

- *"BSC: Supercomputadores, generadores de riqueza". Invited Talk, Ciclo Ciencia
  para Directivos. Fundación APD y Fundación Ramón Areces. Madrid, 12 de Junio de
  2012.*

- *"The Mont-Blanc Approach Towards Exascale". Invited Talk. Universidad de
  Veracruz. Veracruz, May 21th, 2012.*

- *"BSC: Barcelona Supercomputing Center". Invited Talk. Universidad de Veracruz.
  Veracruz, May 21th., 2012.*

- *"The Mont-Blanc Approach Towards Exascale". Invited Talk. CIC, IPN, Instituto Politécnico Nacional. México DF, May, 18th. 2012.*

- *"Future Exascale Supercomputers". Invited Talk. UIS, Universidad Industrial de Santander, Colombia. Parque Tecnológico de Guatiguara, April 23th, 2012.*

- *"Future Exascale Supercomputers". Invited Talk. "Universidad Mexiquense del Bicentenario". ABACUS, Supercomputer Center. Toluca, March, 6th, 2012.*

- *"Future Exascale Supercomputers". Invited Talk. RACAB, Real Academia de Ciencias y Artes de Barcelona. February, 16th., 2012.*

- *"Future Exascale Supercomputers". Invited Talk. Workshop sobre Supercomputación, organizado por la RISC, Red Iberoamericana de Supercomputación. Ciudad de Mçexico, 23 de Noviembre de 2011.*

- *"Future Exascale Supercomputers". Keynote talk. SBAC-PAD. International Symposium on Computer Architecture and High Performance Computing. Vitória, Espirito Santo, Brazil, October 26-29, 2011.*

- *"Supercomputadores del Futuro". Conferencia invitada. Inauguración de curso de la EUPLA, Escuela Universitaria Politécnica de La Almunia de la Universidad de Zaragoza. La Almunia de Doña Godina, 17 de Octubre de 2011.*

- *"Future Exascale Supercomputers". Keynote talk. EUSIPCO, 19th European Signal Processing Conference. Barcelona, August 29-September 2, 2011.*

- *"Future Exascale Supercomputers". Keynote talk. ICME 2011, the IEEE International Conference on MUltimedia and Expo. Barcelona, Spain, July 11-15th 2011*

- *"From Nowhere to MareNostrum and MareIncognito". Invited Talk. First Technion Computer Engineering Conference. Haifa, Israel, June 1-5, 2011.*

- *"Towards Exaflops Supercomputers". Invited Talk. Microsoft Research Faculty Summit 2011. Cartagena de Indias, Colombia, May 18-20, 2011.*

- *"Towards Exaflops Supercomputers". Invited Talk. ISUM, International Supercomputing Conference in Mexico. San Luis de Potosí, Mexico, March, 21-24, 2011.*

- *"Supercomputadores del Futuro". Invited talk. Neocom Workshop. Organized by AATUZ, the Association of Students of the School of Telecommunication Engineers. University of Zaragoza, March, 2011.*

- *"Towards Exaflops Supercomputers". Invited Talk. University of Patras, Greece. February 17th., 2011.*

- *"Towards Exaflops Supercomputers". Invited Talk. BIFI2011: V National Conference. BIFI, Instituto Universitario de Investigación en Biocomputación Y Física de Sistemas Complejos de la Universidad de Zaragoza, Febrero 2011.*

- *"Creación de un centro de investigación de alta tecnología". Conferencia invitada. Master del Instituto de la Salud Carlos III, coordinado pos las Universidades UNED y Alcalá de Henares. Madrid, 2 de Diciembre de 2010.*

- *"Supercomputadores del Futuro". Conferencia Invitada. Facultad de Ciencias. Conmemoración del día de la Ciencia. Santander, 12 de Noviembre de 2010.*

- *"Mi vida Investigadora". Conferencia invitada. Programa Joves i Ciència. Catalunya Caixa. Barcelona, 16 de Octubre de 2010.*

- *"Recerca i Desenvolupament versus Compromis Social". Escola Universitària Salesiana de Sarrià. Lección Inaugural del Curso 2010-2011. Barcelona, 6 de Octubre de 2010.*

- *"BSC: Barcelona Supercomputing Center". Conferencia invitada. Colciencias, Bogotá, Colombia, 1 de Octubre de 2010.*

- *"Supercomputadores del Futuro". Conferencia invitada. Sisoft, V Simposio Internacional en Sistemas de Información e Ingenieria del Software en la Sociedad del Conocimiento. Bogotá, Colombia, 30 Septiembre de 2010.*

- *"Evolución de la Arquitectura de los Computadores". Charla invitada. XX Jornadas Telecom I+D. Valladolid, 27 de Septiembre de 2010.*

- *"Barcelona Supercomputing Center". Meeting between BSC and Nvidia. Santa Clara, Californis, September 2010.*

- *"La Supercomputación del Futuro". Conferencia invitada. Acto de graduación de la promoción 2009-2001 de la Escuela de Ingenieros de Terrassa, 15 de Julio de 2010.*

- *"BSC: Barcelona Supercomputing Center". Rectores de las Universidades Brasileñas. Barcelona, 14 de Julio de 2010.*

- *"Barcelona Supercomputing Center". Invited talk. Inauguración de la Summer School: PUMPS, Programming and tUning Massively Parallel Systems. Barcelona, July, 5th, 2010.*

- *"BSC y la Red Española de Supercomputación". Invited talk. II Jornadas de la RES, Red Española de Supercomputación. Zaragoza, 2 de Julio de 2010.*

- *"Futuros Computadores Exaflop". Conferencia Invitada. Jornada "La Supercomputación al servicio de Investigadores e Innovadores", organizada por Coputaex, Centro de supercomputación de Extremadura. Mérida, 27 de Abril de 2010.*

- *"Evolución de la Arquitectura de Computadores".Conferencia Invitada. XXV Aniversario del I.E.S. Pablo Serrano. Zaragoza, 12 de Marzo de 2010.*

- *"Investigación en Arquitectura de Computadores". Conferencia invitada inaugural. Primeras Jornadas de Investigación del Departamento de Arquitectura de Computadores de la Universitat Politécnica de Catlunya. Barcelona, 25 de febrero de 2010.*

- *"Research at the Barcelona Supercomputing Center". Conferencia invitada. Reunión de FemCat "Fem Catalunya". Barcelona, 18 de Febrero de 2010. bn*

- *"Barcelona Supercomputing Center". Presentación al Cuerpo Nacional de Policia en su visita al MareNostrum. Barcelona, 10 de Febrero de 2010.*

- *"Expectativas del Supercómputo: La Era del Exaflop". Keynote. V Congreso Internacional de Métodos Numéricos. Guanajuato, México, Feb. 3th, 2010.*

- *"Flagship Initiative". Invited talk, Meting of the PRACE and Prospect European Projects. Barcelona, Jan. 26th, 2010.*

- *"Computadores de Altas Prestaciones". Discurso de entrada a la Real Academis de Ciencias y Artes de Barcelona. Barcelona, Jan. 21th, 2010.*

- *"La Investigació, Creadora de Riquesa". Invited talk. Dinars de la Cambra. Barcelona, Dec. 1th, 2009.*

- *"My research at UPC". Invited talk. Campus of Terrassa, Barcelona. Meeting of the PhD students. Terrassa, Nov. 26th, 2009.*

- *"On the Future Exaflops Supercomputers". Keynote Lecture. ICPP, International Conference on Parallel Processing. Vienna, Austria, September 22-25th, 2009.*

- *"La Supercomputación, una Ciencia Multidisciplinar". Conferencia Invitada. Jornadas JENUI, Barcelona, Julio 2009.*

- *"A European Perspective on Supercomputing". Keynote Lecture. ICS-23. IEEE and ACM International Conference on Supercomputing.IBM T.J. Watson Research Center, Yorktown Heights, USA, June 8-12, 2009.*

- *"Barcelona Supercomputing Center". Advisory Board Meeting. BSC-Microsoft research center. Barcelona, June 4th, 2009.*

- *"QoS on Multithreaded Processors". MTAAP-09. Keynote Lecture. Workshop on Multithreaded Architectures and Applications. Held in Conjuction with IPDPS-09, the International Parallel and Distributed Symposium, Rome, May 29, 2009.*

- *"Panorana Mundial de la Supercomputación". Invited Lecture. Jornadas Ibéricas de Supercomputación. Organizadas por el CICA, Centro de Informática de la Comunidad Andaluza. Valencia, 19 de Mayo de 2009.*

- *"Supercomputación en España en los próximos años". Mesa Redonda. Jornadas Ibéricas de Supercomputación. Organizadas por el CICA, Centro de Informática de la Comunidad Andaluza. Valencia, 19 de Mayo de 2009.*

- *"Supercomputing for the Future, Supercomputing from the Past". Keynote lecture. AICCSA, the 7th ACS/IEEE International Conference On Computer Systems and Applications. Rabat, Morocco, May 10-13th 2009.*

- *"La colaboración Universidad-Empresa: Buenas Prácticas en la Transferencia del Conocimiento y Tecnología de la Universidad a la Sociedad". Conferencia invitada. VII Pleno del Consejo Universitario Iberoamericano. Ciudad de Panamá, March 12-13, 2009.*

- *"Supercomputing for the Future, Supercomputing from the Past". Invited talk. University of La Habana, February, 13th, 2009.*

- *"Los Supercomputadores, esos increibles cacharros". Keynote lecture. Congreso Informática 2009. XIII Convención y Ferial Internacional. La Habana, Cuba, February, 12th , 2009.*

- *"On the Future of Supercomputers". Invited talk. Universidad de las Ciencias Informáticas. La Habana, February,11th, 2009.*

- *"Partnership between Public and Private Research". Microsoft-INRIA Forum. Panel session. Paris, January 28th, 2009.*

- *"Supercomputadores del Futuro". Invited lecture. Universidad Veracruzana. Xalapa, December 12th, 2008.*

- *"Barcelona Supercomputing Center". Invited lecture. Universidad Veracruzana. Veracruz, December 11th, 2008.*

- *"Idea i Aplicacions del Supercomputadors". Invited lecture. Aula d'Extensió Universitaria de la gent gran de Vilanova i la Geltrú. Sala de Actos de la Escuela de Ingenieria de Vilanova i la Geltrú. December 2008.*

- *"La Supercomputació al Segle XXI". Invited Lecture. Cicle de Conferèncias AVUI PARLA.... Ciència y Ciutadania". Obra Social Fundació La Caixa. Girona 4 de Diciembre de 2008*

- *"La Supercomputació al Segle XXI". Invited Lecture. Cicle de Conferèncias AVUI PARLA.... Ciència y Ciutadania". Obra Social Fundació La Caixa. Barcelona 2 de Diciembre de 2008.*

- *"Barcelona Supercomputing Center". Visita de Telefónica I+D al Barcelona Supercomputing Center. Barcelona, 1 de Diciembre de 2008.*

- *"Supercomputadores". Invited Talk. Instituto de Estudios Secundarios Princep de Viana. Barcelona, 14 de Noviembre de 2008.*

- *"Barcelona Supercomputing Center". BSC 3rd Annual Meeting. MACBA, Barcelona, Nov. 7th., 2008.*

- *"Supercomputadores del Futuro"*. *Invited Talk. Ciencia con Ñ Semana de la Ciencia y la Innovación en Canarias. Santa Cruz de Tenerife, Nov. 5th., 2008.*

- *"On the Future of Supercomputers". Invited Lecture. Open Research Day. Telefónica I+D. Barcelona, 3 de Noviembre de 2008.*

- *"Computadores de Altas Prestaciones". Conferencia invitada. Celebración del XXX aniversario de la creación de la Escuela de Informática de la Universidad Politécnica de Madrid y homenaje al profesor Rafael Portaencasa. Madrid, 31 de Octubre de 2008.*

- *"Barcelona Supercomputing Center". Invited Talk. Information and Brokerage Conference on Information and Communication Technologies in the EU's 7th Framework Programme. Moscow, 21-23 October 2008.*

- *"Computadores de Altas Prestaciones". Conferencia invitada. Celebración del XXV aniversario de la creación de la Facultad de informática de Murcia, 15 de Octubre de 2008.*

- *"Overcoming the Memory Wall: Kilo-Instruction, Runahead and SMT Processors". Invited lecture. VIPSI Conference. Lake Bled, Slovenia, October 3th. 2008.*

- *"On the Future of Supercomputers". Invited Lecture. VIPSI Conference. Lake Bled, Slovenia, October 3th. 2008.*

- *"Barcelona Supercomputing Center". Invited Talk, durante la celebración del 20 Aniversario de la Red IRIS. CSIC, Madrid, Septiembre 2008.*

- *"On the Future of Supercomputers". Invited Lecture. University of Belgrade, Serbia. September 12th, 2008.*

- *"Supercomputing for the Future, Supercomputing from the Past". Invited Lecture. Onasssis Foundation Summer School. Lectures on Computer Science. Heraklion, Crete, July 25th, 2008.*

- *"Overcoming the Memory Wall: Kilo-Instruction, Runahead and SMT Processors". Invited lecture Onasssis Foundation Summer School. Lectures on Computer Science. Heraklion, Crete, July 25th, 2008.*

- *"Supercomputing for the Future, Supercomputing from the Past". Keynote Lecture. ReCoSoC. Barcelona, July 9-11, 2008.*

- *"Supercomputing for the Future, Supercomputing from the Past". Invited lecture. University of Tsinghua. Beijing, China, June 17th, 2008.*

- *"Barcelona Supercomputing Center: Mision, Research Lines and Microsoft Collaboration". Invited Lecture. Microsoft Research Latin America Academic Summit 2008. Panamá, May 13th -16th, 2008.*

- *"Supercomputing for the Future, Supercomputing from the Past". Invited Lecture. University of Chalmers. Göteborg, May 8th, 2008.*

- *"Researh at the Barcelona Supercomputing Center-Centro Nacional de Supercomputación". Invited Lecture. Joint Seminar on Information and Communication Technologies orhanized by the Chinese Academy of Engineering (CAE) and by the Spanish Royal Academy (RAI). Madrid, May 7th, 2008.*

- *"Supercomputing for the Future, Supercomputing from the Past". Invited Lecture. VIII Telecoforum. Universidad Politécnica de Cartagena, 21 de Abril de 2008.*

- *"Research on High Performance Computing at BSC and UPC". Keynote Lecture. First European-Mexican IT Workshop. LAEMI, Xalapa, April 9-11th, 2008.*

- *"Overcoming the Memory Wall: Kilo-Instruction, Runahead and SMT Processors". Invited lecture. Centro de Investigacion en Computación, CIC del Instituto Politécnico Nacional, IPN. Ciudad de México, April 7th, 2008.*

- *"Supercomputing for the Future, Supercomputing from the Past". Invited Lecture. CINVESTAV, Centro de Investigación y de Estudios Avanzados. Cuidad de México, April 7th, 2008*

- *"Supercomputing for the Future, Supercomputing from the Past". Invited Lecture. First Workshop on Supercomputing. Aeronautics School, Technical University of Madrid, March 26th, 2008.*

- *"Supercomputing for the Future, Supercomputing from the Past". Distinguish Lecture under the ACM Distinguish Speakers Program. Birla Institute of Technology and Science. Pilani, India. March 14th 2008*

- *"Research at the High Performance Computer Group in Barcelona". Birla Institute of Technology and Science. Pilani, India. March 14th 2008*

- *"Research at BSC: Barcelona Supercomputer Center-Spanish National Center of Supercomputing". Birla Institute of Technology and Science. Pilani, India. March 14th 2008.*

- *"Simultaneous Multithreaded Procesors". Birla Institute of Technology and Science. Pilani, India. March 14th 2008.*

- *"Research at the High Performance Computer Group in Barcelona". Indian Institute of Technology in Delhi. March 12th 2008*

- *Supercomputing for the Future, Supercomputing from the Past". Distinguish Lecture under the ACM Distinguish Speakers Program. Indian Institute of Technology in Delhi. March 12th 2008.*

- *"Research at BSC: Barcelona Supercomputer Center-Spanish National Center of Supercomputing". IBM Research Lab. Delhi, India. March 12th. 2008.*

- *"QoS on Multithreaded Processors". Invited lecture. University of Texas at Austin. February 13th, 2008.*

- *"Overcoming the Memory Wall: Kilo-Instruction, Runahead and SMT Processors". Invited lecture. University of Texas at Austin. February 13th, 2008.*

- *"Supercomputing for the Future, Supercomputing from the Past". Distinguished Lecture. University of Texas at Austin. February,11th, 2008.*

- *"Supercomputing for the Future, Supercomputing from the Past". Keynote Lecture. HiPEAC Conference, 2008 Conference on High Performance on High Performance Embedded Architectures&Compilers. Götegorg, Sweden, January 28-29, 2008.*

- *"A+A=A". Invited Lecture. Stamatis Vasiliadis Symposium on "The Future of Computing". Delft, Holland. September 28th, 2007.*

- *"Supercomputadores y BSC". Invited talk. Universidad Autónoma de Barcelona. Año de la Ciencia. 10 de Enero, 2008*

- *"Supercomputing in Europe: Past, Present and Future". Invited Talk. Workshop on "High-Performance Computing Systems", organized by the EU. Brussels, December, 17th , 2007.*

- *"Supercomputadores y BSC". Conferencia invitada. Universidad Veracruzana. Veracruz, Diciembre, 7th, 2007*

- *"Supercomputadores y BSC". Conferencia invitada. IPN, Instituto Politécnico Nacional. México DF, Diciembre, 6th, 2007*

- *"Supercomputadores y BSC". Conferencia invitada. UNAM. México DF, Diciembre, 5th, 2007.*

- *"Supercomputadores, Tercer Pilar para la Investigación Científica". Presentación realizada a los alcaldes de la Comarca de Cariñena. BSC, Barcelona, 23 de Noviembre de 2007.*

- *"Supercomputadores, Tercer Pilar para la Investigación Científica". Presentación realizada a los socios del Centro Aragonés de Barcelona. BSC, Barcelona, 23 de Noviembre de 2007.*
- *"High-Performance and Embedded systems in Europe". Invited Lecture. High-Level Conference on Nanotechnologies. Braga, November 20th, 2007.*
- *"BSC: Barcelona supercomputing Center". Invited talk. GASC, Global Academic Supercomputing Consortium Workshop. Reno, USA, November 12th, 2007.*
- *"Supercomputadores, Tercer Pilar para la Investigación Científica". Presentación realizada a los alumnos del Instituto de Granollers. BSC, Barcelona, 5 de Noviembre de 2007.*
- *"High Performance Computing. Conferencia Invitada. Jornadas sobre Supercomputación de la RAI (Real Academia de la Ingenieria de España). Madrid, Octubre 2007.*
- *"El Futuro de la Supercomputación". Conferencia invitada. XIII Forum Microsoft para Corporaciones. Chipre, Octubre 1-5, 2007.*
- *"A+A=A". Invited Lecture. Stamatis Vasiliadis Symposium on "The Future of Computing". Delft, Holland. September 28th, 2007.*
- *"Supercomputing at BSC". Charla impartida al "Scientific advisory Board" de Telefónica I+D. Barcelona, 21 de Septiembre de 2007.*
- *"Supercomputing at BSC". Charla impartida con motivo de la visita al BSC de Mr. Janez Protocnik, Conisiario de Investigación de la Unión Europea. Barcelona, 18 de Septiembre de 2007.*
- *"Ordinadors a tota velocitat". Conferencia invitada. Els Sopars amb estrelles. L'Observatori Fabra. August 10th, 2007.*
- *"Supercomputación y BSC". Charla inaugural de la escuela de Verano sobre Supercomputación organizada por la Universidad de Málaga. Ronda, 2 de Julio de 2007.*
- *"Supercomputadores del Futuro". Conferencia Invitada. Universidad del Pais Vasco. San Sebastián, 14 de Mayo de 2007.*
- *"Supercomputadores del Futuro". Conferencia Invitada. Universidad de Zaragoza. Jornadas sobre ZIVIS. Zaragoza, 11 de mayo de 2007.*
- *"Futuro de la Supercomputación". Conferencia invitada. Real Sociedad Económica de Amigos del Pais de Valencia. Valencia, 7 de Mayo de 2007.*
- *"BSC y Supercomputadores del Futuro". International Workshop on IP Commercialization: From Knowledge to Wealth in Latin Countries". Workshop organizado por LatIPnet. Gran Canaria, 12-15, Abril, 2007.*
- *"Transferencia de Tecnología entre la Universidad y la Empresa". Charla Inaugural. International Workshop on IP Commercialization: From Knowledge to Wealth in Latin Countries". Workshop organizado por LatIPnet. Gran Canaria, 12-15, Abril, 2007.*
- *"Computadores del Futuro". Reunión de la Sección de Ciencias Físicas de la Real Academia de Ciencias Exactas, Físicas y Naturales. Barcelona, 11 Abril, 2007.*
- *"Computadores del Futuro". Conferencia invitada. Universidad de Zaragoza. Facultad de Informática. Organizada por el periódico Heraldo de Aragón. Zaragoza, 30 de Marzo de 2007.*
- *"Com ajudem els Supercomputadors a fer investigació?". Conferencia invitada. La Ciutadella, Parc de la Ciència. Barcelona, Abril, Ciència 2007*

- *"BSC y Supercomputación". Seminario sobre IBM Power. Conferencia invitada. Universidad Politécnica de Cataluña. Barcelona, 8 de Marzo de 2007.*

- *"Recerca i Supercomputació al Segle XXI". Les Tecnologies de la Informació i les Comunicacions a Catalunya.. Primer Cicle de Conferències sobre les Ciències Experimentals i la Tecnología a les Terres de Parla Catalana al Segle XX. Institut d'Estudis Catalans. Sabadell, 27 Febrer 2007.*

- *"Future High Performance Computers". Mesa Redonda: "Encuentro con los Premios Nacionales de Investigación: Retos que plantea el Siglo XXI". Residencia de Esttudiantes. CSIC, Madrid, 16 Enero 2007.*

- *"Supercomputadores: MareNostrum". Conferencia invitada. Cariñena Lan Party. Cariñena (Zaragoza), Diciembre 2, 2006.*

- *"Supercomputadores: MareNostrum". Conferencia invitada. Cariñena Lan Party. Cariñena (Zaragoza), Diciembre 2, 2006.*

- *"Supercomputadores del Futuro". Conferencia Invitada. Master en Ingenieria del Software. Universidad Pontificia de Salamanca. Madrid, Diciembre 1, 2006.*

- *"Investigación en Arquitectura de Computadores". Charla invitada. Programa de Doctorado en Ingenieria Informática. Universidad Pontificia de Salamanca. Madrid, Noviembre 30, 2006.*

- *"Supercomputadores". Conferencia invitada. Conferencia de Decanos y Directores de Informática". Universidad Politécnica de Valencia, 24 de Noviembre de 2006.*

- *"Supercomputadors: MareNostrum". Conferencia invitada. Ciclo de Conferencias: Tecnologies al Segle XXI. Institut d'Estudis Ilerdencs. Lleida, 22 de Noviembre de 2006.*

- *"Computadores del Futuro". Conferencia invitada. Universidad de Cantabria, Facultad de Informática. Santander, 6 de Noviembre del 2006.*

- *"Future Processors and Supercomputers". Invited lecture. Master en Ingenieria web. Universidad de Oviedo, 31 de Octobre de 2006.*

- *"Computers for the Future". Invited lecture. DELIS-CompNet. Workshop on Theoretical Aspects and Models of Large, Complex and Open Information Networks. Barcelona, October 18-20, 2006.*

- *"Computers for the Future". Invited lecture. ESAC Grid workshop. European Space Agency. Madrid, October 5th, 2006.*

- *"Ordinadors a tota velocitat". Invited talk. Els Sopars amb estrelles. L'Observatori Fabra. September 1th, 2006.*

- *"Barcelona Supercomputing Center: Mare Nostrum". Swedish Foundation for Strategic Research (SFSR). Barcelona, August 31th, 2006.*

- *Mesa Redonda: "Ingeniería de Software, Ingenieria Web y Supercomputación: Impactos en la Sociedad el Conocimiento". Sisoft, Simposio Internacional de Sistemas de Información e Ingeniería del Software en la Sociedad del Conocimiento. Cartagena de Indias, 23-25 de Agosto de 2006.*

- *"Evolución de la Arquitectura de los Computadores". Invited lecture. Sisoft, Simposio Internacional de Sistemas de Información e Ingeniería del Software en la Sociedad del Conocimiento. Cartagena de Indias, 23-25 de Agosto de 2006.*

- *"Supercomputers: Mare Nostrum". Keynote Lecture. Sisoft, Simposio Internacional de Sistemas de Información e Ingeniería del Software en la Sociedad del Conocimiento. Cartagena de Indias, 23-25 de Agosto de 2006.*

- *"Límites de la Supercomputación". Renacer 2006. Conferencia invitada. Barcelona Julio 2006.*

- *"Mare Nostrum y Supercomputación". Cursos de verano de la Universidad Complutense. Curso "Nuevo Modelo de Instalaciones Científico-Tecnológicas: Un Mapa Nacional pactado a 15 Años". San Lorenzo de El Escorial, 5-7 Julio 2006.*

- *"Supercomputación: Pasado, Presente y Futuro". Cursos Internacionales de Verano. Universidad de Extremadura. Curso sobre "Las fronteras de la Supercomputación: Desde la Grid hasta la Computación Cuántica". Mérida, 3-5 de Julio de 2006.*

- *"Barcelona Supercomputing Center: Mare Nostrum". Jornada sobre Instalaciones Científicas Singulares, organizada por el Club Español de la Industría Tecnológica y Minería. Madrid, 22 de Junio de 2006.*

- *"High Performance Computers" Invited Talk. TAM 2006. Transnational Access Meeting. HPC-Europa. Barcelona, June 14th. 2006.*

- *"Supercomputers: Mare Nostrum". Invited talk. Campus de Excelencia. Aula de Nuevas Tecnologías. Fuerteventura, May 2006*

- *"Barcelona Supercomputing Center: a Government/Industry/University partnership". Invited Lecture. IBM Academic Days Conference. Barcelona, May 2006.*

- *"BSC: Barcelona Supercomputing Center". Talk at the BSC-CSC meeting. Barcelona, May 2006*

- *"Supercomputers: MareNostrum" Invited speaker at the Theatre of the CPS (Centro Politecnico Superior) in Zaragoza, April 2006.*

- *"Supercomputers: MareNostrum" Computer Science Faculty ot the Universidad Complutense de Madrid, March 24th. Conference Cicle in Applied Computing.*

- *"The Supercomputers, those crazy machines" Life Science programme of the Department of Education and Science of the Aragon Government. Instituto Ramon y Cajal, Huesca, March 15th 2006.*

- *"Kilo-instruction Processors: Overcoming the Memory Wall". Distinguished lecture. University of Irvine at California, February 19th, 2006.*

- *"BSC: Barcelona Supercomputing Center". MareNostrum Access Comittee. Barcelona, February 2006.*

- *"BSC: Barcelona Supercomputing Center". Cercle de la FIB, Facultad de Informática de Barcelona. Barcelona, December 20, 2005.*

- *"BSC: Barcelona Supercomputing Center". Invited lecture. 1st Workshop on Scientific Cases for European HPC Initiative. Barcelona, November 28-29, 2005.*

- *"Los Supercomputadores, esos locos cacharros". XV Ciclo de Divulgación Científica. Organiza dp por Caja de Burgos. Burgos, 3 de Noviembre de 2005.*

- *"Supercomputing in the Grid". EGEE05. Invited Lecture. EGEE, Enabling Grid for E-Science, fourth conference. Pisa, Italy, October 25th, 2005*

- *"La Investigación, generadora de Bienestar Social". III Jornades de Tecnología, Escola i Futur. Escola Universitaria d'Eninyeria Tècnica Industrial de Terrassa, UPC. Terrassa, 19 de Octubre de 2005.*

- *"Research on Computer Architecture at UPC". Universidad Veracruzana, México. Xalapa, 12 de Octubre de 2005.*

- *"Evolución de las Tecnologías Básicas de la Sociedad del Conocimiento". Universidad Veracruzana, México. Xalapa, 13 de Octubre de 2005.*

- *"Supercomputadores: Pasado, Presente y Futuro". Universidad Veracruzana, México. Veracruz, 14 de Octubre de 2005.*

- *"BSC:Barcelona Supercomputing Center". JOCS: Jornada Catalana de Supercomputación. Universitat Rovira i Virgili. Tarragona, 5 de Octubre de 2005.*

- *"BSC: Barcelona Supercomputing Center". Grandes usuarios de Supercomputación. BSC, Barcelona, Octubre de 2005.*
- *"BSC: Barcelona Supercomputing Center". Centre de Recerca Genòmica. BSC, Barcelona Octubre 2005.*
- *"BSC: Barcelona Supercomputing Center". Fundación Valenciana de Estudios Avanzados. Reunión de las Comisiones de los Premiados Rey Jaime I en Investigación y Nuevas Tecnologías. Alto Consejo Consultivo en I+D+i de la Presidencia de la Generalitat Valenciana. Valencia, 9 de Septiembre de 2005.*
- *"La Supercomputación: Herramienta para la Investigación". Keynote Lecture. Sisoft, Simposio Internacional de Sistemas de Información e Ingeniería del Software en la Sociedad del Conocimiento. Santo Domingo, 24 de Agosto de 2005.*
- *"Introducción a la Arquitectura de los Computadores". Seminario. Sisoft, Simposio Internacional de Sistemas de Información e Ingeniería del Software en la Sociedad del Conocimiento. Santo Domingo, 24 de Agosto de 2005.*
- *"Introducción a la Supercomputación". Curso de formación. ITLA, Instituto Tecnológico de las Americas. Santo Domingo, 23 de Agosto de 2005.*
- *"Implementing Kilo-Instruction Multiprocessors". Invited lecture. IEEE Conference on Pervasive Services, ICPS-05. Santorini, Greece. July 11-14, 2005.*
- *"BSC: Barcelona Supercomputing Center". Sesion sobre e-Ciencia en España. Jornadas españolas de e-Ciencia. Santiago de Compostela, Spain. July 9, 2005*
- *"Investigación e Innovación". SPOC, Barcelona, 30 Junio, 2005.*
- *Beyond Moore´s Law". Invited Talk. Campus de Excelencia 2005. Universida de Las Palmas de Gran Canaria. Gran Canaria, June 11th 2005.*
- *"Beyond Moore´s Law". IGC 2005. Internet Global Conference. Invited talk. Barcelona, June 9th, 2005.*
- *"BSC: Barcelona Supercomputing Center". Mesa redonda dentro del congreso WINCO´05. Workshop on The mutual beneficts of International CO-operation in IST Research for Latin Americam countries and The European Comission. IPN, Instituto Politécnico Nacional. México D.F., México, 14 de Abril de 2005.*
- *"La Investigación, generadora de Riqueza". Keynote talk. WINCO´05. Workshop on The mutual beneficts of International CO-operation in IST Research for Latin Americam countries and The European Comission. IPN, Instituto Politécnico Nacional. México D.F., México, 14 de Abril de 2005.*
- *"Investigación y Redes de Excelencia". CIC, Centro de Investigación en Computación. México D.F., México, 13 de Abril de 2005.*
- *"Procesadores VLIW y Multithreaded". Curso de doctorado en Ingeniería del Software de la Universidad Pontificia de Salamanca. Majadahonda, Madrid, 9 de Abril de 2005.*
- *"Procesadores Superescalares". Curso de doctorado en Ingeniería del Software de la Universidad Pontificia de Salamanca. Majadahonda, Madrid, 8 de Abril de 2005.*
- *"BSC: Barcelona Supercomputing Center". Universidad de Las Palmas de Gran Canaria, 15 de Marzo de 2005.*
- *"Los Supercomputadores, esos increibles y locos cacharros". Fundación "Zaragoza Ciudad del Conocimiento". Salón de Actos del Museo del Teatro de Caesaraugusta. Zaragoza, 8 de Febrero de 2005*
- *"Barcelona Supercomputing Center: Centro Nacional de Supercomputación". II Jornada para el profesorado de Secundaria. Universidad Politécnica de Catalunya, Febrero 2005.*

- *"Advanced Computer Architectures". Invited lecture. Future Emerging Technology Initiative. Information Society Technologies Program. Brussels, January 13th, 2005.*
- *"Kilo-instruction Processors". Invited lecture. Seminar on Computer Architecture. IBM Watson, December 9th, 2004.*
- *"BSC: Barcelona Supercomputing Center". Conferencia invitada. V Jornadas de Bioinformática. Barcelona, 1 de Diciembre de 2004.*
- *"BSC: Barcelona Supercomputing Center". DEISA Workshop. La Haya. November 23th, 2004.*
- *"BSC: Barcelona Supercomputing Center". Encuentros sobre Ciencia y Cultura, organizados por la Delegación en Madrid de la Generalitat de Catalunya y el Institut Ramon Llull. Sesión sobre: Supercomputadores y Ciencias de la Vida. Centro Cultural Blanquerna. Madrid, 16 de Noviembre de 2004,*
- *"Centro de Supercomputación de Barcelona". Jornada sobre "El Supercoputador, Què és i per a què serveix". Organizada por el INEC (Institut dÉnginyers de Catalunya" en la UPC. Barcelona, 16 de Noviembre de 2004.*
- *"BSC: Barcelona Supercomputing Center". IBM Seminar. Hotel Fira Palace. Barcelona, November 3th., 2004*
- *"Procesadores Power-4 y Powe-5". IBM Seminar. Hotel Fira Palace. Barcelona, November 3th., 2004*
- *"BSC: Barcelona Supercomputing Center". Conferencia invitada. Jornadas Técnicas de redIRIS. Toledo, October 29th., 2004.*
- *"Procesadores Power-4 y Powe-5". IBM Seminar. Casino de Madrid, October 28th., 2004.*
- *"BSC: Barcelona Supercomputing Center". IBM Seminar. Casino de Madrid, October 28th., 2004.*
- *"Centro Nacional de Supercomputación". Barcelona Activa. Barcelona, 21 de Octubre de 2004.*
- *"Aplicaciones de la Supercomputación". Facultad de Informática de Madrid, 7 de Octubre de 2004.*
- *"Centro Nacional de Supercomputación". Congreso Galiciatic. Santiago de Compostela, 6 de Octubre de 2004. Congreso Galiciatic. Santiago de Compostela, 6 de Octubre de 2004.*
- *"Aplicaciones de la Supercomputación". Congreso Galiciatic. Santiago de Compostela, 6 de Octubre de 2004. Congreso Galiciatic. Santiago de Compostela, 6 de Octubre de 200*
- *"Centro Nacional de Supercomputación". Congreso Galiciatic. Santiago de Compostela, 6 de Octubre de 2004. Congreso Galiciatic. Santiago de Compostela, 6 de Octubre de 2004.*
- *"La Investigación en España: El triángulo Administración-Universidad-Empresa". Mesa Redonda organizada por BP y la Fundación Valenciana de Estudios Avanzados. Universidad Jaume I, Castellón, 27 de Septiembre de 2004.*
- *"Kilo-instructions Processors". Invited lecture. SIGMICRO Online Seminar. Esta charla fue dada a través de Internet para todo el mundo. Fue la segunda persona que la dió. Septiembre, 14th. 2004.*
- *"Kilo-instructions Processors". Keynote lecture. Europar Conference. Pisa, August-September, 2004.*

- *"Throughput versus Quality of Service in SMT processors". Invited lecture. Euromicro-DSD (Digital System Design). Rennes, August-September 2004.*
- *"Arquitectura de los Computadores y Supercomputadores del Futuro". Charla invitada. Curso de verano de la Universidad de La Gomera sobre "Bases de la Evolución Tecnológica y el Futuro de la Computación". La Gomera, 30 de Julio de 2004.*
- *"Arquitectura de los Computadores y Supercomputadores del Futuro". Charla invitada. Curso de verano de la Universidad de Las Palmas de Gran Canaria "Bases de la Evolución Tecnológica y el Futuro de la Computación". Aula de Agaete, Julio 19, de 2004.*
- *"La llegada a España de un Supercomputador". Conferencia Invitada dentro del curso de verano sobre periodismo científico de la Universidad Autónoma de Madrid, El Pais y el BBVA. "La Ciencia española ante su gran oportunidad". Alameda del Valle, Madrid, 12 de Julio 2004.*
- *Miembro de la mesa redonda: "La arquitectura de computadores en ele nuevo marco europeo", dentro del curso "Una ventana abierta hacia el futuro de la Arquitectura de los Computadores". Cursos de verano de la Universidad de Castilla La Mancha. Albacete, 7-9 de Julio de 2004.*
- *"Procesadores del Futuro". Conferencia invitada dentro del curso "Una ventana abierta hacia el futuro de la Arquitectura de los Computadores". Cursos de verano de la Universidad de Castilla La Mancha. Albacete, 8 de Julio de 2004.*
- *"Mi vida como investigador". Conferencia inaugural del Master en Gestión y Política Universitaria. Fundación Universidad Politécnica de Catalunya. Barcelona, 5 de Julio de 2004.*
- *"Investigación y Redes de Excelencia Europeas". Conferencia invitada dentro del acto de conmemoración de "Solemne Processión de 100 Tesis. Universidad Pontificia de Salamanca, Campus de Madrid. Junio de 2004.*
- *"La Evolución de las Tecnologías Básicas de las TIC". Maratón: El Internet del Futuro y el Futuro de Internet". Museo Nacional de Ciencia y Tecnología. Madrid, 20 de Mayo de 2004.*
- *"Supercomputers". IBM meeting. Barcelona, May 17, 2004.*
- *"Kilo-instructions Processors". Keynote lecture. Europar Conference. Pisa, August-September, 2004.*
- *"Future Computer Architectures". Cursos de verano de la Universidad de Castilla La Mancha. Albacete, 7-9 de Julio de 2004.*
- *"Kilo-instruction Processors". Distinguish Lecturer at the Mexican ACM Student Chapters Annual Meeting at Ciudad de Madero, México. May 10-12, 2004.*
- *"Kilo-instructions Processors". Keynote lecture. CORE-2004. National Conference on Computation. Mexico city, Mexico. May 10., 2004.*
- *"La Supercomputación del Futuro". Conferencia Invitada. Jornada Académica en Postgrado de Investigación en Ingeniería Informática. Universidad Pontificia de Salamanca. Salamanca, 7 de Mayo de 2004.*
- *"Orientation of Basic Research in the FP7, 2007-2010". Workshop organized by FET (Future Emerging Technology". Brussels, April 21, 2004.*
- *"Kilo-instructions Processors". Keynote lecture. HPCA-10. IEEE, International Symposium on High Performance Computer Architecture. Madrid, February 14-18, 2004.*
- *"A Content Aware Integer Register File for Superscalar Registers". Invited lecture. University of Ghent. December 15th. 2003.*

- *"A Content Aware Integer Register File for Superscalar Registers". Invited lecture. Intel MRL (Microprocessor Research Laboratory) Haifa, Israel. November 9th., 2003*

- *"Kilo-instructions in-flight Processors". Invited lecture. Intel MRL (Microprocessor Research Laboratory). Haifa., Israel. November 12th., 2003*

- *"Kilo-instructions in-flight Processors". Keynote lecture. Seminar on Compilers and Architecture. IBM Haifa. November 11, 2003.*

- *"A Content Aware Integer Register File for Superscalar Registers". Invited lecture. Technion University. Haifa, Israel. November 9, 2003.*

- *"Kilo-Instruction Processors". ISHPC-V. The 5th International Symposium on High Performance Computing. Tokyo, Japan, October 20-22, 2003.*

- *"Kilo-Instruction Processors". PA3CT Conference: Program acceleration by Application-driven and architecture-driven Code Transformations. Edegem, Belgium, Spetmeber 22-23, 2003.*

- *Panel Session "Research on Hihf Performance Architectures". Jornadas de Paralelismo. Universidad Carlos III, Leganés, Madrid, September 15-17th, 2003.*

- *"Computer Architectures for the Future". Keynote Lecture. CISIC-2003. II Congreso Internacional de Sociedad de la Internacional de la Información y el Conocimiento. Madrid, May 7th. 2003.*

- *"Evolución de la Arquitectura de Computadores". Matefest. Facultad de Matemáticas. Universidad de Barcelona, 24 de Abril de 2003.*

- *"Evolución de los Procesadores, de las Memorias y de las Comunicaciones". Universidad de Murcia, 26 de marzo del 2003.*

- *"Evolución de los Procesadores, de las Memorias y de las Comunicaciones". 7th. Seminar in Last Advances in Computer Science: processors, Memories and Programming. Universidad de La Laguna. La Laguna, Tenerife, March 13th., 2003.*

- *"Evolución de los Procesadores, de las Memorias y de las Comunicaciones". Seminar on Computer Architecture. Universidad de Las Palmas de Gran Canaria. Las Palmas, March 11th., 2003.*

- *"Arquitectura de los Procesadores del Futuro". Universidad de Santander, ETSI de Telecomunicación. Marzo del 2003.*

- *"Pasado, presente y futuro de los procesadores, de las memorias y de las comunicaciones". Universidad de Santander, Facultad de Física. Marzo del 2003.*

- *"Arquitectura de los Procesadores del Futuro". Universidad de Zaragoza. Marzo 7th., 2003*

- *"Research on Computer Architecture at UPC". Aula de Empresas. IBM. Facultad de Informática de Barcelona. Febrero 6th, 2003.*

- *Past, Present and Future of Computers, Storage and Communications". Invited Lecture. Inaugural Lecture of the year 2003 of the Spanish Academy of Engineering. Madrid, January 30th, 2003.*

- *"Architecture of the IBM Power4". IBM Seminar. Madrid, November 2002*

- *"Computer Architectures for the Future". Invited Lecture. Seminar "Recent Advances in Computer Science". University of La Laguna, Tenerife, November, 2002.*

- *"Research collaboration between Universities and companies", organized by Cercle per al Coneixement. Barcelona, November, 2002.*

- *Panel session "Oportunity for an European Research Council", organized by CRUE (Council of the Presidents of the Spanish Universities). Madrid, November, 2002.*

- *"Will vector ISA survive in the future?" Keynote Lecture. 14th. Symposium on Computer Architecture and High Performance Computing. October 28-30. Vitoria Espirito Santo, Brasil.*

- *"Architecture of the IBM Power4". IBM Seminar. Barcelona, July 2002.*

- *"Computers for the Future". PhD course. University of Murcia, Spain. June 2002.*

- *"Research Topics on Computer Architecture". PhD course. University of Murcia, Spain. June 2002.*

- *"Procesors for the Future". Invited Lecture. SGI Seminar on High Performance Computing. Madrid, June 11th. 2002.*

- *"Computación del Futuro". Keynote Lecture. CISIC. I Congreso Internacional de la Sociedad de la Información. Las Palmas de Gran Canaria. 28 de Febrero de 2002.*

- *Panel session "Research at UPC". Universidad Politécnica de Catalunya. January 2002.*

- *"Arquitectura de los Computadores del Futuro". Universidad de Las Palmas de Gran Canaria, 19 de Diciembre de 2001.*

- *"Investigación y Financiación de la Investigación en Arquitectura de Computadores y en Supercomputadores". Programa Innova, UPC. Diciembre de 2001.*

- *"Arquitectura de los Computadores del futuro". Col.legi Major Universitari Pedralbes. Barcelona, Noviembre de 2001.*

- *"Computadores del Futuro". Invited Talk. Seminar "HAL 9000. Realities and Utopias of the Artificial Intelligence" at the Menéndez Pelayo International University. Valencia, Spain, October 19th. 2001.*

- *Panel session "Limits or no limits of the Artificial Intelligence". Seminar " HAL 9000. Realities and Utopias of the Artificial Intelligence" at the Menéndez Pelayo International University. Valencia, Spain, October 18th. 2001*

- *"Research on High Performance Computers at UPC". Invited Talk. CICINDI 2001. III International Conference on Control, Virtual Instrumentation and Digital Systems. Mexico D.F. August 27-31th. 2001.*

- *"Computer Architectures for the Future". Keynote Lecture. CICINDI 2001. III International Conference on Control, Virtual Instrumentation and Digital Systems. Mexico D.F. August 27-31th. 2001.*

- *"Code Generation and Register File Organization for Future VLIW Architectures". Invited Talk. IWACT, International Workshop on Advanced Compiler Technology for High Performance and Embedded Processors. Bucharest, Romania, July 18-20 , 2001.*

- *Panel session on "New Architectures and their Compilers". IWACT, International Workshop on Advanced Compiler Technology for High Performance and Embedded Processors. Bucharest, Romania, July 18-20 , 2001.*

- *"Future High-Performance Microprocessors". Keynote Lecture. ASCI (Advanced School for Computing and Imaging). Heijen, The Netherlands, May 30th. 2001.*

- *"Research on Computer Architecture at UPC". University of Leiden, Holland. May 29th, 2001.*

- *"Research on Computer Architecture at UPC". University of Delft, Holland. May 28th, 2001.*

- *"Vector Processors: Past, Present and Future". UPC, Barcelona. May 31th, 2001.*

- *"Computadores del Futuro". Agora para la Ciencia. Residencia de Estudiantes. CSIC. Madrid, May 10th. 2001.*

- *"Research on Computer Architecture at UPC". Université de Paris-Sud. LRI, Laboratoire de Recherche en Informatique. Paris. April 26th. 2001.*

- *"Research on Computer Architecture at UPC". IMEC, Belgium. March 2-th, 2001.*

- *"Computers for the Future". Invited talk. Jornada sobre "Nuevos Encuentros", organizada por IQS(Facultat D'Economia) y por la Fundación Germán Sánchez Ruipérez. Barcelona, 22 de Febrero del 2001.*

- *"Closing the Semantic Gap", "Future Vector Architectures for Multimedia Applications" and "Compilers and Register File Organizations for VLIW Architectures". Intel Microprocessor Research Laboratories (MRL). Santa Clara, Dec. 2000.*

- *"Parallel Computers: Past, Present and Future". ETSE de Telecomunicación. Barcelona, Dec. 2000.*

- *"Research on Computer Architecture at UPC". University of Manchester, November 23th. 2000.*

- *"Past, Present and Future of High Performance Microprocessors". Centre for Novel Computing (CNC) 10th. Birthday Symposium. University of Manchester. Nov. 24 2000.*

- *"Research on Computer Architecture at UPC". University of Tokyo, October 17th. 2000.*

- *"Architectures for one Billion of Transistors". Invited Workshop. ISSS-2000. IEEE-ACM International Synposium on System Synthesis. Madrid, September 20th. 2000.*

- *"Computación de Altas Prestaciones II: Arquitectura, Compiladores, Sistemas Operativos y Aplicaciones". I Seminario del Programa Nacional de Tecnologías de la Información y las Comunicaciones. Almagro (Ciudad Real), Septiembre del 2000.*

- *"Processors for the Future". Keynote Talk. Hewlett-Packard High Performance Computing European User Group. Barcelona, Spain. September 2000.*

- *Panel Session on "Where are you going High Performance Computing. Hewlett-Packard High Performance Computing European User Group. Barcelona, Spain. September 2000.*

- *Panel Session on "Research Topics on Computer Architecture". XI Spanish Meeting on Parallel Computing. Sept. 11th. 2000. Granada (Spain).*

- *"Research on High Performance Computing at UPC". University of Paul Sabatier. Toulouse, July 2000.*

- *"Spill Code and Register File Organization for VLIW Architectures". PhD Program (3 hours). Centro Politécnico de Zaragoza. Universidad de Zaragoza, July 2000.*

- *"Vector Architectures for Media Applications". PhD Program (3 hours). Centro Politécnico de Zaragoza. Universidad de Zaragoza, July 2000.*

- *"Computadores del Futuro". Foro Complutense. Universidad Complutense de Madrid. Mayo 2000.*

- *"Research on Computer Architecture at UPC". Universidad Complutense de Madrid. Mayo, 2000.*

- *"Computadors per al proper mil.leni". Instituto Isabel de Aragón. Barcelona, May 2000.*

- *"Research on Computer Architecture at UPC". Georgia Institute of Technology. Atlanta, April 2000.*

- *"High Performance Computing Research at UPC". IBM Watson Research Institute. March 22th, 2000.*
- *"Recent Contributions on ILP at UPC". Universidad de Santander. February 14th. 2000.*
- *"Computadores del Futuro". Mundo Oficina 2000. Las Palmas de Gran Canaria. February 4th. 2000.*
- *"Computadores del Futuro". Invited talk. Congreso "Foro Siglo XXI : Ciencia, Tecnología y Empresa". Barcelona, 20-21 de Enero del 2000.*
- *"Research on Computer Architecture at UPC". Invited talk at University of Gent. 17th December 1999.*
- *"Register File Use and Organization for Future Superscalar Processors". Invited lecture. Int. Symposium on New Trends in Computer Architecture". Gent, 16th December 1999*
- *"Software-Hardware Trace Cache". Facultad de Informática. Las Palmas de Gran Canaria. 26 de Noviembre de 1999*
- *"Uso y organización de los bancos de registros en Procesadores Superescalares del Futuro" Facultad de Informática. Universidad de las Palmas. 26 de Noviembre de 1999*
- *"Computadors per al proper mil.leni". JOC´S 99. Jornadas de Supercomputación. Barcelona, 28 de Octubre de 1999*
- *"Software-Hardware Trace Cache". Computer Science Department. Universidad de Texas en Austin, 18 de Octubre de 1999*
- *"Register File Use and Organization for Future Superscalar Processors". Electrical and Computer Engineering Department. Universidad de Texas en Austín, 18th October 1999*
- *"High Performance Instruction Fetch Through a Software/Hardware Cooperation". Electrical and Computer Engineering Department. Universidad de California en Irvine, 11 de Octubre de 1999*
- *"Software Trace Cache". Computer Science Department. Universidad Urbana Champaign, Illinois 22th July 1999*
- *"Procesadores vectoriales para aplicaciones multimedia". PhD Program (3 hours). Centro Politécnico de Zaragoza. Universidad de Zaragoza, 7 de Julio de 1999*
- *"Mecanismos para búsqueda de instrucciones para los procesadores del futuro". Curso de Doctorado (3 h). Centro Politécnico de Zaragoza. Univ. de Zaragoza, 8 de Julio de 1999*
- *"The future of Vector Processors". Keynote talk ISHPC. "International Symposium on High Performance Computers". Nara, Japón, 28th May 1999*
- *"Computadores para el próximo milenio". Col.legi Major Universitari Pedralbes. Barcelona, 17 de Mayo de 1999*
- *"Arquitectura de Computadores para el próximo milenio". Dentro de las Jornadas "Alpha Day", organizadas por Compaq. Barcelona, 18 de Marzo de 1999*
- *Miembro del Panel "High performance Computer Architecture-Why?". HPCA-5. IEEE "International Symposium on High Performance Computer Architecture". Orlando, Florida, 11th January 1999*
- *"Evolución de los Procesadores Vectoriales". Universidad de La Laguna, 4 Dic. 1998*
- *"The evolution of vector processors". Invited talk. SC-98. IEEE-ACM "Supercomputing Conference". Orlando, 11 de Noviembre de 1998*

- *"Supercomputadores del Futuro". Jornada d´Experimentació en Noves Tecnologies de Supercomputació. CESCA-IBM-IDESCAT. Barcelona, 15 de Sepiembre de 1998*

- *"Redes de Centros de Supercomputación". IX Jornadas de Paralelismo. Donostia, 3 de Sept. de 1998.*

- *"Vector Architectures: Past, Present and Future". Keynote talk. ICS-11. IEEE-ACM "International Conference on Supercomputing". Melbourne, 17th July 1998*

- *"Supercomputers and Simulations". Pannel Session. Congreso AUTO test'98", IDIADA. Barcelona, 17 de Junio de 1998*

- *"Arquitectura de los Computadores de Altas Prestaciones". Curso de doctorado de "Métodos Numéricos para la Ingeniería". Escuela Técnica Superior de Ingenieros de Caminos. Barcelona, 16 de Junio de 1998*

- *"Vector Architectures: Past, Present and Future". Curso de doctorado. Centro Politécnico de Zaragoza. Zaragoza, 12 de Junio de 1998*

- *"Diseño de los procesadores del futuro". Curso de doctorado (3 horas). Centro Politécnico de Zaragoza. Zaragoza, 11 de Junio de 1998*

- *"Procesadores del futuro". Universidad de La Laguna. Tenerife, 9 de Enero de 1998*

- *"Procesadores del futuro". Universidad de Las Palmas. Las Palmas, 8 de Enero de 1998*

- *"Has Exploitable ILP reached the Point of Diminishing Returns?". Mini-Panel on ILP. High Performance Computing Conference. Bangalore, India. 19-21th December 1997*

- *"Evolución de la Arquitectura de los Procesadores". Entrega de títulos de la EUPBL. Sant Just, 10 de Diciembre 1997*

- *"Computadores del Futuro". Seminario HP-Convex sobre "Supercomputación : Plataformas, Arquitecturas y Herramientas de Desarrollo". Madrid, 28 de Octubre de 1997*

- *"Advances in Computer Systems and Selected Research Results from UPC". Keynote talk INFOFEST 97. Festival of Information Technology Achievements. Budva. Montenegro. 28 de Sep-4 de Oct 1997*

- *"Los límites de los Computadores del Futuro". Invited talk. Curso de Verano de la Universidad Complutense "Redes del hilo de cobre a las plataformas digitales". El Escorial, 1 Agosto 1997*

- *"Introducción a la Supercomputación ". Aula de Supercomputación .CESCA. Barcelona, 15 Jul 1997.*

- *"Vector Processors and IRAM". Workshop on Mixing Logic and DRAM: Chips that compute and remember. ISCA-24. IEEE-ACM "Int. Symposium on Computer Architecture". Denver, May 1997*

- *"Formación de los Ingenieros en el Siglo XXI". Acto entrega de diplomas de Final de Carrera en el Centro Politécnico de Zaragoza. Zaragoza, 30 de Mayo de 1997*

- *"Supercomputadores: Pasado, Presente y Futuro". Ateneo de la Universidad de Zaragoza. Zaragoza, 29 de Mayo de 1997*

- *"Arquitectura de los Supercomputadores". Universidad de Santiago de Compostela. Santiago de Compostela, 25 de Abril de 1997*

- *"Vector Architectures for the Future". Ecole Polytechnique Federale de Lausanne (EPFL), April, 3. 1997*

- *"Evolución de la Arquitectura de los Computadores". Curso de 4 horas en el Instituto Politécnico de México. Proyecto PARNET de la iniciativa Comunitaria Alfa. México D.F., 20 y 21 de Marzo de 1997*

- *"Supercomputadores: Pasado, Presente y Futuro". Universitat Oberta de Catalunya. Reunión de la Academia de Ingeniería de España. Barcelona, 18 de Marzo de 1997*

- *"Procesadores del Futuro". Seminario sobre "Futuro de la Computación ". Universidad de La Laguna, 5-8 Octubre 1996*

- *"Procesadores del Futuro". Facultad de Informática, Universidad de Las Palmas. Las Palmas de Gran Canaria, 4 de Octubre de 1996*

- *"High Performance Computer Architectures". Invited talk. Euroconference "Supercomputation in Nonlinear and Disordered Systems: Algorithms, Applications and Architectures". San Lorenzo del Escorial, Madrid, September 23-27, 1996.*

- *"Procesadores del Futuro". Facultad de Informática de La Coruña, 10 Sep. 1996*

- *"Arquitecturas para los Supercomputadores". Tutorial de 6 horas dentro de las II Jornadas de Informática y de la XVIII Escuela de Verano de Informática. Almuñecar (Granada), 15-18 de Julio de 1996*

- *"Centros de Computación en Paralelismo". Codelco. Santiago de Chile. 7 Nov. 1995*

- *"Architectures and Compilers for Parallel Computers". Tutorial dentro del "XV International Conference of the Chilean Computer Science Society". Arica (Chile), 2 de Noviembre de 1995*

- *"Research on High Performance Computers at UPC". Universidad de Santiago de Chile, October 30th. 1985.*

- *"Aplicaciones de los Supercomputadores". Conferencia en el Acto de Entrega Oficial de los Premios a la Mejor Tesis Doctoral y al Mejor Proyecto Fin de Carrera. Colegio Oficial de Ingenieros de Telecomunicación (COIT). Madrid, 24 de Octubre de 1995*

- *"Arquitecturas de los Supercomputadores: Pasado, Presente y Futuro". Universidad de Santiago de Compostela. Santiago de Compostela, 23 de Octubre de 1995*

- *"Centros de Paralelismo". Jornada de I+D en Tecnologías de la Información (Esprit IV). Barcelona, 19 de Octubre de 1995*

- *"High Performance Computer Architectures: Past, Present and Future" . Keynote talk. IBM, SUPEUR95 "High Performance Computing in Europe". Madrid, 25-27 de Septiembre de 1995*

- *"Arquitecturas Vectoriales Desacopladas". Curso de Verano sobre el tema "Supercomputadores", organizado por la Universidad Complutense. Roquetas, Almería. 24-29 de Julio de 1995.*

- *"Aplicaciones de los Supercomputadores". Instituto Astrofísico de Canarias. Las Palmas, 19 Julio 1995*

- *"Evolución de las Arquitecturas de los Computadores de Altas Prestaciones". I Jornadas de Informática. Sta. Cruz de Tenerife. 17-21 de Julio de 1995*

- *"Arquitecturas y Compiladores para Supercomputadores". Universidad de Cantabria. ETSI de Telecomunicación. 26 de Mayo de 1995*

- *"Arquitecturas de Alto Rendimiento para Aplicaciones Numéricas". Universidad de Zaragoza. Centro Politécnico. Zaragoza, 25 de Mayo de 1995*

- *"Understanding Modern Computer Architectures: An Introduction". Curso Comett. Barcelona, 8-10 de Mayo de 1995*

- *"La Recerca en Supercomputació". Col.legi Major Universitari Pedralbes. Barcelona, Mayo 1995*

- *"Arquitecturas para Altas Prestaciones". VII Las Palmas Seminar on Computer Sciences "Advanced Computer Architectures". Las Palmas de Gran Canaria, 21-28 Abril de 1995*

- *"Pasado, Presente y Futuro de las Arquitecturas de los Supercomputadores". Escuela de Ingeniería Informática. Universidad de Granada. Granada, 2 de Diciembre de 1994*

- *"Conceptos Avanzados de Arquitectura de Computadores". Curso Doctorado, 15 horas. Deusto, Diciembre de 1994*

- *"Arquitecturas para Supercomputadores" Curso de Postgrado. Universitas Nebrissensis. Madrid, Diciembre de 1994*

- *"High Performance Computer Architectures". CE Programme on Human Capital & Mobility HPC: Current Trend and Applications. Barcelona, Noviembre de 1994*

- *"Evolución de las Arquitecturas de los Computadores para Altas Prestaciones". Universidad de La Laguna. Facultad de Informática. La Laguna, 23 Nov. de 1994.*

- *"Acceso Eficiente a Vectores en Sistemas Vectoriales Multiprocesador". Universidad de La Laguna, 25 años de Matemáticas. Facultad de Matemáticas. La Laguna, 22 Nov. 1994*

- *"Memory Access Synchronization in Vector Multiprocessors". V Jornadas de Paralelismo. La Alpujarra, 20-23 de Septiembre de 1994*

- *Panelist of the session: "Predicting the Direction of Supercomputing: Last Performance and Future Expectations". ICS-8. ACM "International Conference on Supercomputing". Manchester, July 11, 1994*

- *"Problemas de Paralelismo en términos de teoría de grafos". Seminari de Combinatoria, Teoria de Grafs i Aplicacions. Departament de Matemática Aplicada. UPC. Barcelona, 16 de Junio, 1994*

- *Curso "Supercomputación ". Proyecto ETSIT de Teleenseñanza. Curso de 9 horas impartido desde Madrid y por satélite a 6 Escuelas de Ingenieros de Telecomunicación Madrid, 17-19 de Mayo de 1994.*

- *"Access to Memory in Vector Multiprocessors". Keynote Speaker. Workshop on Efficient use of Parallel Computer: Architecture, Mapping and Communication. EU. Human Capital & Mobility Program. Barcelona, Abril de 1994*

- *"Efficient Access to Streams in Multi-Module Memories". Keynote Speaker. Euromicro Workshop on Parallel and Distributed Processing. Málaga, 26-28 de Enero de 1994.*

- *Curso COMETT "System Software for Parallel Computers". Barcelona, 16-19 de Noviembre de 1993*

- *Curso COMETT "An Introduction to Parallel Processing, Hardware Platforms and Software Environments". Barcelona, 20-22 de Octubre de 1993*

- *"Architectures and Algorithms for Parallel Computers". Invited talk. Colaboración con Jesús Labarta. Specialists meeting on "Adapting Computing Codes in Nuclear Applications to Parallel Architectures". Consejo de Energía Nuclear. Madrid, 14-15 de Octubre de 1993*

- *"Arquitecturas de Supercomputación: Una visión de futuro". Seminario "Sistemas de Supercomputación", organizado por Hewlett-Packard y Convex S.A. Barcelona, 7 de Octubre de 1993*

- *"Arquitecturas de Supercomputación: Una visión de futuro". Seminario "Sistemas de Supercomputación", organizado por Hewlett-Packard y Convex S.A. Madrid, 6 de Octubre de 1993*

- *"Supercomputación mediante Computadores Paralelos". Acto de Inauguración del CEPBA. UPC. Barcelona, 1 de Julio de 1993*

- *Curso "Supercomputación ". Proyecto ETSII de Teleenseñanza. Curso de 10 horas impartido desde Madrid por satélite a tres Escuelas de Telecomunicación . Madrid, 1-4 de Junio de 1993.*

- *"Simulación mediante computadores masivamente paralelos". Jornada sobre "Soporte para la Simulación en Fuerzas Armadas". Escuela de Transmisiones de la Armada. Vigo, 20 de Mayo de 1993.*

- *"Arquitectura de los Computadores para la Simulación ". Sesión COTEC sobre "Identificación de Oportunidades Tecnológicas de la Simulación ".E.T.S.I. Telecomunicación . Vigo, 18 de Mayo de 1993.*

- *"Arquitecturas de los Supercomputadores". Curso de 3 horas dentro del Seminario "Supercomputación Vectorial y Paralela 93" Organizado por el CIEMAT. Madrid, 10-14 de Mayo de 1993.*

- *"Supercomputadores". Col.legi Major Universitari "La Salle". Barcelona, Feb. de 1993*

- *"La Universidad frente al reto de los Supercomputadores". Dentro del Seminario "Situación actual y perspectivas de utilización de los computadores paralelos en España" (coordinado por Prof. Pedro de Miguel). SIMO. Madrid, Octubre de 1992*

- *"Cálculo Paralelo". Curso de 9 horas dentro de la "V Escuela de Otoño Hispano-Francesa sobre Simulación Numérica en Física e Ingeniería". Universidad Jaime I. Benicasim, 28 Sept. - 2 Oct. 1992.*

- *"Arquitectura de los Supercomputadores". Col.legi Major Universitari Pedralbes. Barcelona, Abril de 1992*

- *"Supercomputadores: Arquitecturas, Lenguajes y Algoritmos". Curso de Postgrado en Informática y de Doctorado. Universidad de Zaragoza. Zaragoza, 10-12 Feb. de 1992.*

- *"Arquitecturas para Supercomputadores". Jornadas Técnicas de Supercomputación. Centro Informática Científico de Andalucía. Sevilla, 3-5 de Diciembre de 1991.*

- *"Estado del arte sobre arquitecturas para Supercomputación". Jornada Técnica sobre Supercomputación y Comunicación entre Ordenadores. Sociedad Española de Métodos Numéricos en Ingeniería. Barcelona, 21 de Noviembre de 1991.*

- *"Nested-Loop Partitioning for Shared-Memory Multiprocessor Systems". Int. Workshop on Compilers for Parallel Computers. París, December 1990.*

- *"Arquitectura y Programación de los Computadores de Alta Velocidad". SIMO. Madrid, Nov. de 1990.*

- *"Increasing Systolic Algorithm Granularity for Multicomputers". Invited talk. Second SIAM Conference on Linear Algebra. San Francisco, November 3-5th 1990.*

- *"Parallelism Evaluation and Partitioning of Nested Loops for Parallel Computing". 3rd. Workshop on Programming Languages and Compilers for Parallel Computing. Irvine, California, August 1990.*

- *"Arquitectura de los Supercomputadores".Jornadas sobre computadores, Telefónica Sistemas. Madrid, Febrero de 1990.*

- *"Systematic Design of Two-Level Pipelined Systolic Arrays with Data Contraflow". IEEE "International Symposium on Circuits and Systems". Finland, June 1988.*

- *"Supercomputadors". Col.legi Major Universitari Pedralbes. Barcelona, Marzo de 1988*

- *"LU Decomposition using a Linear Systolic Array". Computer Science Department. UCLA "Universidad de California en Los Angeles", April 1987.*

- *"Efficient Mapping of Some Numerical Linear Algebra Problems on Systolic Array Processors". INRIA-IRISA. Saint-Malo, November 1986.*
- *"DBT-Transformation and Systolic Array Processors". Computer Science Department. UCLA "Universidad de California en Los Angeles", May 1986.*
- *"Pasado, Presente y Futuro de la Informática". Instituto La Salle, Zaragoza. Marzo, 1986.*
- *Seminario "Arquitecturas Avanzadas de Ordenadores". Escuela Universitaria de Informática. Las Palmas de Gran Canaria. 20-22 de Enero de 1986.*
- *"Arquitectura de los Microprocesadores". Facultad de Informática de San Sebastián. Universidad del Pais Vasco. Enero de 1986.*
- *"Arquitectura de los computadores de la 5a. generación". Congreso AEPIA sobre Inteligencia Artificial. Madrid, Diciembre de 1985.*
- *Mesa Redonda "Problemática Actual de las Enseñanzas de Electrónica e Informática" SONIMAG-85. Barcelona, 25 de Septiembre de 1985*
- *"Procesadores RISC y Sistólicos". Entrega premios IEEE Escuela Técnica Superior de Ingenieros de Telecomunicación de Madrid. Madrid. Abril de 1985.*
- *"Arquitecturas de los Computadores Avanzados". Facultad de Informáticas de Las Palmas. Las Palmas de Gran Canaria, Enero de 1985.*
- *Curso "Arquitectura de los Computadores de Alta Velocidad". 10 horas E.T.S.I.T. de Madrid. SICUE. Madrid, 9-13 de abril de 1984*
- *"Redes de Interconexión para Sistemas Multiprocesadores". Facultad de Informática de San Sebastián. Universidad del Pais Vasco. Febrero de 1983.*
- *"Special purpose hardware for signal processing". Invited talk. "Workshop on Signal Processing and its Applications". Vigo, Julio de 1981.*
- *"Diseño de interfaces para Cassettes Analógicos". E.T.S.I. Telecomunicación de Madrid. Madrid, Octubre de 1979.*
- *"Diseño de Interfaces Inteligentes para Discos Flexibles". SATELEC. E.T.S.I. Telecomunicación de Madrid. Madrid, Abril de 1979.*
- *"Mecanismos de bajo nivel para la comunicación en Sistemas Multiprocesadores". SATELEC. E.T.S.I. Telecomunicación de Madrid. Madrid, Abril de 1979.*
- *"Les Systemes Repartis pour les applications in temp rèel". ENSIMAG. Grenoble, December 1978.*

7. Theses

- *Damiá Roca Mari "Analysis and Simulation of Emergent Architectures for Internet of Things". Sobresaliente "Cum Laude".Universitat Politécnica de Catalunya. Febrero, 2018.*
- *Daniel Nemirovsky "Improving Heterogeneous System Efficiency: Architecture, Scheduling, and Machine Learning". Sobresaliente "Cum Laude"Universitat Politècnica de Catalunya. October, 2017.*
- *Leonidas Kosmidis. "Enabling Caches in Probabilistic Timing Analysis". Sobresaliente "Cum Laude". Universitat Politècnica de Catalunya. September, 2017*
- *Milan Stanic. "Design of Energy-Efficient Vector Units for In-Order Cores". Sobresaliente "Cum Laude". Universitat Politècnica de Catalunya. January, 2017*

- *Ivan Ratkovic. "On the Design of Power and Energy-Efficient Functional Units for Vector Processors". Sobresaliente "Cum Laude". Universitat Politècnica de Catalunya. December, 2016*

- *Víctor Jiménez. "Improving the Efficiency of Multicore Systems Through Software and Hardware Cooperation." Sobresaliente "Cum Laude". Universitat Politècnica de Catalunya. September, 2016*

- *Timothy Hayes. "Novel Vector Architectures for Data Management". Sobresaliente "Cum Laude". Universitat Politècnica de Catalunya. July, 2016*

- *Milovan Duric. "Specialization and Reconfiguration of Lightweight Processors for data-Parallel Applications". Sobresaliente "Cum Laude". Universitat Politècnica de Catalunya. February 2016*

- *Qixiao Liu. "Per-task Energy Metering and Accounting in the Multicore Area". To be presented*

- *Leonidas Kosmidis. "Enabling Caches in Probabilistic Timing Analysis". To be presented.*

- *Vesna Novack. "Extending the Applicability of Deterministic Multithreading". Sobresaliente "Cum Laude". Universitat Politècnica de Catalunya. January 2016.*

- *Nikola Markovic. "Hardware Thread Scheduling Algorithms for Single-ISA Asymmetric CMPs". Sobresaliente "Cum Laude". Universitat Politècnica de Catalunya. December 2015*

- *José Carlos Ruiz Luque. "CPU Accounting in Multi-Threaded Processors". Sobresaliente "Cum Laude". Universitat Politècnica de Catalunya. May, 2014.*

- *Bojan Maric. "Cache Designs for Reliable Hybrid High and Ultra-Low Voltage Operation". Sobresaliente "Cum Laude". Universitat Politècnica de Catalunya, May 2014.*

- *Srdjan Stipic. "Techniques for Improving the Performance of Software Transactional Memory". Universitat Politècnica de Catalunya. July, 2014.*

- *Alessandro Morari.. "Scalable System Software for High Performance Large-Scale Applications". Universitat Politècnica de Catalunya. 2013*

- *Alejandro Rico. "Raising the Level of Abstraction: Simulation of Large Chip Multiprocessors Running Multithreaded Applications". Sobresaliente "Cum Laude". Universitat Politècnica de Catalunya. Octubre, 2013.*

- *Augusto Vega. "Performance and Power Optimizations in Chip Multiprocessors for Throughput-Aware Computation". Apto "Cum Laude". Universitat Politècnica de Catalunya. Julio, 2013*

- *Vladimir Subotic. "Evaluating Techniques for Parallelization Tunig in MPI, OmpSs and MPI/OmpSs". Apto "Cum Laude". Universitat Politècnica de Catalunya. Julio 2013*

- *Isidro González. "The Multi-sates Processors". Universitat Politècnica de Catalunya, Departament d´Arquitectura de Computadors. Apto "Cum Laude". October, 2012.*

- *Marco Paolieri. "A Multi-core Processor for Hard Real-Time Systems". Universitat Politècnica de Catalunya, Departament d´Arquitectura de Computadors. Apto "Cum Laude". November, 2011*

- *Felipe Cabarcas Jaramillo. "Castell: A Heterogeneous CMP Architecture Scalable to Hundreds of Processors". Universitat Politècnica de Catalunya, Departament d´Arquitectura de Computadors. Apto "Cum Laude". September, 2011.*

- *Mauricio Alvarez Mesa. "Parallel Video Decoding". Universitat Politècnica de Catalunya, Departament d´Arquitectura de Computadors. Apto "Cum Laude". September, 2011*

- *Ferad Zyulkyarov. "Programming, debugging, Profiling and Optimizing Transactional Memory Programs". Universitat Politècnica de Catalunya, Departament d´Arquitectura de Computadors. Apto "Cum Laude". July, 2011*

- *Friman Sánchez Castaño. "Exploiting Multiple Levels of Parallelism in Bioinformatics Applications. Universitat Politècnica de Catalunya, Departament d´Arquitectura de Computadors. Apto "Cum Laude". Abril, 2011.*

- *Tanausú Ramírez García: "Runahead Threads". Universitat Politècnica de Catalunya, Departament d´Arquitectura de Computadors. Apto "Cum Laude". Abril, 2010.*

- *Miquel Moretó Planas. "Improving Cache Behavior in CMP Architectures through Cache Partitioning Techniques". Universitat Politècnica de Catalunya, Departament d´Arquitectura de Computadors. Apto "Cum Laude", Diciembre 2009.*

- *Jesús Alastruey Benedé. "Renombre de Registros Especulativo". Universidad de Zaragoza. Pto Cum Laude por Unanimidad.Diciembre de 2009.*

- *Ruben González. "Content-Aware Architectures". Universitat Politècnica de Catalunya, Departament d´Arquitectura de Computadors. Apto "Cum Laude", Diciembre, 2009.*

- *Carlos Boneti. "Exploring Coordinated Software and Hardware Support for Hardware Resource Allocation". Universitat Politècnica de Catalunya, Departament d´Arquitectura de Computadors. Apto "Cum Laude", Septiembre, 2009.*

- *Carmelo Acosta. "Heterogeneity-Awareness in Multithreaded Multicore Processors". Universitat Politècnica de Catalunya, Departament d´Arquitectura de Computadors. Apto "Cum Laude", Julio, 2009.*

- *Miquel Pericás. "Affordable Kilo-Instruction Processors". Universitat Politècnica de Catalunya, Departament d´Arquitectura de Computadors. Apto "Cum Laude", Diciembre 2008*

- *Xavier Verdú. "Analysis and Architectural Supprot for Stateful Packet Processing". Universitat Politècnica de Catalunya, Departament d´Arquitectura de Computadors. Apto "Cum Laude", July 2008.*

- *Marco Ramírez. "Low-Power Instruntion Queue for Out-of-Order Processors". Universitat Politècnica de Catalunya, Departament d´Arquitectura de Computadors, July 2007.*

- *Esther Salami. "Optimizing VLIW Architectures for Multimedia Applications". Universitat Politècnica de Catalunya, Departament d´Arquitectura de Computadors. June, 2007.*

- *Carlos Alvarez. "Computación Difusa". Universitat Politècnica de Catalunya, Departament d´Arquitectura de Computadors. Apto "Cum Laude". May, 2007.*

- *Adrián Cristal. "Kilo-Instruction Processors". Universitat Politècnica de Catalunya, Departament d´Arquitectura de Computadors. Apto "Cum Laude", Abril 2006.*

- *Manuel Alejandro Pajuelo González. "Vectorización Especulativa para Procesadores Superscalares". Universitat Politècnica de Catalunya, Departament d´Arquitectura de Computadors. Apto "Cum Laude", Noviembre 2005*

- *Francisco Javier Cazorla Almeida. "Quality of Service for Simultaneous Multithreaded Processors". Universitat Politècnica de Catalunya, Departament d´Arquitectura de Computadors. Apto "Cum Laude", Octubre 2005.*

- *Oliver Santana. "Advanced Stream Prediction". Universitat Politècnica de Catalunya, Departament d´Arquitectura de Computadors. Apto "Cum Laude", Mayo 2005.*

- *Ayose Falcón. "Fetch Improvement Mechanisms for Next-Generation Processors". Universitat Politècnica de Catalunya, Departament d´Arquitectura de Computadors, Apto "Cum Laude", Febrero, 2005.*

- *Daniel Ortega. "Dynamic Instruction Bypassing". Universitat Politècnica de Catalunya, Departament d´Arquitectura de Computadors, Apto "Cum Laude", July 2003.*

- *Teresa Monreal. "Técnicas Hardware para Optimizar el Uso de los Registros en Procesadores Superescalares". Universidad de Zaragoza. Apto "Cum Laude", June 2003.*

- *Jesús Corbal San Adrián "N-Dimensional Vector Instruction Set Architectures for Multimedia Applications". Universitat Politècnica de Catalunya, Departament d´Arquitectura de Computadors, Apto "Cum Laude", July 2002.*

- *Alex Ramírez Bellido "High Performance Instruction Fetch Using Software and Hardware Co-Design". Universitat Politècnica de Catalunya, Departament d´Arquitectura de Computadors, Apto "Cum Laude", July 2002. Premio UPC a la mejor tesis informática del curso 2001-2002.*

- *Francisca Quintana Rodríguez, "Aceleradores Vectoriales para Procesadores Superescalares". Universidad de Las Palmas de Gran Canaria, Departamento de Informática y Sistemas, Apto "Cum Laude", Dec. 2001.*

- *Luis Alfonso Villa Vargas, "Evaluación de Arquitecturas Vectoriales Avanzadas con Registros Cortos", Universitat Politècnica de Catalunya, Departament d´Arquitectura de Computadors, Apto "Cum Laude", 1999.*

- *David López Álvarez, "Recursos anchos: Una técnica de bajo coste para explotar paralelismo agresivo en códigos numéricos y Arquitecturas VLIW", Universitat Politècnica de Catalunya, Departament d´Arquitectura de Computadors, Apto "Cum laude", 1998.*

- *Roger Espasa Sans, "Advanced Vector Architectures", Universitat Politècnica de Catalunya, Departament d'Arquitectura de Computadors, Apto "Cum laude", 1997. Premio UPC a la mejor tesis informática del curso 1996-97.*

- *Josep Llosa Espuny, "Reducing the Impact of Register Pressure on Software Pipelining", Universitat Politècnica de Catalunya, Departament d'Arquitectura de Computadors, Apto "Cum laude", 1996. Premio UPC a la mejor tesis informática del curso 1995-96.*

- *Montse Peiron Guardia, "Optimització del Rendiment del Sistema de Memòria en Multiprocessadors Vectorials", Universitat Politècnica de Catalunya, Departament d'Arquitectura de Computadors, Apto "Cum laude", 1996.*

- *Fernando Núñez Mendoza, "On Mapping Selected Graphs Problems onto VLSI Array Processors", Universitat Politècnica de Catalunya, Facultad de Informática de Barcelona, Apto "Cum laude", 1988.*

- *Jesús Labarta Mancho, "Reducción de Interferencias en Sistemas Multiprocesadores", Universitat Politècnica de Catalunya, E.T.S.I. Telecomunicación de Barcelona, Apto "Cum laude", 1983.*

- *José Mª. Llabería Griñó, "Estudio de Redes de Interconexión con Multiplexación Adaptativa para Sistemas Multiprocesador", Universitat Politècnica de Catalunya, E.T.S.I. Telecomunicación de Barcelona, Apto "Cum laude", 1983.*

- *Enrique Herrada Lillo, "Contribución al Diseño y Evaluación de Redes de Interconexión para Multiprocesadores" Universitat Politècnica de Catalunya, E.T.S.I. Telecomunicació de Barcelona, Apto "Cum laude", 1983.*